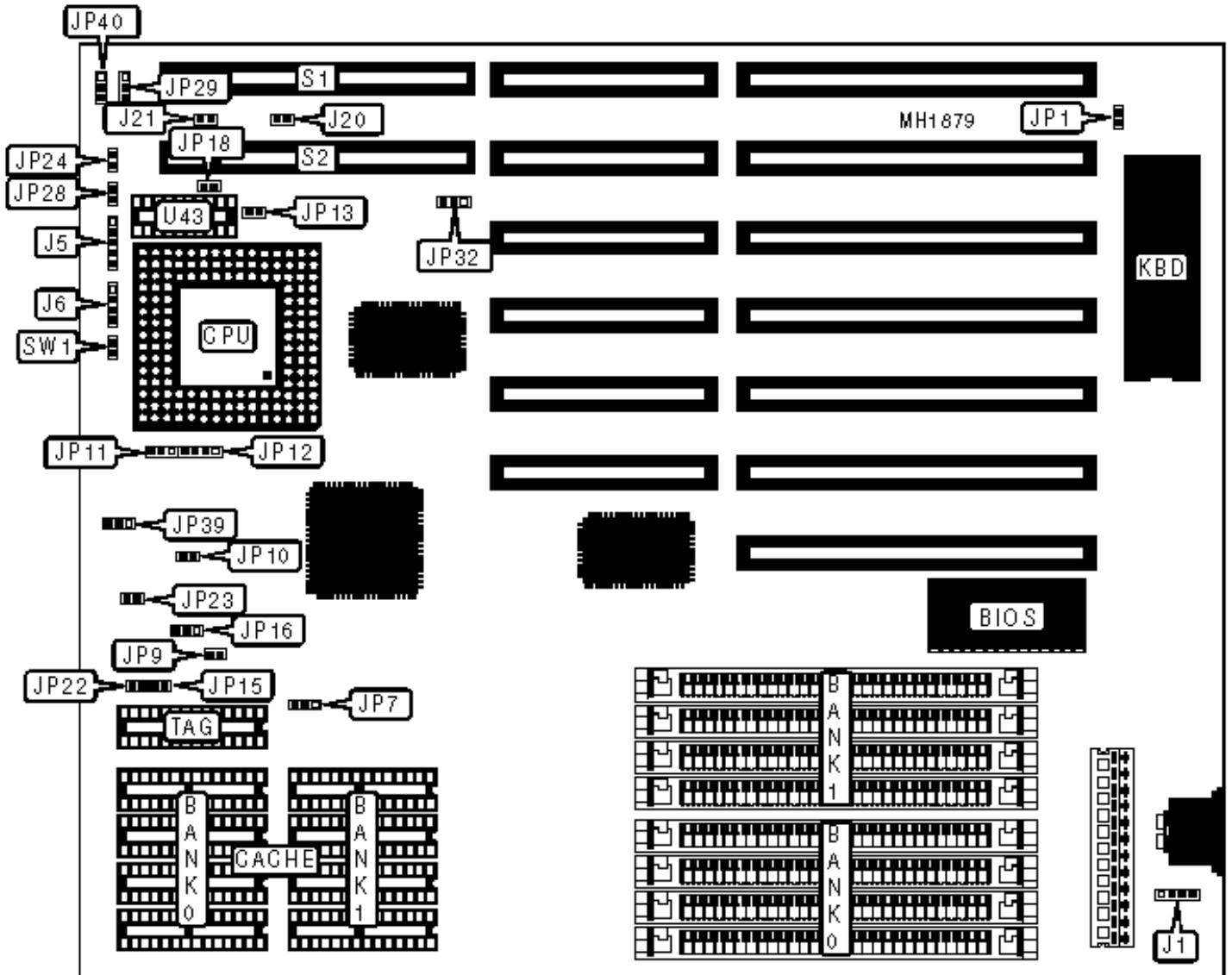


UNIDENTIFIED

486-VL BUS ANTI-VIRUS

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	JP28
Power LED & keylock	J5	32-bit VESA Local bus slot	S1
Speaker	J6	32-bit VESA Local bus slot	S2
Turbo LED	JP24	Reset switch	SW1

USER CONFIGURABLE SETTINGS

Function	Jumper	Position
» Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
» Factory configured - do not alter	JP32	pins 2 & 3 closed
» ADS # signal delay enabled	JP40	pins 2 & 3 closed
ADS # signal delay disabled	JP40	pins 1 & 2 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 8/(1) 32K x 8

Note: The exact locations of Bank 0 and Bank 1 are unknown.

CACHE JUMPER CONFIGURATION

Size	JP7	JP9	JP15	JP22
64KB	pins 2 & 3 closed	Open	Open	Open
128KB	pins 1 & 2 closed	Closed	Closed	Open
256KB	pins 2 & 3 closed	Closed	Closed	Closed

Note: The exact locations of Bank 0 and Bank 1 are unknown.

CPU TYPE CONFIGURATION

Type	JP11	JP12
80486SX	Open	pins 2 & 3 closed
80487SX	pins 2 & 3 closed	pins 1 & 2 and 3 & 4 closed
80486DX	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed
80486DX2	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed
Pentium Overdrive	pins 2 & 3 closed	pins 1 & 2 and 3 & 4 closed

CPU SPEED CONFIGURATION

Speed	JP10	JP16	JP23
25MHz	Open	pins 2 & 3 closed	Closed
33MHz	Closed	pins 2 & 3 closed	Open

40MHz	Open	pins 2 & 3 closed	Open
50i MHz	Open	pins 2 & 3 closed	Closed
50MHz	Open	pins 1 & 2 closed	Closed
66i MHz	Closed	pins 2 & 3 closed	Open

VESA WAIT STATE/BUS SPEED CONFIGURATION

CPU speed	Wait states	J20	J21
< 33MHz	0 wait states	Closed	Closed
> 33MHz	1 wait state	Open	Open

VL-BUS MASTER/SLAVE CONFIGURATION

Setting	JP13	JP18
» Slave mode	Closed	Open
Master & slave	Closed	Open

Note:For VL-Bus device to support both master and slave mode, an additional PAL must be inserted in U43

VL-BUS SETUP MODE CONFIGURATION

Setting	JP29	JP39
» Setup 0	pins 1 & 2 closed	pins 1 & 2 closed
Setup 1	pins 1 & 2 closed	pins 2 & 3 closed
Setup 2	pins 2 & 3 closed	pins 2 & 3 closed