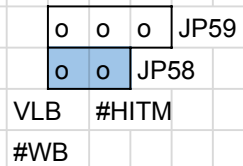


ABIT AB-PW4 extra cache jumpers to enable write-back cache on AM5x86

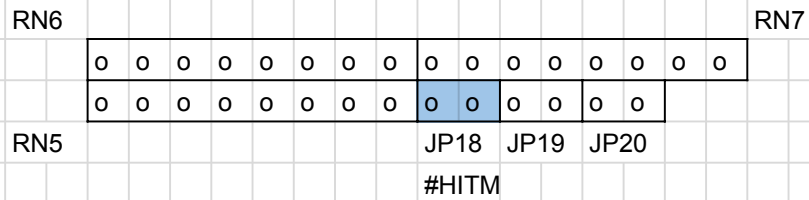
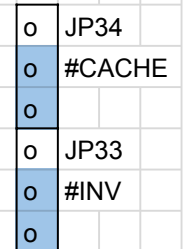
V1.2

NOTE: Requires modified TRW BIOS to work properly!

<https://theretroweb.com/motherboards/s/abit-ab-pw4#bios>



CPU SOCKET



NOTE: JP33 and JP34 headers must be added manually

Reverse-engineered by Eric Voirin (oerg866)

