



MANUAIS DE MOTHERBOARDS JBOND

PCI500C-N



- Socket 7
- VIA Apollo VP3 (VT82C597AT) chipset.
- Winbond W83877F Multi I/O chip.
- Dimensions: 8.7x9.1 inches 2/3 Baby AT form.
- Award PnP PCI flash BIOS.
- 512K bytes L2 SRAM cache.
- Three 168-pin DIMM sockets.
- Two Enhance IDE sockets (up to four IDE devices) support fast ATA-2, ATAPI, and Ultra DMA/33 functions.
- One Floppy socket supports two floppy drivers with 360K, 720K, 1.22M, 1.44M, and 2.88M bytes.
- One AGP slot supports x1 and x2 AGP card.
- Three PCI slots. (PCI spec. V2.1)
- Three ISA slots. (1 PCI/ISA shared slot)
- PS/2 keyboard and PS/2 mouse connectors on board.
- Two Serial Port sockets.
- One Parallel Port socket supports SPP, EPP, and ECP.
- Two USB Port connectors on board.
- One FIR (Fast IrDA) Port connector on board (transfer rate up to 4MB/s).

1. CPU Jumper Settings

JP1		PIN 3	PIN 2	PIN 1	
JP2	JCK 1	PIN 1 toward AGP slot	PIN 1	PIN 2	PIN 3
	JCK 2	PIN 1 toward AGP slot	PIN 1	PIN 2	PIN 3
	JCK 3	PIN 1 toward AGP slot	PIN 1	PIN 2	PIN 3
JPW2		PIN 4	PIN 3	PIN 2	PIN 1

Intel	JP1	JP1	JP1	JP2	JP2	JP2	JPW2	JPW2	JPW2	JPW2
w/o MMX technology	PIN 1	PIN 2	PIN 3	JCK 1	JCK 2	JCK 3	PIN 1	PIN 2	PIN 3	PIN 4
75MHz	NC	NC	NC	2-3	2-3	2-3	ON	NC	ON	ON
90MHz	NC	NC	NC	2-3	1-2	1-2	ON	NC	ON	ON
100MHz	NC	NC	NC	1-2	1-2	1-2	ON	NC	ON	ON
120MHz	ON	NC	NC	2-3	1-2	1-2	ON	NC	ON	ON
133MHz	ON	NC	NC	2-3	1-2	1-2	ON	NC	ON	ON
150MHz	ON	ON	NC	2-3	1-2	1-2	ON	NC	ON	ON
166MHz	ON	ON	NC	1-2	1-2	1-2	ON	NC	ON	ON
180MHz	NC	ON	NC	2-3	1-2	1-2	ON	NC	ON	ON
200MHz	NC	ON	NC	1-2	1-2	1-2	ON	NC	ON	ON
Intel	JP1	JP1	JP1	JP2	JP2	JP2	JPW2	JPW2	JPW2	JPW2
w/ MMX technology	PIN 1	PIN 2	PIN 3	JCK 1	JCK 2	JCK 3	PIN 1	PIN 2	PIN 3	PIN 4
150MHz	ON	ON	NC	2-3	1-2	1-2	NC	NC	NC	ON
166MHz	ON	ON	NC	1-2	1-2	1-2	NC	NC	NC	ON
180MHz	NC	ON	NC	2-3	1-2	1-2	NC	NC	NC	ON
200MHz	NC	ON	NC	1-2	1-2	1-2	NC	NC	NC	ON
233MHz	NC	NC	NC	1-2	1-2	1-2	NC	NC	NC	ON
266MHz	ON	NC	ON	1-2	1-2	1-2	NC	NC	NC	ON
300MHz	ON	ON	ON	1-2	1-2	1-2	NC	NC	NC	ON
AMD	JP1	JP1	JP1	JP2	JP2	JP2	JPW2	JPW2	JPW2	JPW2
K5	PIN 1	PIN 2	PIN 3	JCK 1	JCK 2	JCK 3	PIN 1	PIN 2	PIN 3	PIN 4
K5-PR75	NC	NC	NC	2-3	2-3	2-3	ON	ON	ON	ON
K5-PR90	NC	NC	NC	2-3	1-2	1-2	ON	ON	ON	ON
K5-PR100	NC	NC	NC	1-2	1-2	1-2	ON	ON	ON	ON
K5-PR120	ON	NC	NC	2-3	1-2	1-2	ON	ON	ON	ON
K5-PR133	ON	NC	NC	1-2	1-2	1-2	ON	ON	ON	ON
K5-PR166	ON	ON	NC	1-2	1-2	1-2	ON	ON	ON	ON
AMD	JP1	JP1	JP1	JP2	JP2	JP2	JPW2	JPW2	JPW2	JPW2

K6	PIN 1	PIN 2	PIN 3	JCK 1	JCK 2	JCK 3	PIN 1	PIN 2	PIN 3	PIN 4
K6-PR166 (2.9v)	ON	ON	NC	1-2	1-2	1-2	ON	NC	NC	ON
K6-PR200 (2.9v)	NC	ON	NC	1-2	1-2	1-2	ON	NC	NC	ON
K6-PR200 (2.2v)	NC	ON	NC	1-2	1-2	1-2	ON	NC	NC	NC
K6-PR233 (3.2v)	NC	NC	NC	1-2	1-2	1-2	NC	NC	ON	ON
K6-PR233 (2.2v)	NC	NC	NC	1-2	1-2	1-2	ON	NC	NC	NC
K6-PR266 (2.2v)	ON	NC	ON	1-2	1-2	1-2	ON	NC	NC	NC
K6-PR300 (2.2v)	ON	ON	ON	1-2	1-2	1-2	ON	NC	NC	NC
Cyrix / IBM	JP1	JP1	JP1	JP2	JP2	JP2	JPW2	JPW2	JPW2	JPW2
6x86	PIN 1	PIN 2	PIN 3	JCK 1	JCK 2	JCK 3	PIN 1	PIN 2	PIN 3	PIN 4
PR120+GP (50x2)	ON	NC	NC	2-3	2-3	2-3	ON	ON	ON	ON
PR150+GP (60x2)	ON	NC	NC	2-3	1-2	1-2	ON	ON	ON	ON
PR166+GP (66X2)	ON	NC	NC	1-2	1-2	1-2	ON	ON	ON	ON
PR200+GP (75X2)	ON	NC	NC	1-2	2-3	1-2	ON	ON	ON	ON
Cyrix / IBM	JP1	JP1	JP1	JP2	JP2	JP2	JPW2	JPW2	JPW2	JPW2
6x86L	PIN 1	PIN 2	PIN 3	JCK 1	JCK 2	JCK 3	PIN 1	PIN 2	PIN 3	PIN 4
PR166+GP (66x2)	ON	NC	NC	1-2	1-2	1-2	NC	NC	NC	ON
PR200+GP (75X2)	ON	NC	NC	1-2	2-3	1-2	NC	NC	NC	ON
Cyrix / IBM	JP1	JP1	JP1	JP2	JP2	JP2	JPW2	JPW2	JPW2	JPW2
6x86MX	PIN 1	PIN 2	PIN 3	JCK 1	JCK 2	JCK 3	PIN 1	PIN 2	PIN 3	PIN 4
MX-PR166GP(66x2)	ON	NC	NC	1-2	1-2	1-2	NC	NC	NC	ON
MX-PR166GP(60X2.5)	ON	ON	NC	2-3	1-2	1-2	NC	NC	NC	ON
MX-PR200GP(75X2)	ON	NC	NC	1-2	2-3	1-2	NC	NC	NC	ON
MX-PR200GP(66x2.5)	ON	ON	NC	1-2	1-2	1-2	NC	NC	NC	ON
MX-PR233GP(75X2.5)	ON	ON	NC	1-2	2-3	1-2	NC	NC	NC	ON
MX-PR233GP(66x3)	NC	ON	NC	1-2	1-2	1-2	NC	NC	NC	ON
Cyrix / IBM	JP1	JP1	JP1	JP2	JP2	JP2	JPW2	JPW2	JPW2	JPW2
MII	PIN 1	PIN 2	PIN 3	JCK 1	JCK 2	JCK 3	PIN 1	PIN 2	PIN 3	PIN 4
MII 233 (66x3)	NC	ON	NC	1-2	1-2	1-2	ON	NC	NC	ON
MII 300 (66x3.5)	NC	NC	NC	1-2	1-2	1-2	ON	NC	NC	ON
MII 300 (75x3)	ON	NC	ON	1-2	2-3	1-2	ON	NC	NC	ON

Note:

- ON - jumper block short
- NC- jumper block open

2. Clear CMOS Data Jumper Settings

Operating Mode	JVBAT1
Normal Operating (default)	Short 1-2
Clear CMOS Data	Short 2-3 while computer power turn OFF

3. Support DIMM Module List

- Each DIMM socket supports 8M to 128M bytes DIMM module.
- Vcc provides 3.3v and 5.0v
- Support 4-clock SDRAM-II, SDRAM and EDO DIMM modules.

4. (Optional) SIMM Module Adapter

- Each SIMM socket supports 2M to 64M bytes SIMM module.
- Vcc provides 5.0v
- Support SRAM, EDO, and FPG SIMM modules.

5. Support Year 2000 Compliance

- BIOS version 1.00 or later supports Year 2000 compliance.

6. Support LS-120 Zip Driver Boot Function

7. Support SCSI/CD-ROM Function

8. Support INT 13 Expansion Mode (control above 8.4 GB IDE Hard Disk)