



# MANUAIS DE MOTHERBOARDS JBOND

PCI500C-H



- Socket 7.
- Intel 82430VX chipset.
- Winbond W83877F Multi I/O chip.
- Dimensions: 8.7x11.3 inches 2/3 Baby AT form.
- Award PnP PCI flash BIOS.
- 256K bytes L2 SRAM cache.
- One 256K / 512K SRAM module socket.
- Two 168-pin DIMM sockets.
- Two 72-pin SIMM sockets.
- Two Enchance IDE sockets (up to four IDE devices) support fast ATA-2 and ATAPI functions.
- One Floppy socket supports two floppy drivers with 360K, 720K, 1.22M, 1.44M, and 2.88M bytes.
- Four PCI slots. (PCI spec. V2.1)
- Four ISA slots. (1 PCI/ISA shared slot)
- PS/2 keyboard and PS/2 mouse connectors on board.
- Two Serial Port sockets.
- One Parallel Port socket supports SPP, EPP, and ECP.
- Two USB Port connectors on board.
- One FIR (Fast IrDA) Port connector on board (transfer rate up to 4MB/s).
- (Optional) Adaptec AIC-7850 Ultra SCSI controller.

## 1. CPU Jumper Settings

<b>Intel w/o MMX technology</b>	<b>JP4</b>	<b>JP5</b>	<b>JP7</b>	<b>JP8</b>	<b>JP9</b>	<b>JP25</b>	<b>JP26</b>	<b>JP27</b>	<b>JP28</b>	<b>JP29</b>	<b>JP33</b>
75MHz	NC	NC	NC	ON	ON	NC	NC	NC	NC	NC	NC
90MHz	NC	NC	NC	ON	NC	NC	NC	NC	NC	NC	NC
100MHz	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
120MHz	ON	NC	NC	ON	NC	NC	NC	NC	NC	NC	NC
133MHz	ON	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
150MHz	ON	ON	NC	ON	NC	NC	NC	NC	NC	NC	NC
166MHz	ON	ON	NC	NC	NC	NC	NC	NC	NC	NC	NC
180MHz	NC	ON	NC	ON	NC	NC	NC	NC	NC	NC	NC
200MHz	NC	ON	NC	NC	NC	NC	NC	NC	NC	NC	NC
<b>Intel w/ MMX technology</b>	<b>JP4</b>	<b>JP5</b>	<b>JP7</b>	<b>JP8</b>	<b>JP9</b>	<b>JP25</b>	<b>JP26</b>	<b>JP27</b>	<b>JP28</b>	<b>JP29</b>	<b>JP33</b>
150MHz	ON	ON	NC	ON	NC	NC	NC	ON	ON	ON	NC
166MHz	ON	ON	NC	NC	NC	NC	NC	ON	ON	ON	NC
180MHz	NC	ON	NC	ON	NC	NC	NC	ON	ON	ON	NC
200MHz	NC	ON	NC	NC	NC	NC	NC	ON	ON	ON	NC
233MHz	NC	NC	NC	NC	NC	NC	NC	ON	ON	ON	NC
<b>AMD K5</b>	<b>JP4</b>	<b>JP5</b>	<b>JP7</b>	<b>JP8</b>	<b>JP9</b>	<b>JP25</b>	<b>JP26</b>	<b>JP27</b>	<b>JP28</b>	<b>JP29</b>	<b>JP33</b>
K5-PR75	NC	NC	NC	ON	ON	NC	NC	NC	NC	NC	NC
K5-PR90	NC	NC	NC	ON	NC	NC	NC	NC	NC	NC	NC
K5-PR100	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
K5-PR120	ON	NC	NC	ON	NC	NC	NC	NC	NC	NC	NC
K5-PR133	ON	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
K5-PR166	ON	ON	NC	NC	NC	NC	NC	NC	NC	NC	NC
<b>AMD K6</b>	<b>JP4</b>	<b>JP5</b>	<b>JP7</b>	<b>JP8</b>	<b>JP9</b>	<b>JP25</b>	<b>JP26</b>	<b>JP27</b>	<b>JP28</b>	<b>JP29</b>	<b>JP33</b>
K6-PR166 (2.9v)	ON	ON	NC	NC	NC	NC	NC	ON	ON	NC	NC
K6-PR200 (2.9v)	NC	ON	NC	NC	NC	NC	NC	ON	ON	NC	NC
<b>Cyrix / IBM</b>	<b>JP4</b>	<b>JP5</b>	<b>JP7</b>	<b>JP8</b>	<b>JP9</b>	<b>JP25</b>	<b>JP26</b>	<b>JP27</b>	<b>JP28</b>	<b>JP29</b>	<b>JP33</b>

<b>6x86</b>											
PR120+GP(50x2)	ON	NC	NC	ON	ON	NC	NC	NC	NC	NC	NC
PR133+GP(55x2)	ON	NC	NC	NC	ON	NC	NC	NC	NC	NC	NC
PR150+GP(60x2)	ON	NC	NC	ON	NC	NC	NC	NC	NC	NC	NC
PR166+GP(66x2)	ON	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
<b>Cyrix / IBM</b>	<b>JP4</b>	<b>JP5</b>	<b>JP7</b>	<b>JP8</b>	<b>JP9</b>	<b>JP25</b>	<b>JP26</b>	<b>JP27</b>	<b>JP28</b>	<b>JP29</b>	<b>JP33</b>
<b>6x86L</b>											
PR166+GP(66x2)	ON	NC	NC	NC	NC	NC	NC	ON	ON	ON	NC
<b>Cyrix / IBM</b>	<b>JP4</b>	<b>JP5</b>	<b>JP7</b>	<b>JP8</b>	<b>JP9</b>	<b>JP25</b>	<b>JP26</b>	<b>JP27</b>	<b>JP28</b>	<b>JP29</b>	<b>JP33</b>
<b>6x86 MX</b>											
MX-PR166GP(66x2)	ON	NC	NC	NC	NC	NC	NC	ON	ON	ON	NC
MX-PR166GP(60x2.5)	ON	ON	NC	ON	NC	NC	NC	ON	ON	ON	NC
MX-PR200GP(66x2.5)	ON	ON	NC	NC	NC	NC	NC	ON	ON	ON	NC
MX-PR233GP(66x3)	NC	ON	NC	NC	NC	NC	NC	ON	ON	ON	NC
<b>Cyrix / IBM</b>	<b>JP4</b>	<b>JP5</b>	<b>JP7</b>	<b>JP8</b>	<b>JP9</b>	<b>JP25</b>	<b>JP26</b>	<b>JP27</b>	<b>JP28</b>	<b>JP29</b>	<b>JP33</b>
<b>MII</b>											
MII 233(66x3.5)	NC	NC	NC	NC	NC	NC	NC	ON	ON	NC	NC

**Note:**

- ON - jumper block short
- NC- jumper block open

**2. Clear CMOS Data Jumper Settings**

<b>Operating Mode</b>	<b>JP3</b>
Normal Operating (default)	Short 1-2
Clear CMOS Data	Short 3-4 while computer power turn <b>OFF</b>

**3. External Cache Memory Jumper Settings**

<b>Operating Mode</b>	<b>JP17</b>
w/ 256K embedded SRAM chip9s), w/o add-on SRAM module	Short 2-3
w/ 256K embedded SRAM chip9s), and 256K add-on SRAM module	Short 1-2
w/o 256K embedded SRAM chip9s), w/ 256K add-on SRAM module	Don't care

**4. Support DIMM Module List**

- Each DIMM socket supports 4M to 32M bytes DIMM module.
- Vcc provides 3.3v and 5.0v
- Support 2-clock only SDRAM and EDO DIMM modules.

**5. Support SIMM Module List**

- Each SIMM socket supports 1M to 32M bytes SIMM module.
- Vcc provides 5.0v
- Support SRAM, EDO, and FPG SIMM modules.

**6. Support Year 2000 Compliance**

- BIOS version 1.40A or later supports Year 2000 compliance.

**7. Support LS-120 Zip Driver Boot Function**

**8. Support SCSI/CD-ROM Function**

**9. Support Ultra DMA/33 Function?**

- Intel 82430VX chipset **does not** support Ultra DMA/33 function.