MiTAC PB5500C-256K, Rev 1E (AT&T OEM) Board

Connectors and jumper-settings. These are based on what's witten on the PCB itself, as I don't have the manual. Stars indicate selection on my board, which I assume may be default settings for a 60MHz Pentium.

Connectors:

	J1	Keyboard
	J2	External Battery
	J13	Speaker
	J14	Keylock
	J15	Reset
	BANK1	2x 72-pin SIMM
	BANK2	2x 72-pin SIMM
	PC1	PCI
	PC2	PCI
	PC3	PCI
	PC4	PCI

CPU:

		JP17	JP18	JP19
	33MHz	Closed	Closed	Closed
	40MHz	Closed	Closed	Open
	50MHz	Open	Open	Closed
*	60MHz	Open	Closed	Open
	66MHz	Closed	Open	Closed

Cache:

		JP1	JP2	JP15
*	Bank 0	1-2	1-2	1-2
	Bank 0 + Bank 1	2-3	2-3	2-3

		JP9	JP10	JP11	JP12
	8Kx8	Open	Open	Open	Open
	8Kx8 + 8Kx8	Closed	Open	Open	Open
*	32Kx8	Closed	Closed	Open	Open
	32Kx8 + 32Kx8	Closed	Closed	Closed	Open
	128Kx8	Closed	Closed	Closed	Closed

Bus:

		JP5
	Back to Back IO Delay Disabled	Open
*	Back to Back IO Delay Enabled	Closed

		JP6	JP7
	ATCLK = LCLK/2	Open	Open
	ATCLK = LCLK/3	Open	Closed
*	ATCLK = LCLK/4	Closed	Open
	ATCLK = LCLK/5	Closed	Closed

		JP20	JP23
*	External LCLK source	1-2	Closed
	Internal LCLK source	2-3	Open

		JP21
	LCLK W/ F08	1-2
*	Direct Drive LCLK	2-3

		JP3
*	VESA Local Bus Enabled	Open
	VESA Local Bus Disabled	Closed

		JP4
*	82C822 LDEV# Sample at end of 1st T2	Open
	82C822 LDEV# Sample at end of 2nd T2	Closed

		JP22
*	MDHODE# Inactive at end of last T2	Open
	MDHODE# Inactive at beginning of last T2	Closed

		JP26
*	VESA Local Bus ID0 = 0	Closed
	VESA Local Bus ID0 = 1	Open

		JP27
	VESA Local Bus ID1 = 0	Closed
*	VESA Local Bus ID1 = 1	Open

		JP28
*	VESA Local Bus ID2 = 0	Closed
	VESA Local Bus ID2 = 1	Open

		JP29
	VESA Local Bus ID3 = 0	Closed
*	VESA Local Bus ID3 = 1	Open

		JP30
	VESA Local Bus ID4 = 0	Closed
*	VESA Local Bus ID4 = 1	Open

Misc:

		JP13
	Erase CMOS Settings	1-2
*	Normal, keep CMOS Settings	2-3

		JP24
	+12V on EPROM VPP	1-2
*	+5V on EPROM VPP	2-3

		JP25
*	Reserved (1 on KBD-Ctrl P16)	Open
	Reserved (0 on KBD-Ctrl P16)	Closed