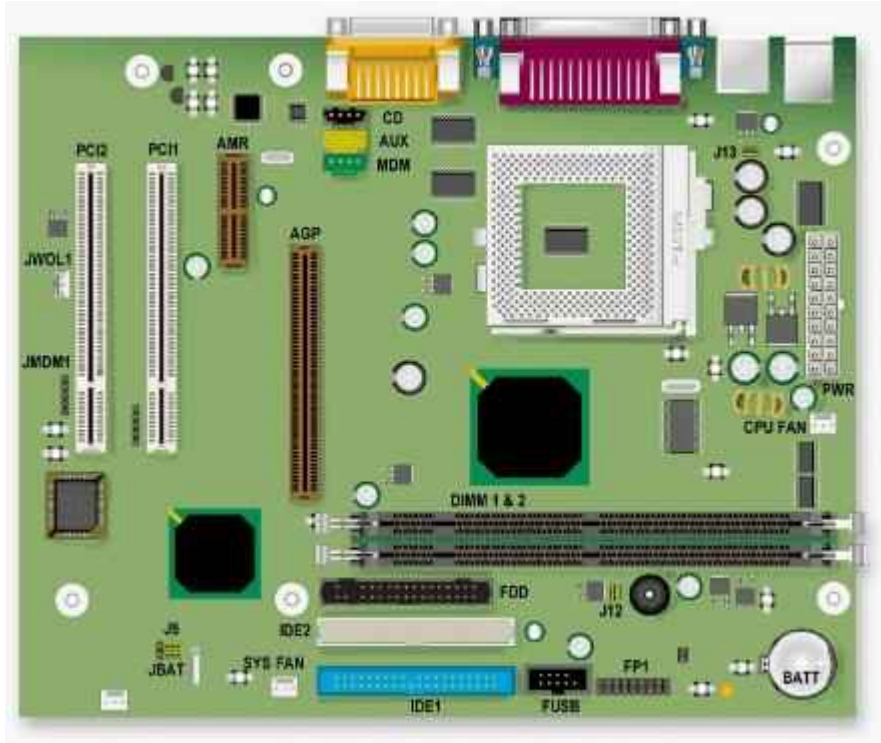# Packard Bell Luna M/Board

Specification

- **Audio:** VT1611A 18-audio - audio codec
- **Bios:** AMI
- **Chipset :** VIA Apollo Pro133A (VT82C694X system controller, VT82C686A PCI to ISA bridge)
- **CPU Support :** Socket 370
  - Supports Intel PIII Coppermine FC-PGA (Flip-Chip Pin Grid Array) processor 600/100 - 850/100 Mhz.
  - Supports Intel PIII Coppermine FC-PGA (Flip-Chip Pin Grid Array) processor 600/133 - 1000/133 MHz
  - Note: above CPU's are only supported with the latest BIOS version.
  - 66/100/133 MHz Host bus speed processors are supported.
- **Expansion Slots :** 2 PCI extension slots, 1 AGP connector, 1 Audio Modem Riser connector
- **Form Factor :** ATX 24.4cm. x 20.0 cm Form Factor
- **RAM :**
  - Supports 4 memory banks (2 slots) using three 168-pin unbuffered DIMM.
  - Supports single or double sided DIMMS with gold-plated contacts at a clock speed of 66, 100 or 133 MHz.

CPU Bus Frequency	Memory Frequency Supported
66 MHz	66, 100 or 133 MHz
100 MHz	66, 100 or 133 MHz
133 MHz	100 or 133 MHz

- Supports a maximum memory size of: 512 (128-bit technology) or 256 MB (64-bit technology) registered DIMMs or 1 GB unregistered DIMMS (256-bit technology)
- Supports 3.3 V SDRAM DIMMs.
- ECC memory checking is supported

Jumpers

Jumper	Configuration
JBAT1 - Clear CMOS jumper	**1-2**: Normal operation (default) **2-3**: Clear current BIOS default settings
J5 - BIOS mode selection jumper	**1-2**: CMOS and NVRAM are only cleared if they failed the checksum test **2-3**: BIOS Setup is put in extended configuration mode (a maintenance menu is available, in which you can select the Clear Password option). **Open**: BIOS recovery from the floppy
J12 - USB devices 1&2 power on function from S3 states jumper	**Open**: disable **Close**: enable
J13 - USB devices 3&4 power on function from S3 states jumper	**Open**: Disable **Close**: Enable