

Personal Computer

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# March 1986

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# NCR PERSONAL COMPUTER PC 41

# SERVICE MANUAL

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System Overview

#### NCR PERSONAL COMPUTER HARDWARE

The NCR Personal Computer consists of a main Display Unit and a Keyboard. The computer is available in different models, and has a variety of options that can be added to each model. Expansion boards from non-NCR ("third party") manufacturers can be used with the NCR Personal Compter, as well as expansion boards offered by NCR.

#### DISPLAY UNIT

The standard Display Unit consists of the following parts:

- o Cabinet
- o Power Supply
- o 1 or 2 flexible disk drive(s) or one flexible disk drive and one hard disk drive
- 12-inch monochrome CRT display or 12-inch color CRT display
- o Processor Board with:
  - 8088 Microprocessor
  - o 256 KB Random Access Memory (RAM)
  - o 16K Read Only Memory (ROM)
  - o RS-232-C Serial Interface
  - o Centronics Parallel Interface (for printer)

#### SYSTEM OVERVIEW

- Plug-in board option expansion slots
- Fixed disk drive controller plug-in board if the unit includes a fixed disk.
- Video controller plug-in board.

A block diagram of the NCR Personal Computer Hardware is shown in Figure 1-1.



Figure 1-1 Hardware Block Diagram

The Main Processor Board and any option boards are

#### SYSTEM OVERVIEW

accommodated in an easily removable Card Rack Assembly, this assembly allows for a maximum of 7 option boards to be added to the system. The shape of the Card Rack Assembly imposes the following restrictions on the size of the option boards that may be installed:

The option boards must conform to the following dimensions:

- 3 Boards 334 mm x 107 mm (13.1 in. x 4.2 in.)
- 3 Boards 296 mm x 107 mm (11.6 in. x 4.2 in.)
- 1 Board 254 mm x 107 mm (10.0 in. x 4.2 in.)

#### MODELS

The NCR Personal Computer is available in various models. The standard internal RAM memory size for all models is 256KB. The models currently available are:

- A 12-inch monochrome display, 1 flexible disk drive and 256KB memory
- A 12-inch monochrome display, 2 flexible disk drives and 256KB memory
- A 12-inch monochrome display, 1 flexible disk drive and 1 fixed disk drive, and a 256KB memory
- A 12-inch color display, 1 flexible disk drive and 256KB memory
- A 12-inch color display with 2 flexible disk drives and 256KB memory
- A 12-inch color display with 1 flexible disk drive and a fixed disk drive, and 256KB memory
- A 12-inch color display with Videotex , 1 flexible disk drive and 256KB memory
- o A 12-inch color display with Videotex , 2

1-3

flexible disk drives and 256 KB memory

 A 12-inch color display with Videotex, 1 flexible disk drive and 1 fixed disk drive, and 256KB memory

The Videotex systems are dual purpose systems, to be used either as a Videotex system, or as a normal personal computer. These systems are tailored to meet individual country requirements, for example Bildschirmtext for Germany, Teletel for France, and Prestel for Great Britain.

#### WARNING

Disconnect the power cable before removing the cabinet. Dangerous voltages are present when working on the computer with the cabinet removed.

ONLY TRAINED ENGINEERS AND NCR TRAINED SER-VICE PERSONNEL SHOULD ATTEMPT TO WORK ON THE COMPUTER WITH THE CABINET REMOVED AND THE POWER ON.

It is recommended that the first operation after removing the cabinet should be to discharge CRT anode voltage.

#### CAUTION

Electrostatic discharge can cause damage to some components used in the computer. Where possible, work in a static free area and use special wrist-ground straps. Avoid touching the components and circuit paths, handle printed circuit boards by their edges. The construction technique (multi-layer) used on some PC boards makes the soldering components extremely difficult, therefore such work should only be undertaken by a field engineer who is experienced in repairing PCBs, and using the special facilities that are available in NCR workshops.

#### MODEL NUMBERS

The model number of the NCR PC 41 is shown on the plate at the back of the unit. The meaning of the model number is shown below;



The NCR model numbers are defined in Figure 1-2.

SYSTEM OVERVIEW

| Class                 | 3277   | NCR Class Number   |
|-----------------------|--|--|
| Major Model           | 11<br>12<br>13   | Monochrome CRT<br>Color CRT<br>Videotext CRT (Color)   |
| Sub Model             | 01<br>02<br>03   | Single Flexible Disk Drive<br>Dual Flexible Disk Drives<br>Fixed and Flexible Disk Drives  |
| Voltage/<br>Frequency | 71<br>73   | 120 Volts, 50/60 Hz. *)<br>220/230/240 Volts, 50/60 Hz.  |
| Language              | 00<br>01<br>02<br>03<br>05<br>06<br>09<br>10<br>11<br>12<br>20 | U.S. English<br>International English<br>U.K. English<br>Spanish<br>French<br>German<br>Swedish<br>Danish<br>Norwegian<br>Italian<br>Finnish |

Figure 1-2 Model Number Definition

\*) The monochrome version is strapped and can be used in the voltage range of 120/220 to 240 V.

#### TOOLS

A normal set of hand tool for an electronic engineer is sufficient to service the computer. You also need an NCR Alignment Disk.

o For 48 TPI Drives - part no. 603-9009167

To enable the execution of the Level O Loop Test the following test adapters are available:

o Turnaround Keyboard Test Adapter 017-0044311

o Turnaround Parallel/Serial Test Adapter 017-0044320

To allow access to the Card Rack Assembly with power applied, an extension cable can be made locally. Figure 1-3 provides the information needed to make the cable.

Parts Required:

Receptacle Connector (Female)007-2007711Receptacle Connector (Male)007-2007712Cable 15 Wire, 18 AWG,Local PurchaseLength approx. 700mmLocal Purchase



Figure 1-3 Extension Cable

#### SYSTEM OVERVIEW

## ALTERNATE MAIN PROCESSOR BOARD

Since the introduction of the NCR Personal Computer PC4i a new style Main Processor Board, known as "Alternate" Main Processor Board (in this manual sometimes referred to as "Generic" Main Processor Board) has been introduced. Much of the information in this manual applies both to the "Alternate" Main Processor Board and to the two Early-type Main Processor Boards. The "Alternate" MPB is used in a variety of NCR computers. Distinguishing characteristics of this board are its 8 slots, 2 switch packs, VLSI ICs, RAM location and RAM accessibility. Computers with such a mainboard installed are marked with a "G" (which stands for "Generic") on the PC board rack.

#### Basic Troubleshooting

This chapter is intended to help the engineer in either repairing the system, or bringing it to a partially operative state so that the diagnostic diskette may be used for further troubleshooting.

Presented here is a structured sequence of operations to follow when either the system is unable to run diagnostics, or the level 0 (power-on) diagnostics fail (see Fig. 2-1).

Generally faulty systems can be classified into two types:

- Non-operative a system where it is not possible to load a diagnostic diskette.
- Partially operative a system with sufficient working components to allow the loading of a diagnostic diskette.

### LEVEL C (POWER-ON) DIAGNOSTICS

The level 0 diagnostics are performed automatically each time the system is switched on.

These diagnostics check the following:

- RAM memory (for memory size setting refer to Chapter 8 "Strapping Information")
- 8088 CPU (CPU is used to test all the modules)
- o 27182 EPROM (containing BIOS)
- 765 Flex Disk Controller

#### BASIC TROUBLESHOOTING

- o 8237A DMA controller
- o 8253 Timer
- o 8259A Programmable Interrupt Controller
- o Keyboard Interface

#### STEP ONE

#### POWER ON

#### **Power Cords and Switches**

- Ensure that the power cord is plugged into the back of the computer
- Ensure that the power cord is plugged into a properly grounded wall outlet. (Use a lamp that you know is in working order, and check that the outlet has power.)
- Ensure that the power cords for any currentlyattached external devices (printer, etc.) that have separate power cords are plugged into properly grounded wall outlets.
- 4. Insert the Service Diagnostic diskette into drive A. Use a working copy of the original diskette. Make a back-up copy on another computer, if necessary, but do not use a diskette that has no backup. The possibility exists that a problem could result in the loss of the software.
- Turn ON any attached external devices that have separate power cords and switches (unless the device installation instructions specified that the device be turned on after the computer).
- Turn ON the computer power. Turn the volume control and brightness to maximum.

 Turn ON any currently-attached external devices that were specified to be turned on after the processing unit.

#### Memory Test Messages

- In a properly operating system the turn-on procedure results in the display of Memory Test on the screen; quickly followed by steps through the increments of memory installed. An audible tone or beep is followed by the display of some introductory messages about the software. Then the prompt A> (or other) appears along with a blinking cursor. The following few steps treat anomalies of that sequence.
  - a. If the screen does not display a message, the tube may not have been warmed up in time. Wait approx. 30 seconds for the tube to warm up. Then turn the power OFF and ON for a retry.



Figure 2-1 Troubleshooting Flowchart

b. If there is no diaplay after the tube is warm, a display failure, a power supply failure, or main processor failure is suspected. Proceed to repair or replace as appropriate.

Make a check of the power supply voltage before disassembling the unit. The MPB Parallel Port Error Codes can be checked with a turnaround test adapter (refer to "Tools" in Chapter 1).

If display failure is indicated, then review and observe the instructions for installation of the display (refer to Chapter 6 "Removal/Replacement").

- The procedure of steps a and b should result in a display Memory Test. The installed amount of memory should be indicated by steps in increments of 64KB each.
- 3. If the correct amount of memory is not indicated, turn the power OFF and check the switch settings (see Chapter 8 "Strapping Information") for the correct positions.
- Use the Service Diagnostics.

#### NOTE

The Power-ON diagnostics routines issue a set of codes (see Table 3-1 Error Codes, Chapter 3) to the parallel port which are applicable to the MPB. By plugging the test adapter (see "Tools" Chapter 1) or an oscilloscope to this port, these codes can be observed. This is of particular value if the video display is not working.

- 5. If Step 2 indicated that the correct amount of memory has been tested, a tone or beep is heard. The test procedure is cycling through the complete number of increments. A successful test is indicated by a screen message.
  - a.) If no beep is heard, proceed to step 6.
  - b.) If a Keyboard Error message is displayed, go to Check for Keyboard, Step 1.

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- c.) If a **Memory Error** is displayed, go to check for Diagnostics Errors, Step 1.
- 6. If this was the first attempt to power up, go to "After First Attempt", Step 1. If this was the second attempt to power up, go to "After Second Attempt", Step 1.

If this procedure fails to locate the problem after the third attempt, then disassemble the computer and check the power supply (see Chapter 6 "Removal/Replacement"). If there is no problem with the power supply, then the MPB is faulty. Replace MPB.

#### After First Attempt

- Turn OFF the power switches for any external devices (printer etc.).
- 2. Turn OFF the computer power switch.
- 3. Unplug the computer power cord, and any external device power cord, from the wall outlet.
- Unplug the power cord from the back of the computer.
- Remove any adapter board that interferes with access to the MPB configuration switch blocks SW1 and SWA. Refer to the instructions for installation and removal.
- Check all configuration switch settings (refer to Chapter 8 "Strapping Information")
- 7. Check the attachment and condition of all cables.
- Check the AC power source. (Use a lamp that you know is in working order, and check that the outlet has power).

- 9. Check the AC slow-blow power fuse, located in the power supply. For the available versions the following fuses have been inserted: Monochrome: 3 A Color: 6.3 A Videotex: 4 A
- Ensure that the adapter boards edge connectors are correctly seated in the MPB board connector slots.
- Turn the display brightness and the computer volume control knobs fully clockwise to their maximum settings.
- 12. Replace any adapter board removed.
- 13. Turn on the computer again.

#### After Second Attempt

- 1. Turn OFF power and unplug power cords.
- Take out all expansion adapter boards for any features which had been added to the original basic machine.

Refer to Chapter 6 "Removal/Replacement" for instructions on removing the cabinet from the computer and on removing each pertinent adapter board.

The adapter boards that remain will be:

- The monochrome or color/graphics display adapter board.
- b. The fixed disk drive controller board, if the computer has a fixed disk.
- Disconnect each printer or other peripheral device which is connected to either the parallel or serial port connector on the rear of the computer.

- 4. Take out any adapter board which interferes with access to the MPB configuration switches (see Chapter 8 "Strapping Information").
- Set the configuration switches for the minimum 256 KB memory size (Refer to Chapter 8 for proper switch settings).

#### NOTE

If any device is disconnected from the MPB serial port in step above, the serial port switches (see Chapter 8) must be changed to show the port disabled.

- 6. Check all internal and external connectors that are visible and reachable for the devices that remain installed on the computer. Ensure that the connections are tight and proper.
- Replace the monochrome adapter card, if it was removed in Step 4. Follow the instructions for installing boards in Chapter 6 "Removal/Replacement".
- 8. Turn ON the computer again.

BASIC TROUBLESHOOTING

#### POWER SUPPLY

Since without a correctly functioning power supply no part of the system can function, therefore it should always be one of the first areas to be checked.

The power supply is a sub-assembly of the CRT assembly. Fig.2-2 shows the power supply connections. Three versions of the power supply are used:

o for systems with a monochrome CRT

o for systems with a color CRT

o for systems with a Videotex

All power supply components are located on the power supply PBC. Except for replacing the fuse (F1) the power supply is not considered field repairable, however, some checks should be made before replacing the power supply.

Use a meter to monitor the +5V, -5V,+12V and -12V. These may be checked at the following points: Connector J4 Pin 10 +5 Volts Pin 1 -5 Volts Pin 12 +12 Volts Pin 4 -12 Volts Pin 2 Ground

The flow-chart, Figure 2-3, and 2-4 will help to eliminate possible failures.



Figure 2-2 Power Supply Connections (Monochrome systems) (1 of 2)

|          |   |                |        |               |             |      |     | _       |     |                         |
|----------|---|----------------|--------|---------------|-------------|------|-----|---------|-----|-------------------------|
|          |   |                | P      | οw            | ER          |      | S U | PI      | ΡĽ  | Y                       |
| J/P<br>3 | Γ | -              | 2<br>1 | Groi<br>+ 85' | ind<br>V DC | :    |     |         |     | Connector<br>CRT Supply |
|          |   |                |        |               |             |      |     |         |     |                         |
|          |   | -              | 14     | Groa          | ınd         |      | _   |         |     |                         |
|          |   | -              | 13     | Pow           | er G        | ood  | (0) | pe n    | )   |                         |
|          |   | -              | 12     | +12           | 1           |      |     |         |     |                         |
|          |   | -              | 11     | +51           |             |      |     |         |     |                         |
|          |   | -              | 10     | +5¥           |             |      |     |         |     | •                       |
| • • •    |   | -              | 9      | +5 %          |             |      |     |         |     | Connector               |
| J/P      |   | -              |        | +5 Y          | d           |      |     |         |     |                         |
| 4        |   | -              | 6      | Gnot          | und<br>und  |      |     |         |     | Vala                    |
|          |   | -              | 5      | Gnot          | and         |      |     |         |     | Processor               |
|          |   | -              | Д      | -12           | aniu<br>F   |      |     |         |     | Roand                   |
|          |   | -              | 2      | Grou          | ind.        |      |     |         |     | DOLLG                   |
|          |   | _              | 2      | Grou          | and         |      |     |         |     |                         |
|          |   | -              | ĩ      | -5V           |             |      |     |         |     |                         |
|          | 4 |                | •      |               |             |      |     |         |     |                         |
|          |   | -              | 4      | Grou          | pur         |      |     |         |     | i                       |
| J/P      |   | -              | 3      | +12           | V           |      |     |         |     | Connector               |
| 5        |   | -              | 2      | +5¥           |             |      |     |         |     | Disk Drives             |
|          |   | -              | 1      | Grou          | ınd         |      |     |         |     |                         |
|          |   | I              |        | _             |             |      |     |         |     | _                       |
|          |   | -              | 3      | fro           | n t         | he . | 3w1 | tch     |     | Connector               |
| J/P      |   | -              | 2      | N.C.          | •           |      |     |         |     | Power Switch            |
| 2        | Ч | -              | 1      | Lin           | 8           |      |     |         |     |                         |
|          |   |                | •      | 7 d -         | _           |      |     |         |     |                         |
| 770      |   | -              | 3      | 41 <b>0</b> 0 | Avad        |      |     | )<br>Ve | H I | Connector               |
| J/r<br>1 |   | -              | 1      | Neur          | ten1        | an   |     | =ye     | 4   | Bowen Tarot             |
| ,        | Ч | -              |        | Med           |             | -    |     |         |     | LONGL TUDAR             |
|          |   | <b>0</b> -   - |        |               |             |      |     |         |     |                         |

Figure 2-2 Power Supply Connections (Color Systems) (2 of 2)



Replace Power Supply

Figure 2-3 Troubleshooting Flow Chart PS (1 of 5)\*

 This troubleshooting flow chart applies to the Power Supply 008-0072344 (Color).



Figure 2-3 Troubleshooting Flow Chart PS [2 of 5]

# BASIC TROUBLESHOOTING

No 5V Output L Is No ->Set Loed Dutput to be broken et Load normal? to be both no-losd and overt L oad normal I Yee t Нев overvoltage Turn VR151 Hae Adjust. protective Yes counterclockoutput output circuit ->wise to full− ->voltage been started? capacity noticed? 1 L t I No No 1 I Нав L Check Check avercurrent protective Yee overcurrent. overvoltage circuit ->protective protective started? circuit circuit н I No. I. Аге **Үөв** Asplacs Iв R101 & R102 -->R101 & R102-->output burnt out? normal? L 1 No ! No L t Ъ Үев IΒ Į. R114 burnt -->output l out? R114 normal? ι I No No 1 ŝ 14l L Ł 14t IC101 to be replaced

Figure 2-3 Troubleshooting Flow Chart PS (3 of 5)

No 12V Dutput L. Is Set Losd \*: Output to be broken at Load -≻to be both no losd and normal? No normel overloed L Yee ι t. Нав Turn VR521 Has Adovervoltage Yes counterclockoutput Yes just >wise to fullprotective ---baen ---·≻outcircuit started? cepacity noticed? but. н L val~ 1 No I tage I 1 Haa L overcurrent Check Check. Yee protective >overcurrent overvol tage circuit protective protective started? circuit circuit L L No L Аге Yes Replace Ιs R201 & R202 -->R201 & R202 -----> Output normal? burnt out? 1 1 L No L L 4 1 L Iα L R214 burnt Yee Replace out? ->R214 -> Ie L Qutput I L. No No normal? I -----1 <---I 14-1 ٩ 10201 To be nepi açadı

Figure 2-3 Troubleehooting Flow Chart PS [4 Of 5]

# BASIC TROUBLESBOOTING



Figure 2-3 Troubleshuoting Flow Chert PS [5 of 5]

#### Peripherals

Remove all peripherals that may be attached to the NCR Personal Computer, and their interface PC boards. If necessary, reset the configuration switches on the Main Processor Board to match the downgraded system configuration.

The pertinent switch configurations are shown in Chapter 8 "Strapping Information".

If the Level O Diagnostics now run correctly then replace the PC boards and reconnect the peripherals one at a time. Rerun the Level O Diagnostics after replacing each PC board, and after reconnecting each peripheral to determine which part of the system is causing the trouble.

#### CHECK KEYBOARD

 Check that the keyboard cable is properly plugged into the 5-pin connector on the back of the MPB. If it is, use either of the diskette-based sets of diagnostic routines to check the keyboard.

If the keyboard is not properly plugged in, go to Step 2.

- Turn OFF the power as detailed in "After First Attempt".
- 3. Plug the keyboard cable connector into the keyboard connector on the MPB.
- 4. Turn ON the computer again.

#### CHECK FOR DIAGNOSTICS ERRORS

- Check the display screen to see whether the poweron diagnostics routines have detected and indicated the presence of any memory error.
- a. If no memory error message is displayed, but there is an interrupt failure message, go to step 2.

### BASIC TROUBLESHOOTING

- b. If any other errors are indicated, go to step 2.
- c. If the computer has been reconfigured for the purpose of troubleshooting, it is necessary to restore the machine step by step to full configuration, retesting at each incremental buildup to determine whether the addition of any specific component causes any malfunction.
- d. The computer has been reconfigured if any of the following steps were performed:
  - Configuration switch settings were changed as a result of the check in step 6 of "After the First Attempt".
  - Expansion adapter boards were removed in step 2 "After Second Attempt".
  - Parallel or serial port devices were disconnected in step 3 of After Second Attempt.
  - Configuration switches were changed to restrict the memory test to the minimum 256KB in step 5 of "After Second Attempt".
- e. If the computer was reconfigured, and has not yet been restored and retested at full configuration, go to step 1 in the Restore Full Configuration section.
- f. If the computer was not reconfigured, or has been restored step by step to full configuration and retested, go to step 3.
  - If the message "Interrupt Pailure" is displayed, a MPB failure is indicated. See "Service Diagnostics" to continue diagnosing the condition.
  - 3. If the message Error in "Expansion Memory" is displayed on the screen, hardware parity must be checked by removing the expansion memory

board testing each increment separately.

- 4. If the message Brror in Base Memory is displayed, an error in lower 128 RAM is indicated. This RAM is soldered into the main processor board; therefore, the board should be replaced to assure the problem has been corrected.
- 5. If the error message "1701" is displayed on screen an error in the fixed disk is indicated. This may be due to: fixed disk not connected, device defective, fixed disk controller defective or no power to device.
- 6. If the computer now appears to be operating properly in all respects at full configuration (the power-on diagnostics have displayed no error, the operating system is booted from the diskette, and the system prompt, A> (or other), is displayed), the procedure to recover from a nonoperative condition has been completed successfully.
- 7. If the computer is still not functioning properly, use either of the diskette-based sets of diagnostic routines to attempt identification of the problem.

#### RESTORE FULL CONFIGURATION

#### TEST TOTAL MEMORY

1. If the configuration switches indicating the amount of memory were set to the minimum 256 KB, raise the setting in minimum KB increments until the actual total amount of memory installed in this computer is indicated. Retest after each incremental setting to determine whether any problem exists with any specific bank of memory.

If the amount of memory indicated on the screen as tested is less than the total amount installed, go to step 2.

### BASIC TROUBLESHOOTING

If all memory installed in the computer has been successfully tested, go to Test CRT And Disk Drives.

- Increase the amount of memory indicated by the configuration switches by minimum increments and retest by performing the following sequence of actions:
  - a. Turn OFF the power.
  - b. Take out any adapter board which interferes with access to the MPB configuration switches.
  - c. Set the configuration switches to indicate the next higher increment of memory (see Chapter 8 for proper switch settings).
  - Replace the adapter board, if removed for access to the switches.
  - e. Turn ON the computer again.

#### TEST CRT AND DISK DRIVES

 Observe whether any characters and/or the cursor are displayed on the screen. If they are not, use either of the diskette-based sets of diagnostic routines to attempt identification of the problem.

If the cursor and/or characters (other than a diagnostic error message) are displayed, go to Step 2.

2. If a fixed disk drive unit is installed, and currently attached (it might have been temporarily disconnected for an earlier test), listen for the whirring noise that the drive makes when it is rotating. The drive should be rotating all the time the processing unit is turned on.
## BASIC TROUBLESHOOTING

If the fixed disk drive, installed and attached, is not active and running as it should be, check the fixed disk controller board switches to see if they are set correctly (see Chapter 8 for proper switch setting).

If the switch settings are correct, use either of the diskette-based sets of diagnostic routines to attempt identification of problem.

Disconnect the fixed disk if it is not operating properly. When all of the connections and switch settings are correct, proceed to step 3.

If no fixed disk drive is installed and attached, or if the drive is active and running properly, proceed to step 3.

3. Check whether a diskette was inserted in disk drive A, with the drive access lever turned to enable the drive, prior to the start of the current test pass. If so, observe whether the drive active light is on.

If the drive is loaded and active, proceed to step 5. If no diskette is in the drive, or if the drive or the drive active is not on, proceed to step 4.

- Restart the test with the disk drive properly loaded by the following steps:
  - a. Turn the computer power switch OFF
  - b. Insert a backup copy of an NCR-DOS (or other) operating system diskette into drive A. The problem being diagnosed could destroy data on a diskette, so be sure to use a back-up copy.
  - c. Turn the drive lever clockwise to enable the drive.
  - d. Turn ON the computer again.

# BASIC TROUBLESHOOTING

5. Observe whether any indication of a problem exists, e.g. an error message on the screen or some other indication of error. At the conclusion of the power-on diagnostics, the operating system should be booted into the computer and the system prompt A> (or other) should be displayed.

If any problem is visible, or if the operating system is not properly booted in, proceed to step  $\delta$  to retry the process with the backup diskette.

If everything appears to be working properly, continue the process of restoring the machine to full configuration.

6. If this step has previously been performed (that is, the backup diskette has already been used), use either of the diskette-based sets of diagnostic routines to attempt identification of the problem.

If this step has not been done yet and a backup copy of the operating system diskette (or a diskette for a different operating system) is available, do the following:

- a. Turn OFF the computer power switch.
- b. Insert the backup (or alternate) operating system diskette into Drive A.
- c. Turn the drive lever clockwise to enable the drive.
- d. Turn ON the computer again.

## REINSTALLING THE EXPANSION ADADPTER BOARDS AND PRRIPHERALS

To return the computer to full configuration step by step, reinstall one at a time the expansion adapter boards and peripherals removed or disconnected in tests. This procedure will isolate any problem which exists to one adapter board or peripheral device. The technique requires reiteration of this and other steps as many times as there are expansion adapter cards or port-connected peripherals.

If some action taken during the procedure, such as making connections secure or correcting configuration switch settings, has cleared up the problem(s) which required this effort, the steps of the Check For Diagnostics Errors section will have completed the troubleshooting procedure.

A recommended Sequence for re-installing expansion adapter cards and/or peripheral devices is:

- Memory Expander Board If it was not reinstalled in the memory test, follow the instructions in Test Total Memory for accessing the additional memory in 64 KB increments.
- 2. Flexible Disk Drive Expander Kit.
- Fixed Disk Drive adapter board and connections to the drive(s).
- 4. Printer connected to the parallel port.
- Printer or other device connected to the serial port - Note that this requires changing of the configuration switch settings to show the serial port enabled as COM1.
- Asynchronous Communications Adapter Board and attached peripheral(s).
- 7. Any other expansion adapter board.

When re-installing an expansion adapter card or portconnected peripheral, follow the procedures outlined in the Chapter 6 "Removal/Replacement" of this manual.

If the configuration switches must be changed, see Chapter 8 "Strapping Information" for proper switch settings. Refer to the instructions in the Chapter 6

# BASIC TROUBLESHOOTING

"Removal/Replacement" for removing and replacing boards, if necessary, for access to switches.

Then turn ON the computer again.

Chapter 3

## Level 0 Diagnostics

This chapter details the tests that are performed during the execution of the automatic power-on diagnostic routines that are contained in the ROM of the NCR Personal Computer. These diagnostics are initiated before any other activity is attempted each time the computer is turned on.

Normal messages and error messages which may be displayed on the screen are shown. Error codes which are issued via the Main Processor Board (MPB) parallel port are also listed. These error codes are shown if the screen error messages are not displayed because of malfunction.

The action to take for each specific error condition is suggested.

It is recommended to log all tests that are running as soon as the computer is switched on.

#### MEMORY TEST

Each time the power is turned on, the ROM program performs a self-diagnosis that includes a check of the Dynamic Random Access Memory (DRAM) resident on the MPB and on the memory expansion board if present. Other MPB components and the keyboard are also tested. The following occurrences are normal:

- A whirring motor noise from the disk drive is heard.
- 2. A blinking cursor appears on the screen.
- The memory test is conducted in 64 KB increments.

LEVEL 0 DIAGNOSTICS

In normal operation, the upper boundary of the bank of memory tested is displayed in the upper left-hand corner of the screen; at successful completion of the test of each bank, the following screens appear:

MEMORY TEST 64K

MEMORY TEST 128 K MERORI TAUT 1928

.

.See Hote Following

NEMORY TEST 2561

See Note Following

#### NOTE

Memory banks on the "Alternate" MPB higher than 256KB are tested, and their upper boundaries displayed, only if the configuration switches (switch 1 and 2 of switch block A, SWA, on the MPB are set to indicate that a higher total amount of memory (640 KB) is present. The memory diagnostic routine tests up to the size set in the switches.

The early-type MPB contains one switch block including 8 switches 2 of them are for memory setting. The maximum size of memory to be set are 256 KB.

If a Main Processor Board error is found or a software memory error is identified in the first 64KB, the processor is looping in this test routine. If this occurs, refer to chapter 2 "Basic Troubleshooting" of this manual and follow the procedures detailed there.

If a software memory error is found above 64KB, the message MEMORY ERROR is displayed and the memory size in the diagnostic routine work area is set to the size of the error-free amount of RAM (to the nearest increment) and the next test in the diagnostic routines is started.

As soon as the processor is in the Level O Loop mode it does not continue the test run, but enters HOLD. If a Parity Error occurs in the first 64KB only one error message is issued as an LED code on the parallel port. An error occurring in the memory area of 64KB to 640KB is issued as an error message in the form of an LED code and additionally it is displayed on the screen (e.g. PARITY ERROR MAIN BOARD). The same message appears if the Level O Loop is run on the Memory Extension Board (PARITY ERROR EXTENSION BOARD).

## OTHER MPB TESTS

Other MPB components are tested. These include:

- o The 8088 Central Processing Unit (CPU) IC.
- o The 2764 Erasable-Programmable Read-Only Memory (EPROM) IC, This IC contains the Basic I/O System (BIOS). BIOS has two major sets of components: the power-on diagnostics/initialization routines; and the device handling routines. Since this is a ROM IC, testing cannot include writing to the IC. The test, therefore, consists of reading the IC, calculating a checksum, and comparing the checksum with that recorded on the IC when it was programmed.
- o The 8237A Direct Memory Access (DMA) Controller.

o The 8253 Timer.

o The 8259 Programmable Interrupt Controller.

#### KRYBOARD TEST

The keyboard interface is tested to verify that a keyboard which can communicate with the processor is attached.

The speaker in the computer ( on the MPB) should sound a single audible tone ("beep") to indicate successful conclusion of the keyboard test and start of the initialization and booting phase.

If no beep is heard, refer to chapter 2 "Basic Troubleshooting" and follow the procedures given.

## INITIALIZATION AND LOADING

If all the above tests have been successfully completed, a beep and the message LOADING appears to signify the initialization portion of the power-on routines is begun. The initialization includes:

## LEVEL 0 DIAGNOSTICS

- Zeroing of DRAM memory
- Setting up in DRAM memory the tables and control information (flags, vectors) needed by the operating system.
- Loading and (booting) the operative system from the diskette in drive A (normally) and transferring control to the operating system coding.

At the conclusion of the initialization and loading process, the operative system should display the system prompt A>. The computer is now ready to run any application or other software.

#### NOTE

The message DISK ERROR, INSERT SYSTEM DISK AND PRESS ANY KEY TO TRY AGAIN is displayed if the boot disk is missing or improperly formatted.

#### ERROR RETURNS

If no activity (disk drive motor noise, blinking cursor, memory test) occurs after the computer and the peripherals are turned on, try the following:

- Turn OFF the power switch.
- Check that your wall outlet is working properly (use a lamp that you know works).
- Turn ON the computer again.
- 4. If the test is still not successful, turn to chapter 2 "Basic Troubleshooting" of this manual and follow the procedures detailed there.

If an audible tone ("beep") is heard after the memory test, and the system prompt is not displayed (or if the message KEYBOARD EAROR is displayed), go to the "Check Keyboard" section of chapter 2 "Basic Troubleshooting",

## PARALLEL PORT ERROR CODES

The power-on diagnostics/initialization routines also issue a set of two character hexadecimal codes to the MPB parallel port. These enable a technician to attach the Farallel/Serial Test Adapter (see "Tools", Chapter 1) and/or an oscilloscope to the parallel port and display these codes to obtain an indication of hardware problems and status. This technique is quite valuable when, for any reason, the programs are unable to display messages on the screen.

# LEVEL O LOOP TESTS ON GENERIC PROCESSOR BOARD

After turning on the computer its hardware is tested in a sequence of test routines before booting the operative system. There exist two groups of tests, the first group is always run, whereas the second group is only executed when Level 0 Loop has been selected. After completion the test sequence is concluded by the booting procedure, if no Level 0 Loop mode has been chosen.

With the Keyboard Adapter (See "Tools", Chapter 1) plugged or the disk drives disconnected from the MPB the Level O Loop Diagnostics are selected. The Parallel/Serial Test Adapter (see "Tools", Chapter 1) is provided with & LEDs on which error messages are issued as error codes (see Fig. 1-3 "Table of Error Messages" Before selecting the Level O Loop Mode the jumpers 3 and 4 of the SWA switch block on the "Alternate" MPB have to be in ON position and the Parallel/Serial has to be plugged into the respective receptacle.

As soon as the Level O Loop Diagnostics encounters an error while running the test, the test procedure is repeated and the respective error message is issued on the LEDs or displayed and/or on screen.

# LEVEL O DIAGNOSTICS

| Error<br>Codes<br>76543210 | Test Description          | Screen<br>Message | Notes                            | Corrective<br>Actions                         |
|----------------------------|---------------------------|-------------------|----------------------------------|---|
| 0000000                    | 2088 Register Test        | UB                | Processor<br>is entering<br>HOLD | Reptece US<br>or MPB                          |
| 00000000                   | EPROM Checksum Test       | U14               | See Note #1                      | Replace U14                                   |
| 000000X0                   | System 1/0 Test           | мрв               | See Note #1                      | Replace MP8                                   |
| 0 <b>0</b> ×0000×          | DMA Controller Test       | U91               | See Note #1                      | Replace U31                                   |
| 00×0000×                   | Timer D                   |                   | See Note #1                      |   |
| 000000000                  | Timer 1                   | 130               | Ses Note #1                      | Replece U30<br>or MPB                         |
| 00000000                   | Timer 2                   |                   | See Noter1                       |   |
| xxx0000                    | Start Memory<br>Refreek   |                   |                                  | No test,<br>no actione                        |
| 00000000                   | Scan for TOP<br>OF MEMORY |                   |                                  | No test,<br>no ections                        |
| DEXXXX                     | RAM Test OD-64K8          |                   | Sas Note F1                      | Replace<br>RAMs in the<br>resp.memory<br>bank |
|                            | Parity Error OD-64K       |                   | Processor<br>is entering<br>HOLD | Replaca US9                                   |
|                            |                           |                   |                                  |   |

Fig. 3-1 Error Messages \*) on Screen and LEDs (1 of 6)

\*) For PC41 with "Alternate" MPB installed only,

LEVEL O DIAGNOSTICS

| Error<br>Codes<br>76543290 | Test Description                   | Screen<br>Massage                                | Note             | 35                     |             | Correct<br>Action  | îve                                |
|----------------------------|------------------------------------|--|------------------|------------------------|-------------|--|------------------------------------|
| 000000000                  | Interrupt Test                     | INTERPUPT<br>FAILURE                             | Sea              | Note                   | <b>₽</b> 1  | Aaplace  | 125                                |
| DDXQQXQX                   | Weit for unex-<br>pected Interrupt | INTERRUPT<br>FAIL DRE                            | See              | Nate                   | <b>#1</b>   | Replace  | U25                                |
| 0000000                    | Weit for ax<br>pected Interrupt    | INTERRUPT<br>FAILURE                             | See              | Note                   | <b>#1</b>   | Replace  | U25                                |
| 00000000                   | RAM Tøst<br>64–128KB               | Menory<br>Test 64K8                              | See              | Note                   | <b>#</b> 2  | Replace<br>acc, to<br>mory co<br>guratio<br>and arr<br>message | RAM<br>me<br>nfi-<br>n<br>or<br>or |
| DEPONEDOX                  | 128-196KØ                          | 12868  | U                | "                      | <b>#</b> 2  | η  | 1                                  |
| n <b>txxxx000</b>          | 196-256KB                          | 196KB  | н                | ų                      | <b>#</b> 2  | Ħ  | v                                  |
| athoxath                   | 256-320KB                          | 256KB  | pr.              |                        | <b>#</b> 2  | H  | 14                                 |
| DEBOOKERKO                 | 320-384KB                          | 320K8  | 11               | v                      | #2          | "  | 11                                 |
| 0000000                    | 384 <b>-448KB</b>                  | 3B4K8  | Ħ                | IJ                     | <b>#</b> 2  | •  | "                                  |
| 00000000                   | 44 <del>8 -</del> 512KB            | 4 <b>48</b> KB                                   | "                | Ħ                      | <b>#</b> 2  | H  | v                                  |
| anoooxax                   | 512-576KB                          | 51 2KB   | Ð                | N                      | <b>\$</b> 8 | 11   | br                                 |
| DIDOCOCKI                  | 576-640×8                          | 576KB  | Π                |                        | <b>#</b> 2  | 4  | U                                  |
| DEXERSION                  | Parity Test                        | PARITY<br>ERROR ON<br>MAIN<br>PROCESSOR<br>BOARD | Pro<br>1s<br>ing | cesso<br>enter<br>HOLC |             | Replace<br>U77,U88<br>MPB                                      | 0088<br>3 ar                       |

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| Error<br>Codea<br>76543210 | Test Description                               | Screen<br>Message                        | Notes  | Corrective<br>Action                       |
|----------------------------|--|--|--|--|
| DEXENOUS                   | Parity Test                                    | PARITY<br>EAROR ON<br>EXPANSION<br>BOARD | Processor<br>is anter-<br>ing HOLD   | Replace<br>Expansion<br>Board              |
| XXX60000                   | CLeer RAM/Satup<br>temporary/Stack<br>for Boot |  |  | No test,<br>no corr.<br>action             |
| 000000                     | Flex Disk Con-<br>troller Farady<br>IC         | FLOPPY DISK<br>Controller<br>Failure     | U <b>48</b>  | Replace U46                                |
|                            | Barial Control-<br>lar Test                    | SERIAL, I/O<br>TROLLER<br>FAILURE        | Smitch 3 on<br>SWA must be<br>DN, Thie<br>test is only<br>run in the<br>Level D<br>Loop mode,<br>See Note #1 | Replece 131<br>or MP8                      |
| 00000000                   | Serial Port<br>Teet                            | SERIAL<br>PORT<br>FAILURE                | Switch on<br>SwA must be<br>ON.This<br>test is<br>only run in<br>Loop mode. S                                | Rapiece<br>MPB<br>the Level O<br>se Note1# |

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Fig. 3-1 Error Messages on Screen and LEDs [3 of 8]

| Error<br>Codes<br>78543210 | Test Description            | Screen<br>Messege          | Notas  | Corrective<br>Action       |
|----------------------------|-----------------------------|----------------------------|--|----------------------------|
| 000000                     | Paralial Fort<br>Test       | PAINTER<br>PORT<br>FAILURE | Switch 4<br>on SWA must<br>be ON<br>This test is<br>run in the<br>Leval O<br>Loop mode.<br>Processor<br>is enter-<br>ing HOLD.                         | Replace MPB                |
| C×00000C×                  | Kayboard Test               | Keyboard<br>Error          | In the<br>Level O<br>Loop mode<br>the key-<br>board<br>connector<br>or test<br>adapter<br>must be<br>plugged.<br>Otherwise<br>test run<br>is continued | Reptece MP8<br>or keyboard |
| 00×00000                   | Display Loading             | LDADING                    |  | No Test                    |
| <b>OXOXOODO</b>            | Set Interrupt<br>Controller |                            |  | No Test                    |
| DOOKDEX0                   | Configure Parallel<br>Ports |                            |  | Not THUE                   |
| BOOKDDOX                   | Configura Parallai<br>Porta |                            |  | No Test                    |

Fig. 3-1 Error Nessagas on Screen and LEDs (4 of 6)

# LEVEL O DIAGNOSTICS

| Error<br>Godas<br>76543210              | Test Description                             | Screen N<br>Message   | otes  | Corrective<br>Action          |
|---|--|---|---|-------------------------------|
| XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX | Eneble Parity                                |   | •   | No Test                       |
| (DOX(DOX()                              | Configure<br>Keyboard                        |   |   | No Test                       |
| 0×000×0                                 | Configure Disk                               |   | ·· · · · · · · · · · · · · · · · · · ·  | No Test                       |
| XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX | Configure Game<br>Card                       |   |   | No Test                       |
| DACK DX CX                              | Determine if<br>Coprecessor is<br>present    | SWITCH<br>SETTING<br>COPROCESBOR,<br>BUT INDICATES<br>NO COPROCESSO | US Repl<br>or l<br>R IS DETECTI   | 1969 15<br>176<br>20          |
| 000x0x00                                | Initialize ROM<br>Drives<br>(including fixed | disk)   |   |                               |
| XXXDDDXX                                | Hipa Test                                    | (XP) EXTRA<br>PERFORMANCE<br>OPTION IS<br>NON-FUNCTIONA             | In Level O<br>Loop pro-<br>cessor re-<br>L mains in<br>this test;<br>otherwise<br>the follow<br>tests are | Raplace<br>MPE<br>ing<br>rum. |
| DOCO2X00                                | Set up Drives                                | ADVANCED<br>QUAD FLEX<br>DISK CON-<br>TROLLER CARD<br>INSTALLED     | This mes-<br>sege is<br>displayed<br>only with<br>this con-<br>troller<br>instatled                       | Not Test                      |

Fig. 3-1 Error Messeges on Screen and LEDs (5 of 6)

| Error<br>Codes<br>76543210              | Test | Description | Screen<br>Message   | Notes  | Corrective<br>Action |
|---|------|-------------|---|--|----------------------|
| XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX | Boot |             | DISK ERROR,<br>INSERT<br>SYSTEM DISK<br>AND PRESS<br>TO TRY AGAIN | This mas-<br>sage is<br>displayed<br>if startup<br>was tried<br>with a<br>non-system<br>disk pre-<br>sent. |                      |

Fig. 3-1 Error Measages on Screen and LEDs (6 of 8)

Note #1 Processor is looping in the respective test

Note #2 The following format is additionally displayed on the screen:

NEMORY ERROR ADDRESS = X00X:X00X EXPECTED =>00X ACTUAL = X00X

The error codes shown in Fig. 3-1 are valid for the ROM BIOS 3.5 (150-0002348) only. This ROM BIOS can also be contained in the early-type MPB.

## LEVEL 0 DIAGNOSTICS

## BEROR MESSAGES OF THE BARLIER-TYPE MPB \*

If a ROM.BIO 2.3 version 017-00xxxxx is concerned the Level 0 Loop mode is running when: 1. S1 on switch block is in ON position (refer to Chapter 8 "Strapping Information")

2. Fixed disk controller is disconnected

| Error<br>Code**<br>76543210 | Test Description       | Sçraan Maşsage  | Notes                         |
|-----------------------------|------------------------|---|-------------------------------|
| XOXOXOXO                    | 8088 Register<br>Test  |   | Processor<br>entering<br>HOLD |
| XOKXODOX                    | EPROM Checksum<br>Test |   | Processor<br>entering<br>HOLD |
| XOCKOOKO                    | DMA Controller<br>Test |   | Processor<br>entering<br>HOLD |
| XOXXXXX                     | 8253 Timer Test        |   | Processor<br>antering<br>HOLD |
| XIXXIXOD                    | RAM Test<br>CO - 64KB  |   | Processor<br>entering<br>HOLD |
|                             | 64 — 64DKB             | MEMORY TEST and/or<br>MEMORY ERROR                        | Firmwere<br>not helted        |
| XDXXDXOO                    | Parity Error           | EAROR IN BASE<br>MEMORY or<br>ERROR ON EXPANSION<br>BOARD | Processor<br>antering<br>HCLD |

Figure 3-2 Error Codee on LEDs and Screen (1 Of 2)

Early-type Main Processor Board (017-0044046)
 on the LEDs of the Test Adepter (Chapter 1 "Toole")

| Error<br>Code<br>78543218 | Test Descriptioon                         | Screen Мезваде    | Note   |
|---------------------------|---|-------------------|--|
| XUXXUXUX                  | Interrupt<br>Controller Test              | INTERRUPT FAILURE | Processor<br>entering<br>HOLD  |
| XOCKUXUX                  | Keyboard Teat                             | Keyboard Error    | Firmmere<br>not halted   |
| XEDXOREX                  | Display of Load                           | LOADING           | No test  |
| XDOXDOX                   | Configuration for<br>Parailel Port        |                   | No test  |
| None                      | Fixed Diek Test<br>Error in Fixed<br>Diek | 1701              | Fixed Disk<br>not<br>connected,<br>devica de-<br>fective,<br>controller<br>defective |

Fig. 3-2 Error Codes on LEDs and Screens (2 of 2)

#### SUGGESTED ACTION

For some of the error conditions which can occur during the automatic running of the power-on diagnostics routines, the indication has been given in this chapter to go to the Troubleshooting Nonoperative Generic Systems chapter of this manual.

This tactic is generally appropriate when the problem appears to be in the RAM memory, or if the problem may be in any component added to the original main computer either by addition of an expansion adapter card or by plugging a cable into either the parallel port or the serial port on the MPB.

# LEVEL O DIAGNOSTICS

The Troubleshooting Nonoperative generic systems chapter gives detailed instructions for removing addon memory and/or other devices to return the computer to the original status, then for restoring the machine to full configuration one step at a time. This procedure can isolate problems to a specific device.

Where the problem is indicated to be in one of the key MPB ICs (Processor, EPROM, DMA controller, Timer, Interrupt controller), the procedures in the Troubleshooting Nonoperative Generic Systems chapter are not likely to help.

Rerun the power-on process from completely OFF as described in this chapter two or three additional times to ensure that the error indication is "hard", i.e., it consistently repeats the identical message, code, etc.

When a problem appears to be definitely located in one of thge pluggable MPB ICs the next step is disassemble the unit by removing the cabinet as shown in the Removal/Replacement chapter of this manual. Read and heed all warnings and cautions in that chapter before working on the computer with the cabinet removed.

In some cases, more positive seating of the IC can be accomplished either by gently but firmly pushing on it (discharge static first by touching a finger to the frame or to a board support standoff) or by very carefully removing the IC concerned, inspecting and, if necessary, straightening the pins, and reinserting the IC in its socket. After reassembling the cabinet, retry the power-on procedure.

#### NOTE

When handling ICs on boards observe ESD Precautions. Wrist straps (with 1Megohm resistor) are required when handling any unprotected IC or PCB or when working on equipment without cabinetry. If the attempt to improve the IC seating does not help, or if a hard error is indicated in a solderedin IC (Timer, Interrupt Controller), the MPB must be removed from the computer and either replaced or repaired.

Follow the instructions in the "Cabinet" and "MPB" sections of Chapter 6 "Removal/Replacement". When the MPB has been removed, it can be taken to an NCR service facility or NCR-authorized dealer for repair or replacement.

(Repair of the MPB can be successfully done only by a well-trained field engineer or technician, since it involves unsoldering the defective component from a multi-layer PC board, and then soldering the new component into the same location on the same multilayer PCB. The danger that the heat of the desoldering/sooldering may damage the printed circuits is considerable).

## LEVEL 1 DIAGNOSTICS

This chapter tells you how to use the SERVICE DIAGNOSTICS diskette which is in the back of this manual. These diagnostics, also referred to as Level 1 Diagnostics, are the most comprehensive set of test routines supplied and supported by NCR for the NCR PERSONAL COMPUTER Model 4i.

Following the instructions in this chapter, the field engineer or technician interacts with the SERVICE DIAGNOSTICS software to test all or selected components of the computer. The software presents a hierarchy of menu selection screens on the CRT to simplify the choice of tests to perform. For all menu screens, corresponding HELP screens are available to further explain the purposes of the tests which may be selected.

Each menu and HELP screen is shown in this chapter, along with the results of each selection path chosen (the next level menu screen, or a completion screen). How to begin the testing, and how to terminate it, are described.

## HOW TO BEGIN TESTING

#### LOAD THE DISKETTE

- Make certain the power cord is plugged into the back of the processing unit.
- Make certain that the processing unit power cord is plugged into a properly grounded wall outlet.
- Make certain that the power cords for any currently-attached external devices (printer, etc.) that have separate power cords are plugged into properly grounded wall outlets.
- Insert the SERVICE DIAGNOSTICS diskette into drive A.

## BOTE

Use a back-up copy of the diskette included in the manual; the possibility exists that a problem with the computer could destroy the data on the disk.

- Turn the drive access lever clockwise to enable the drive.
- 6. Turn ON any currently-attached external devices that have separate power cords and switches (unless the device installation instructions specified that the device be turned on after the processing unit).
- 7. Turn the computer.
- Turn ON any currently-attached external devices that were specified to be turned on after the computer.

The BIOS ROM power-on diagnostics/initialization routines perform the power-on testing that occurs each time the machine is turned on, then DOS is loaded and the date and time should be entered. The Diagnostics Shell is then loaded.

## MAIN MENU

The main menu is displayed each time the SERVICE DIAGNOSTICS diskette is booted in, and each time the user chooses to return to the main menu at the completion of a specific test.

The main menu gives the operator the following categories of choices:

- o Run All Tests (Press <F7>)
- o Run All Tests Continuously (Press <F8>)
- Test A Specific Component (or type) (Menu items 1 to 11)

- o Run Utilities (Press <F6>)
- o View Main Menu HELP screen (Press <F1>)
- Initiate A Continuous Test Run (Press menu item number and <F8>)

The following diagram illustrates the structure of the diagnostic system:



Fig. 4-1 Diagnostic Structure Diagram

The screens shown in this chapter may vary slightly depending on the version of diagnostics being used.

The Main Menu, as it appears on the display screen, is as shown:

The NCR Personal Geneuter Page 1 of 2 Diagnostic Shell Vereige 1.0 Copyright 1985 NCH Corp. MAIN NEWL Available Teste 1 Henry Þ Controllers (Circuit Designs) MBEED. з Keybaard 360K8 Flexible Diek Drive[s] 4 ٥ Find Stab Drive ٠ Monochroge Dieplay FALLED Color/Grephics Display 7 8 Peripherels (F1) Explanation of Tests (F7) Run All Teste Dice **0F8**> Utilities (PP) Sup ALL Teste Concinuous Add/Delete Toots to Main Menu Poup> Previoue Page (FIL) OFID> Eait to DDB C'gOn> Heat Pepe Press ediection and (Enter) for running test once - OR Press selection and (FS) for continuous run of selection ((FA) to stop) SELECTION =>

Page 2 of the Main Menu, containing items 9 to 11, is accessed by pressing the <PgDn> key.

Page 2 of 2 The NCR Personal Computer Diagnostic Shell Version 1.01 Copyright 1984,1985 NCR Corp. NACH KENU Available Tests 09 High Speed {1P} 10 Tape Drive IL BTX (F1) Explanation of Tests (F7) Run All Tests Once (F6) Utilities (F8) Run All Tests Continuous (F9) Add/Delete Tests to Main Menu (PgDn) Previous Page (F10) Exit to DOS (PgDn) Next Page Press selection and (Enter) for running test once OR Press selection and (FB) for continuous run of selection (F4) (to stap) SELECTION =>

All highlighted tests except HIGH SPEED (XP) are available for your system.

To add/delete tests to/from the Main Menu, press the <F9> key. If the <F7> key is pressed, each test (1 through 11) listed on the menu is automatically run in sequence, starting with the memory test. The test, for any device (fixed disk, color/graphics display, etc.) which is found by the diagnostic routines not to be present, is skipped.

To select any specific test, type the number for that test and press ENTER.

To run any test continuously, type the number and press the  $\langle F8 \rangle$  key. That will be run continuously until $\langle F4 \rangle$  is pressed. If no test number is selected before the  $\langle F8 \rangle$  key is pressed, the series of tests 1 through 11 is repeated continuously until $\langle F4 \rangle$  is pressed.

Fress the  $\langle F6 \rangle$  key to access a set of utility routines. These utilities perform functions that are related to or needed during diagnostic or maintenance operations.

The utility functions available include:

- o Error Log Maintenance
- o Initialize Fixed Disk
- o Position Head For Relocation
- o Alignment Utilities (for video displays and fixed disk.)
- Speed Check (for flexible disk drives)

#### MAIN MENU HELP SCREENS

To obtain more information about main menu choices without referring back to this chapter while running the diagnostics, press the <F1> key. The main menu HELP is displayed: Memory - Test for GAM pattern, MAM address, MAM parity and FIRMMARE checkaum.
Controllars - Teste for a CPU, DAA, interval timer, interrupt, CPU controllar, CRT, and serial controllar.
Fayboard - Teste the keyboard port, such keyboard key and its seencisted code.
Fiatizis disk drive - Teste for feed only and read/write operation of each flaticle disk drive.
Fixed disk drive - Teste for feed only and read/write operation of the disk and for the fixed disk controller.
Monochroms display - Teste for feed only end read/write operation of the disk and for the fixed disk controller.
Monochroms display - Teste the CRT RAM, surger addressing, the ASCII character set, the scrolt register and various monitor statibutes.
Calor/Graphics display - Teste the CRT RAM, cursor addressing, the ASCII character set, the acrolt register and various monitor statibutes.
Peripherale - Teste the parallel and seriel parts and en optionet printer.
(53) Return to previous acround

#### TEST COMPLETION DISPLAYS

When a test is completed, the diagnostics routines display either a PASSED message or a detailed coded error message. Chapter 5 Level 1 Diagnostics Error Returns, explains all of the possible error messages and the suggested action to take for each.

RAM ADDREAS PASHED (F2) formingto Tost Moduly (F3) Roturn to Providue Neou Examples of completion displays are:

# Successful Completion

```
___NAM ADORESS
PASSED
(F2) Torminets Test Modula
(F3) Return to province Monu
```

In the example shown, the RAM Address test, one of the memory tests included in selection 1 of the menu has run and that part of the memory has passed the test procedures.

If the selection was made and the Enter key pressed, a MEMORY menu is displayed and a test selection must be made pressing the Enter key pressed again. If the selection of memory was made and the  $\langle F8 \rangle$  key was pressed, the tests run continuously until the  $\langle F4 \rangle$ key is pressed. **Onsuccessful Completion** 

FLEX:03:00 (deta) itied DV FITY STO ST1 ST2 TRACK HEAD SECTOR DENSITY 0 00 zz uz fz kk Hi zu zu

In this example, flexible drive B failed the Write test, part of the Flexible Disk Drive test (selection 4 of the main menu). It failed because the flexible disk controller (FDC) failed to respond.

The major error codes (03 = flexible disk drive write error) and detail error codes (80 = FDC failed to respond), and the abbreviations and values on the next two lines, are explained in Chapter 5, Diagnostics Error Returns.

## DIAGNOSTIC SELECTIONS

The following sections describe the results of each selection chosen from the main menu. Specific device, or device type, menus, HELP screens accessed from the device menus, and each test chosen from a device menu, are shown. The display upon completion of each test or sub-test has the PASSED or FAILED messages which are clear from the screen as the next message is displayed. All error messages are retained in the Error log file for reference.

#### RUN ALL TESTS ONCE (FUNCTION KEY <F7>)

When the <F7> key is pressed from the main menu screen, the separate tests for specific devices (selections 1 through 11) are automatically run in sequence. The test for any device (fixed disk, color/graphics display, etc.) which is determined by the diagnostic routines to be not present is skipped.

## CAUTION

The flexible disk diagnostic tests are destructive to the data on any diskette other than the diagnostic diskette. BEFORE CONTINUING, ensure that no diskettes other than the diagnostics diskette and/or formatted scratch diskette(s) are inserted in the flexible disk drive(s). If any other diskette is left in, data and/or program on it will be written over by test patterns. The program does not stop again after this point. If no formatted disks are available. and one or two are needed at this point, exit from the diagnostics routines (press <F10>, load the DOS diskette, and follow the instructions in the DOS manual to format as many diskettes as you need. When finished, return to the diagnostic routines and select the RUN All Tests selection (press <F7>).

#### Automatic Completion

The program displays the message PASSED after successful completion of each test or sub-test in the same format as with individual tests. The main menu does not remain on the screen.

The appropriate message is displayed after the test or sub-test during which the error is detected; however, the program does not stop. The program runs to successful or unsuccessful completion of all tests regardless of test results. The diagnostics will return to the main menu upon completion of the tests. Upon completion the messages appear as shown in the sample screen below. In this sample, both the keyboard and the monochrome display failed in some aspect, and because there is no color display, there is no message for that menu selection.

| The MCR Personal Computer<br>Discoveric Shell Version 1.0 |                 | Page 1 of 2              |
|---|-----------------|--------------------------|
| Copyright 1905 NCR Corp.                                  |                 |                          |
| NAIH MENU   |                 |                          |
| Averlebie Teers   |                 |                          |
| 1. Hamory   |                 | PASSED                   |
| 2 Controllere [Circuit Devices]                           |                 | PASSED                   |
| 3 Kayboard  |                 | FAILED                   |
| 4 380×8 Flaskbla Cisk Orive[s]                            |                 | PAGGED                   |
| 5 Fised Diek Orive  |                 | PL95ED                   |
| 5 Monachrose Olepier                                      |                 | FAILED                   |
| 7 Color/Graphics Display                                  |                 |                          |
| 8 Peripherets   |                 | PAGAED                   |
| <f1> Explanation of Tests</f1>                            | (F7) I          | Run All Tgets Onco       |
| <f8> Utilities</f8>                                       | (CFB) I         | Run All Tests Concingous |
| <f9> Add/Oclote Tests to Mein Menu</f9>                   | <pgup> i</pgup> | Provioué Pége            |
| <fid> Exit to DOS</fid>                                   | (PgCn) (        | Neuz Page                |
| Preds delection and (Enter) for munning                   | a tees om       |                          |
| Prode beloction and (FB) for continuous                   | n nun of        | melection (SF4) to etop; |
| accevition */   |                 |                          |

To see a record of any errors detected, select the utilities function (F6 key) and then select Error Log Maintenance ("3"). From the Error Log Maintenance Menu, select "1", Display Error Log, and press ENTER.

#### MEMORY (MAIN MENU SELECTION 1)

When selection number "1" is chosen from the main menu screen, the Memory tests menu screen is displayed:

```
Copyright 1985 NCK Corp.
The NCH Personal Computer
                                   NE2408 Y
                         Installed Remory - axes XB
Available Selections
                       - Writes and reads test Patterns to/from memory
L KAM Pattern
                      - Verifles addressing in KAM
2 RAM Address
                      - Checks the operation of MAN parity
3 MAM Parley
                       - Writes and reads patterns of welking once and terus
  RAH Walking Ones
4
5 FIRMWARE Checksum - Performs a checksum test of the system FIRMWARE
(Ft) explanation of Selections
(F2) Tetainate Test Nodule
Press selection and (Enter) for Evening test once OA
Frees selection and (F8) for continued run of selection ((T4) to stop)
SELECTION - >
```

Each of the five available tests is selected by entering the corresponding number (1 through 5). To run a specific test once, type the selection and press ENTER. To run the test continuously, type the selection and press the  $\langle F8 \rangle$  key. Press the  $\langle F4 \rangle$  key to stop a continuous run.

When the <F2> key is pressed, the program returns to the main menu.

#### NOTE

The Walking Ones Test will take considerably more time to run than the other tests. It will not be performed when the memory tests, are run from the Main Menu (Selection 1, Function Key  $\langle F7 \rangle$  or  $\langle F8 \rangle$ ). It must be selected from this menu. LEVEL 1 DIAGNOSTICS

Memory Test HELP Screen To obtain more information about the available memory tests, press <F1> to see the HELP screen:

```
RAM Pattern - Writes test potterns to check DRAM, dete path and
consol directory.
RAM Addrees - Teats both asgment addressing and los order addressing
copsbilities.
BAN Portay - Writes checkerboord patterns to all installed parity ORAM,
FIRMMARE Checkeum - Varifies the firmware checkeum
(F3) Return to provide more
```

#### RAM Pattern Test

If selection ; of the Memory test menu is chosen, the RAM pattern test is run. The name of the test is displayed while the test is in progress:

RAM PATTERN PASSED (F2) Terminate Test Module (F3) Asturn to previous wells

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 10 of this manual for a description of error messages and recommended corrective actions.

#### RAM Address Test

If selection 2 of the Memory test menu is chosen, the RAM Parity test is run. The name of the test is displayed while the test is in progress:

NAM ADDRESS PASSED (F2) Terminate Test Module (F3) Assure to previous menu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions. LEVEL 1 DIAGNOSTICS

## **RAM Parity Test**

If selection 3 of the Memory test menu is chosen, the Firmware Checksum test is run. The name of the test is displayed while the test is in progress;

RAM PARITY PASSED (F2) Terminuto Test Module (F3) Return to providua menu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error message and recommended corrective actions.
#### RAM Walking Ones Test

If Selection 4 of the Memory Test Menu is selected, the RAM Walking Ones Test is run.

#### FOLE

This test can only be run, after the three preceding RAM tests have been completed. Otherwise a warning is displayed.

A menu similar to the other memory test menus is displayed while the test is in progress. Upon completion of the test, either the passed message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description or ERROR messages and recommended corrective actions.

Che BCR Personal Computer RENORY Costalled Memory = 0256 KB PRENORS CHESS PRESETD CF23 Ferminate Test Module (F3) Return to previous senu

### **PIRMNARE Checksum Test**

If selection 5 of the Memory test menu is chosen, the Firmware Checksum test is run. The name of the test is displayed while the test is in progress:

```
FIRNMARE CHEERSUM
MASSED
(F2) Terminate Test Kodule
(F3) Return to previous menu
```

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

### CONTROLLERS (CIRCUIT DEVICES) (MAIN MENU SELECTION 2)

When selection number "2" is chosen from the main menu screen, the Controllers test menu screen is displayed:

| The ACR Personal Cumputer   | Copyright 1885 KCA Corp.   |
|---|--|
|   | CONTROLLERS  |
| Available Selections  |  |
| 1 CPU<br>2 DNA<br>3 Five/bib Disk<br>4 Interval Tison<br>5 Interval Tison<br>6 CPU Controller<br>7 Monochrome CRT<br>8 Seriel<br>(F1) Exploration of Seven<br>(F2) Tervinete Test Modul<br>Press selection and (Enter<br>Press selection and (F8) f<br>5ELECTION => | <ul> <li>Tasks the 8088-2 (PU chip)</li> <li>Tests the Offect Memory Access controller</li> <li>Tests the 8279 Fightalls Disk controller</li> <li>Tests the 8253 there and conter chip</li> <li>Tests the 8259 interrupt controller</li> <li>Tests the 6845 for wahedrows operation</li> <li>Tests the 8250 emrial controller</li> </ul> |
|   |  |

Each of the nine available controller tests is selected by entering the corresponding number, 1 through 9. If the F8 key is pressed after the number for a specific test is selected, that test is run continuously until  $\langle F4 \rangle$  is pressed. If the ENTER key is pressed after the number of a specific test is selected, the test will run once and stop. Press  $\langle F2 \rangle$ to return to the main menu.

#### Controllers Test RELP Screen

To obtain more information about the available tests of the various Main Processor Board (MPB) and adapter card controller chips, press F1 to see the HELP screens:

CPU - Teers the execution of the instruction set CMA - Verified the CMA controller can be accessed and performs a partial functionality test, Finaible Disk - Varifies the Figsebie disk controller can be uncaused and performs a partial functionality test. Interval Timor - Varifies the interval timer can be accessed and performs e partiel functionality Lest. interrupe - Varifies the interrupt innergiler can be accessed and performs a partial functionality case, CPU Controller - Verifies the VLSI CPU controller can be eccaseed by performing on internal test on the paripheral interface and the keyboard interface. CRT - Ver: flee the CRT controller can be eccused and performe a persial functionality test, Serial - Verifies the serial controller can be accessed and performs a presial functionality test. (F3) Recurs to previous serv

## CPU Test

If selection 1 of the Controllers test menu is chosen, the CPU test is run. The name of the test is displayed while the test is in progress:

```
______
PASSED
(F2) Tarainsta Tost Medula
(F3) Return to providue menu
```

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

#### DMA Test

If selection 2 of the Controllers test menu is chosen, the DMA Controller test is run. The name of the test is displayed while the test is in progress.

\_\_\_ОНА PASSED (F2> Termineta Test Module (F3> Roturn to pesylous menu

Upon completion of the test, either the PASSED message or an error message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

## Flexible Disk Controller Test

If selection 3 of the Controllers test menu is chosen, the Flexible Disk Controller test is run. The name of the test is displayed as the test is in progress.

\_\_\_FLEXIBLE DISK PASSED (F2) Tarminate Tust Module (F3) Return to providue benu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions. Interval Timer Test

If selection 4 of the Controllers test menu is chosen, the Interval Timer test is run. The name of the test is displayed while the test is in progress.

\_\_\_INTERVAL TIMER PASSED <F2) Tureinste Test Hoduls <F2) Return to previews menu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description or error messages and recommended corrective actions.

### Interrupt Controller Test

If selection 5 of the Controllers test menu is chosen, the Interrupt Controller test is run. The name of the test is displayed while the test is in progress.

INTERAUPT CONTROLLER PASSED (F2) Torminate Teet Modula (F3) Return to providue menu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Hefer to chapter 5 of this manual for a description of error messages and recommended corrective actions.

### CPU Controller Test

If selection 6 of the Controllers test menu is chosen, the CPU Controller test is run. The name of the test is displayed while the test is in progress.

CPU CONTROLLER PASSED (F2) Terminate Test Module (F3) Return to previous eacu

Upon completion of the test, either the PASSED message or an ERROR message is added to the desplay. Refer to chapter 5 of this manual for a description of error messages and recommended corrective actions.

### Monochrome Display Controller Test

If selection 7 of the Controllers test menu is chosen, the Monochrome Display Controller test is run, if the program senses the presence of a monochrome display adapter. If it does not sense the presence of a monochrome display adapter, the program redisplays the Controllers test menu. The name of the test is displayed while the test is in progress.

\_\_\_\_CRT, MEND PASSED (F2) Terwinsts Twat Moduls (F3) Raturn go pravique menu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

## Color/Graphics Display Controller Test

If selection 8 of the Controllers test menu is chosen, the Color/Graphics Display Controller test is run, if the program senses the presence of a color/graphics display adapter. If it does not sense the presence of a color display adapter, the program redisplays the Controllers test menu. The name of the test is displayed while the test is in progress.

\_\_\_\_\_CRT, COLOR PAGSED G-2> Terminate Tast Module <F3> Heturn to previous serv

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

Serial Asynchronous Communications Controller Test If selection 9 of the Controllers test menu is chosen, the Serial Asynchronous Communications Controller test is run. The name of the test is run. The name of the test is displayed while the test is in progress.

\_\_\_\_SERIAL PAGEED (F2) Terminate Test Module (F3) Report to providus edmu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

## **KEYBOARD (MAIN MENU SELECTION 3)**

When selection number "3" is chosen from the main menu screen, a message indicating a test of the keyboard port is displayed, followed by a line indicating a test of the keyboard and a pattern of rectangular blocks representing the positions of keys on the keyboard. The operator presses any or all keys while visually verifying that for each key the proper key value appears at the proper location in the pattern.

The last line in the display directs the operator to ENTER(F1) AND  $\langle F10 \rangle$  IN SEQUENCE TO EXIT TEST. Whether

any other keys have been pressed or not, pressing these two causes another message to be displayed. After all keys have been pressed, followed by the  $\langle F1 \rangle / \langle F10 \rangle$  entry, the display is similar to the screen shown:

The MCS Personal Computer Copyright 1885 WCR Corp. LEYSCARD. KEY BOLKO F1 F2 ESC 1 DEL PU NL 2 з 4 5 6 2 B ģ Ð 85 5L R F TAB D Е т ¥ Ш 0 P ۲, LA GTL END PD N.M T ۴u ι F5 64 CTC 4 \$ p ċ ж 4 R. L 5 г : ZXG F7 F8 H h **P**5 HM - ENG PO. . Ŧ FB F10 ALT 68 CL INS ING DEL 2R SATER THE KEY CODES TO BE TESTED AND VERIET THAT THE CORRECT CODE APPEARS DA THE CRT DISPLAT. EXTER (F1) AND (F10) IN BELIEVEL TO EXIT TEST.

An entry "Y" indicates a successful completion of the test and a message, PASSED, will be displayed. Press the <F2> key to return to the main menu.

An entry of "N" indicates unsuccessful completion and an ERROR message is displayed. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

FLEXIBLE DISK DRIVE (MAIN MENU SELECTION 4) When selection number "4" is chosen from the main menu screen, the Flexible Disk Drive test menu screen is displayed:

| The KCR Personal Computer  | Copyright 1984,1985 MCR Corp.                                |
|--|--|
| FLEJIB   | LE DISK DAIVE  |
| Number   | of Orives = 02   |
| Available Selections   |  |
| Read Only tests on available drives                                    | Read/write tests on Available Drives                         |
| 1 Drive A  | S Orlye A  |
| 2 Drive B  | 6 Drive B  |
| 3 Drive C  | 7 Drive C  |
| 4 Orive D  | 8 Drive D  |
| Note: To insure proper operation und<br>in the utilities section of th | ler all conditions, run the dist speed test<br>we Main Menu. |
| (FI) Explanation of Selections   |  |
| (F2) Terminate Test Module   |  |
| Press selection and (Enter) for runn                                   | ning test once OR  |
| Press selection and (FB) for continu<br>SELECTION =>                   | ous run of selection ((F4) to stop)                          |

Each of the eight available tests is selected by entering the corresponding number (1 through 8). If the  $\langle F8 \rangle$  key is pressed after the number for a specific test is selected, that test is run continuously until  $\langle F4 \rangle$  is pressed. If the ENTER key is pressed after the number for a specific test is selected, the test runs one time and stops. When  $\langle F2 \rangle$ is pressed, the program returns to the main menu.

### Flexible Disk Drive Test HELP Screen

To obtain more information about the available flexible disk drive tests, press <F1> to see the HELP soreen:

Read Only - Versies an entire NCR PC standard format dist in one of the installed drives. To check disk rotational speed accuracy, execute the FLEITBLE DISK SPEED Utility.

Read/Write - Performs a format/write/read on selected sectors that are reserved by the diagnostic system in a DDS file. This is a destructive test on those reserved sectors. To check disk rotational speed accuracy, execute the FLECIBLE DISK SPEED Utility.

(#3) Return to previous menu

#### Read Only Drive & Test

If selection 1 of the Flexible Disk Drive test menu is chosen, the Read Only test is run on drive A. The name of the test is displayed while the test is in progress:

\_\_\_\_READ CHLY, FLEXIBLE OMIVE A PASSED (F2) Tormingte Twot Module (F2) Return to previous sons

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

#### Read/Write Drive A Test

If selection 5 of the Flexible Disk Drive test menu is chosen, the Read/Write test is run on drive A:

```
___READ/WRITE, FLEXIBLE DRIVE A
PASSED
```

(F2) Termineta Test Medule (F3) Return to previous denui

#### CAUTION

The flexible disk diagnostics tests are destructive to data on any diskette other than the diagnostics diskette. BEFORE CONTINUING, ensure that no diskette other than the diagnostics diskette and/or a formatted scratch diskette is inserted in flexible disk drive A. If any other diskette is left in, data and/or programs on it will be written over by test patterns.

Press the space bar or any alphabetic or numeric key to continue.

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error message and recommended corrective actions.

#### Other Flexible Disk Drives

The selection of other tests on the Flexible Disk Drive menu causes the test selected to run in the same manner as the tests for Drive A. The messages are the same in all cases.

#### FIXED DISK DRIVE (MAIN MENU SELECTION 5)

When selection number "5" is chosen from the main menu screen, the Fixed Disk Drive test menu screen is displayed:

```
Copyright 1985 NCA Corp.
The MCR Parennet Computer
                       FIXED DISA CAIVE
Available Setections
1 Read/Write Maintenance Track - Gestructive teet to anintenence track
                                  Read sector gars on each track
2 Aund Doly Sector Zero
3 Reed Only Entire D'ex
                                 - Read such sector on Fixed disk
 Reed/Write Entire Diew
                                 - Contructive test to entire fixed disk
đ
                                 - Specific test to fixed disk controllor
5 Fired Dick Controller
($1) Explanation of Selections
(F2) Terminate Sept Moduly
                                                 C.O.
Proce selection and sentery for running test ance.
Press selection and (FD) for continuous cun of estaction [<F4) to stopi
SELECTION ->
```

Each of the five available tests is selected by entering the corresponding number (1 through 5). If the  $\langle F8 \rangle$  key is pressed after a specific test is entered, that test runs continuously until  $\langle F4 \rangle$  is pressed. If ENTER is pressed after the selection is made, the test runs once and then stops. Press  $\langle F2 \rangle$ to return to the main menu.

#### Fixed Disk Drive Test HELP Screen

To obtain more information about the available fixed disk drive tests, press (F1) to see the HELP screen:

Prior to executing each epecific test, the controller test is performed. Reso/Write Naintanence Track - Foreste, stide, and rende the weintenence track. Reed Only Sector Zord - Verifies sector 3 of every track on disk. Reed Only Entire Disk - Performe a read only test on the mattre fixed disk. This is a non-destructive test. Read/Write Entire Disk - Neites and code the entire disk. This is a destructive test; theorefore, the MCR-005 FDISK and FDRMAT utilities must be run on the fixed disk before using the disk again. Fixed Disk Equtrollar - Performe a epochfic test of the Fixed Oisk Controller. (F3> Return to previous menu

## Read/Write Maintenance Track

If selection 1 of the Fixed Disk Drive test menu is chosen the Read/Write Maintenance Track test is run. The name of the test is displayed while the test is in progress:

The NCR Personal Computer FIXED DISK DRIVE AEAD/WRITE KRINTEWANCE TRACK - DRIVE OO PASSED (F2) Terminate Test Module (F3) Return to previous menu Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

Read Only Sector O If selection 2 of the Fixed Disk Drive test menu is chosen, the Read Only Sector O test is run:

AEAD ONLY SECTOR O Drive XX PASSED (F2) Tetminate Teet Module (F3) Return to previous semu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

Read Only Entire Disk

If selection 3 of the Fixed Disk Drive test menu is chosen. First a screen is displayed where you are requested enter the number of fixed disk to test.

| The NCR Personal Computer                        | Copyright | 1984,1985 | MCR Corp. |
|--|-----------|-----------|-----------|
| FIJED DESK DRIVE                                 |           |           |           |
| READ/WRITE ENITHE DISK                           |           |           |           |
|  |           |           |           |
|  |           |           |           |
| Enter number of fined disk to test               |           |           |           |
| IN-FILME FIRED DISK, 1=SECORD FILED DIST, PEC.PI |           |           |           |

After selecting the number of the drive to be tested the Read Only Disk test is run:

| (  | Fixed ofse   | ) |
|--|--------------|---|
| READ INLY ENTIRE DISK (F4) Stop Teacing                              |              |   |
| 0r5vo00  | FunctionREAD |   |
| ness <u> </u>  | CyclaDB31    |   |
| Surface errore:<br>Soft errore3000<br>Merd errore60                  |              |   |
| Total errores<br>Gaft errore <u></u> COOO<br>Hyra errore <u> </u> CJ |              |   |
|  |              |   |
|  |              |   |
|  | _            |   |

In the above display, the drive number is 00 for the first integrated fixed disk drive or for the first external fixed disk drive if no integrated drive is present. Head number is increased by increments of 1 from 00 to 03 as the test proceeds. Track number for each bead is increased by increments of 1 from 0000 to maximum track number (depending on whether 10MB or 20MB) then is reset to 0000 for the next head. The sector number is increased by increments of 1 from 01 to 17 for each track.

Cycle number is 0001 if this test is being run just once; if continuous run was chosen, cycle number begins at 0001 and is increased by 1 for each complete test cycle until <F4> is pressed to stop the test.

If "any key" (space bar or any alphabetic or numeric key or some special characters) is pressed, the Fixed Disk Test Summary screen is displayed.

#### Read/Write Entire Disk

When selection 4 of the Fixed Disk Drive test menu is chosen, the warning screen of the Read/Write test is displayed:

KEARATHE SHTINE DISK MANADING - This (a a destruction teel. Dote on the entire fixed disk will be destructed. A backup of the disk whould be done before arequiring this tear (Cone before arequiring this tear (CESC) To Cancel Tuet of Any Other Kay to Cantinue In the following screen you are requested to enter the number of the fixed disk to be tested.

The NCS Personal Computer

Copyright 1984.1985 WCR Carp.

FILLED DESK ORIVE

READ/WRITE ENFIRE DISK

Enter number of fixed dust to test 10=Farst Fixed Disk, 1-Second Fired Dust, etc.t:

When "any" key is pressed again, the write pass of the test begins. The information displayed is almost identical to the Read Only Entire Disk test; the main difference is that the Function WRITE is indicated:

| (F4) Scop Testing                                   |              |  |
|---|--------------|--|
| Delra00<br>Haad0n<br>FrackDhnn<br>Sectornn          | Function0001 |  |
| Surface ermone:<br>Saft ermone0000<br>Hend ermone00 |              |  |
| Totel effore:<br>Soft errors0000<br>Hard errors00   |              |  |
|   |              |  |
|   |              |  |

In the above display, the drive number is 00 for the first integrated fixed disk drive or for the first external fixed disk drive if no integrated drive is present. Head number is increased by increments of 1 from 00 to 03 as the test proceeds. Track number for each head is increased by increments of 1 from 0000 to maximum track number (depending on whether 10MB or 20MB) then is reset to 0000 for the next head. Sector number is increased by increments of 1 from 01 to 17 for each track.

After a complete WRITE cycle, the Function is changed to READ, and the complete READ cycle is begun. The display values start over at 00(00) or 01 and are increased to maximum values as above.

Cycle number is 0001 if this test is being run just once; if continuous run is chosen, cycle number begins at 001 and is increased by 1 for each complete test cycle (WRITE and READ) until  $\langle F4 \rangle$  is pressed to stop the continuous run, or until $\langle F2 \rangle$  is pressed to return the program to the main menu.

If "any key" (space bar or any alphabetic or numeric key or some special characters) is pressed in response to the last line, the Fixed Disk Test Summary screen is displayed:

| *** HARD DIG4 TE | 57 SLIMMARY             |              |              |  |
|------------------|-------------------------|--------------|--------------|--|
| Tast tycles: 400 | 1                       |              |              |  |
| Hard arrors: D3  |                         |              |              |  |
| Sofe errore: 030 | 0                       |              |              |  |
|                  | Error Trac <sup>4</sup> | Sector Cycle | Soft presets |  |
| Serface: DO      | NO ENGA                 |              |              |  |
|                  | NC ERPOR                |              |              |  |
|                  | HC EPAJA                |              |              |  |
|                  | NC ERROR                |              |              |  |
| Surface: 91      | NO ETHER                |              |              |  |
|                  | NO ERROR                |              |              |  |
|                  | NO ERROR                |              |              |  |
|                  | NO ERMOR                |              |              |  |
| Garfece: 02      | NO ENGA                 |              | 0000         |  |
|                  | NO ERROR                |              |              |  |
|                  | NO EARIA                |              |              |  |
|                  | NO SERVER               |              |              |  |
| Garfees: D       | ID ERROR                |              | 0000         |  |
|                  | ND EARDR                |              |              |  |
|                  | HO ERMOR                |              |              |  |
|                  | ND ERIBOR               |              |              |  |
|                  |                         |              |              |  |
|                  |                         |              |              |  |
|                  |                         |              |              |  |
|                  |                         |              |              |  |

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

### Fixed Disk Controller

When selection 5 of the Fixed Disk Drive test menu is chosen, the Fixed Disk Controller test screen is displayed.

| The NER Personal Computer                                  | Copyright 1984,1985 NER Corp. |
|--|-------------------------------|
| FLIED DISK DRI   | VE                            |
| FITED DISK CONTROLLER<br>Passed                            |                               |
|  |                               |
|  |                               |
|  |                               |
| (F2) Terminate Test Modu)e<br>(F3) Return to oravinge engl |                               |

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

## MONOCHROME DISPLAY (MAIN MENU SELECTION 6)

When selection number "6" is chosen from the main menu screen, the Monochrome Display test runs by briefly displaying the test name as it tests the controller:



| CPT BAN<br>PA66ED |  |   |
|-------------------|--|---|
|                   |  |   |
|                   |  |   |
|                   |  |   |
|                   |  |   |
|                   |  | , |

CRI ATTRIBUTES

Several lines, each deconstructing one or a coebination of ettributes such as humail video, inverse video, underlining, blinking and high intensity, are displayed.)

<u>CHARACTER SET</u> (A pattern of mil characters shich can be generated in briefly displayed on the screen.) **COLOR/GRAPHICS DISPLAY (MAIN MENU SELECTION 7)** When selection number "7" is chosen from the main menu screen, the Color/Graphics Display test begins by briefly displaying the test name as it tests the controller:



Then, without stopping, the test displays this screen:



Again without stopping, the test displays this screen asking for verification of the attributes shown:

If "N" is entered, an ERROR message is displayed and choice similar to those described in the "Unsuccessful Completion" section of the "Test Completion Displays" section of this chapter are displayed.

If "Y" is entered, the next screen is displayed:

\_\_\_ CHARAGTER SET IA pattorn of oll characters which can be generated is briefly ofsployed on the ecreen.] IE the character set currect? [Y/h]\_ If "N" is entered, an ERROR message is displayed as before. If "Y" is entered, the next screen is displayed:

SCHOL (PST These lings phonic school frue barrow to top any the sides right above this one? [f/k]\_ (The First two links start at the totspe of the screpe ord scrept until they reach the LCP. Then the shirld line it added to the staping.]

If "N" is entered, an ERROR message is displayed as before. If "Y" is entered, the next screen is displayed:



If "N" is entered, an ERROR message is displayed as before. If "Y" is entered, the next screen is displayed:



If "N" is entered, an ERROR message is displayed as before. If "Y" is entered, the next screen is displayed:



If "N" is entered, an ERROR message is displayed as before. If "Y" is entered, the next acreen is displayed:



If "N" is entered, an ERROR message is displayed as before. If "Y" is entered, the next screen is displayed:

> GRAPHICS 60 x 25 [A pettern of all characters which ten be generated is briefly displayed on the arrean.] Is the character set correct? [T/N]\_

If "N" is entered, an ERROR message is displayed as before. If "Y" is entered, the next screen is displayed:



If "N" is entered, the Color/Graphics Display test has been successfully completed and the programm returns to the main menu. If "Y" is entered, additional lines for testing light pen performance are added to the display:

LIGHT PEN Is light pan stiechnd? (Y/N) Piets tight pan ip st center of displayed block and scrivele Light pan. F4=MO REEPONSE FROM PEN. If  $\langle F4 \rangle$  is pressed, an ERROR message is displayed as before.

The PASSED message is displayed upon successful completion of all the tests. Refer to Chapter 5 of this manual for a description or error messages and recommended corrective actiuons.

#### PERIPHERALS (MAIN MENU SELECTION 8)

When selection number "8" is chosen from the main senu screen, the Peripherals Test Menu is displayed:

| The NCR Personal Computer  | Copyright 1985 HCH Corp.  |
|--|---|
| PE   | #EPHERALS   |
| Aveilable Selections   |   |
| 1 Seriel Part Lampback<br>2 Persilel Part Lampback<br>3 Generic Printer<br>4 PC-Competible Printer<br>(F1) Explanation of Selection<br>(F2) Terminate Type Module<br>Press selection and (Enter) f<br>Press selection and (F8) for<br>SELECTION => | - Performs test an internal sarial port<br>- Performe test on internal peralial port<br>- Test ron-Industry standard printer<br>- Test Industry standard printer<br>ne<br>or running test once CR<br>continuous run of selection ( <fa> co etep)</fa> |
| L  |   |

HELP Screen To obtain more information about the available tests, press F1 to see the HELP screen:

```
Serial Port Lappack - Tests the SBF161 port switch on the amin processor
board. Then, tests the port (copbeck, Requires a serial port (copbeck wrap
plug for the port.
Ametial Port Lappack. Tests the perdict port switch on the moin processor
board. Then, tests the port (copbeck, Requires perdict port toppheck
for the port.
Generic Printer - Prints the ASELI character ist on the perdict printer.
PC-Compatible Printer - Prints the ASELI character ist and PC-specific
control codes on a PC-compatible printer.
```

Serial Port Loopback Test (Peripherals Menu Line 1) When selection number "1" is chosen from the peripherals menu, the Serial Port Loopback test is performed. The name of the test is displayed while the test is in progress.

This test requires the use of a serial port Test Adapter <refer to Chapter 1 "Tools") for the test. For test run the Test Adapter (on screen displayed as "wrap plug") has to be plugged into the appropriate port. \_\_\_SERIAL PORT PAEGEO <F2> Terminete Tent Module <F3> Return to previous espu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

**Parallel Port Loopback Test (Peripherals Menu Line 2)** When selection number "2" is chosen from the peripherals menu, the Parallel Port Loopback test is performed. The name of the test is displayed while the test is in progress.

This test requires the use of a parallel port loopback Test Adapter for the test (refer to Chapter 1 "Tools"). For the test run the Test Adapter (on screen displayed as "wrap plug") has to be plugged into the appropriate port. \_\_\_PARALLEL PORT FASSED (F2) Terminols Tast Module (F3) Réturn to previous menu

Upon completion of the test, either the PASSED message is added to the display. Refer to Chapter 10 of this manual for a description of error messages and recommended corrective actions.

**Printer Tests** 

#### NOTE

These tests are for a printer attached to the printer (parallel) port of the computer. Any parallel printer can be tested using the "generic" printer test. Only a printer which is designed to respond to PC-specific control codes for additional functional capability can be tested using the "PC-Compatible" printer test.

Each of the two available tests is selected entering the corresponding number (3 or 4) of the Peripherals Menu. If the ENTER key is pressed after selecting the specific test, the test runs once and stops. If the <F8> key is pressed after selecting a specific test, that test is run continuously until  $\langle F4 \rangle$  is pressed.
When  $\langle F4 \rangle$  is pressed, the program returns to the peripherals menu.

#### Generic Printer Test

If selection "3" of the Peripherals test menu is chosen, the Generic Printer test is run. The name of the test is displayed while the test is in progress:

\_\_GENERIC PRINTER PASSED <F2> Terminale Teb: Modula <F3> Return to previous modu

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to Chapter 5 of this manual for a description of error messages and recommended corrective actions.

## PC Compatible Printer Test

If selection "4" of the Peripherals menu is chosen, the PC Compatible Printer test is run. The name of the test is displayed while the test is in progress:

```
_PC-COMPATIBLE PRINTER
PASEED
(F2) Tarminuts Test Module
(F2) Return to previous sonu
```

Upon completion of the test, either the PASSED message or an ERROR message is added to the display. Refer to chapter 5 of this manual for a description of error messages and recommended corrective action.

#### UTILITIES (MAIN MEND P6 KEY)

When the <F6> key is pressed from the main menu screen, the menu screen for the Utility functions available on the diagnostics diskette is displayed:

```
The NCR Parsonal Cosputar
                                                                   Page 1 of 1
Copyright 1985 WCR Carp.
                               VILLITIES
Available Salections
1 Inisialize Fixed Disk
                                -Propercy Fignd Disk for 005 operations
2 Poettian Head for Relocation -Positions Fixed dids hand for transportation
  Ecror Log Naintenence
3
                                -Provides utilities for error log seintenence
4 Monachrows Allgament
                                -Screens for monochrone display elignment
5 Color Alignment
                                "Screens for color display alignment
                                -Utility for proper disk alignment
6 Diek Alignment
7 Flasible Disk Spand Check
                                -Oleplays disk speed for adjustment
(F3) Astorn to provide menu
                                (Polp) Previoue Page
                                (Pgun) Heat Page
Press selection number and (Enter) to choose from Aveilable Selections.
SELECTION =>
```

The Utilities Menu explains the function of each of the possible utilities that can be selected by typing the corresponding number and pressing the ENTER key. The selected utility runs once and stops.

During its run, appropriate screen and menus are displayed to prompt any actions on the user's part and to explain what operation is being done.

Pressing the <F3> key the program returns to the main menu.

#### LEVEL 1 DIAGNOSTICS

#### VIDEOTEI (BTI) ADAPTER TESTS (Main Menu Selection 11)

This test routine enables the testing of the functional characteristics of

- the modem and video long player (VLP) interfaces (local loop test)
- the modem and VLP interfaces for remote operation (remote loop test)
- EUROM memory/controllers (test pictures)

When selection "11" is chosen from the main menu screen of the LEVEL 1 Diagnostics the BTX (Videotex) test menu screen is displayed:

| The NCR Personal Commputor   | Copyright  | 19 <b>85</b> N | CR Corp.       |
|--|------------|----------------|----------------|
| втх  |            |                |                |
| Availeble Salections   |            |                |                |
| 1, USART: Local Test   |            |                |                |
| 2. Usart: remote Test 12V  |            |                |                |
| 3. Usert: remote Test 5V   |            |                |                |
| 4, Pictures  |            |                |                |
| <f1> Explemetion of Selections<br/><f2> Torminate Test Module</f2></f1>                                      |            |                |                |
| Press selection and (Enter) test once OB<br>Press selection and (FC) for continuous<br>stop]<br>\$8,ECTION=> | run of sel | ectian         | ( <f4> to</f4> |

## Local Test

If selection "1" of the Videotex (BTX) test menu is chosen, the Local Test is run , the following screen is displayed:

| The NCA Personal       | Computer    | BIX | Capyright | 1985 | NCA | Corp |
|------------------------|-------------|-----|-----------|------|-----|------|
| USART Local<br>PASSED  | Test        |     |           |      |     |      |
|                        |             |     |           |      |     |      |
|                        |             |     |           |      |     |      |
| <f2> Terminate te</f2> | st module   |     |           |      |     |      |
| <f3> Asturn to pr</f3> | BVİQUS MƏNU |     |           |      |     |      |

## Remote Test

If selection 1 of the Videotext (BTX) test menu is chosen the following screen is displayed:

| ETX | The NCf | Personal | Computer |     | Copyrigh | t 1985 | NCH Corp |
|-----|---------|----------|----------|-----|----------|--------|----------|
|     |         |          |          | өтх |          |        |          |
|     |         |          |          |     |          |        |          |
|     |         |          |          |     |          |        |          |
|     | _       | _        |          |     |          |        |          |

Remote testing is based on the comparison of characters. As seen on the screen displayed the test can only be carried out using two different turnaround plugs. These plugs are wired as shown in the table below:

| Turnaround     | Turnaround    |
|----------------|---------------|
| Plug 1         | Plug 2        |
| J2-2 -> J2-1   | J2-8 -> J2-9  |
| J2-5 -> J2-3   | J2-5 -> J2-3  |
| J2=6 -> J2=4   | J2-6 -> J2-4  |
| J2-12 -> J2-13 | J2-12 ->J2-13 |
| J2-15 -> J2-14 | J2-15 ->J2-14 |

Before remote loop testing is started you are requested to insert the proper turnaround plug in the modem/VLP interface on the Videotex (BTX) adapter board. After having plugged the turnaround plug the following screen is displayed:

| The NCR Personal Computer   |     | Copyright | 1985 | NCR | Corp. |
|---|-----|-----------|------|-----|-------|
|   | BTX |           |      |     |       |
| USART remota test<br>PASSED   |     |           |      |     |       |
| <f2> Terminate Test Module<br/><f3> Return to previous menu</f3></f2> |     |           |      |     |       |

Picture Test If selection "4" in the Test Menu is chosen, test screen No.1 testing the character and attribute memory is displayed. Test Screen No.1- Testing the character and attribute memory The screen consists of white letters ("U") displayed on a red background; the border color is half intensity green. Errors: characters other than "U" other colors for the foreground/background This test does not permit to test DRCS characters. Press any key to continue. Test Screen No. 2 - Testing the inverse character and attribute memory The screen display shows white "#" characters blinking on a blue background. Errors: characters other than "#" intermittent blinking other colors This test does not permit to test DRCS characters. Press any key to continue. Test Screen No. 3 - DRCS Memory Test A bar pattern is displayed on the screen. The bars are displayed in half-intensity red, black, and white. The border color is half-intensity eyan. Errors: irregularities in the pattern other colors Press any key to continue

LEVEL 1 DIAGNOSTICS

Test Screen No. 4 - Inverse DRCS Character Test

A bar pattern is displayed on the screen. The bars are blue and red. The border color is halfintensity white.

Errors: irregularities in the pattern other colors

Press any key to continue.

Test Screen No. 5 - The screen display consists of a white area enclosed by a black border. The white area designates the area in which characters can be displayed.

This picture is useful for checking the screen setup and the position of the image on the screen.

Press any key to continue.

Test Screen No. 6 - "Video Test" is displayed in yellow letters on a transparent background. The display is positioned in the middle of the screen.

Checks for transparent background when merging input from a video long player.

Press any key to continue ...

Next you are asked whether the screen displays were correct. Enter "Y" and press the(F2)key to return to the main menu.

#### Level | Diagnostics Error Returns

This chapter provides information needed to diagnose and repair the NCR PERSONAL COMPUTER Model PC 41. It is not the intent of this manual to educate the user in all aspects of computer operation. The material presented in this chapter is of a technical nature and the user should possess some experience with digital circuits before attempting any major repairs. However, this material can serve as a valuable tool in determining the most probable cause of system failure.

#### REPAIR GOIDELINES

Before any attempts are made to repair the computer system, some guidelines need to be established. It is considered good practice to reduce the system to a basic configuration before troubleshooting begins. This can help eliminate problems that are not machine dependent but exist in some piece of peripheral equipment. If such a problem is detected, the peripheral equipment may be re-installed a piece at a time (taking care to check the system for proper operation after each installation) until the defective equipment is found.

#### WARNING

Fower should be turned off before removing or installing any system components.

#### SYSTEM CONFIGURATION

A system may have the following configuration:

- Main processor board with controller and memory ICs installed and including configuration switches and connectors.
- Power supply package with appropriate

connectors and outlets.

- One fixed disk drive installed in the computer housing.
- Up to two flexible disk drives installed in the computer housing.
- 5. Fixed disk controller board.
- Controller board for a video display, either color or monochrome, and the cables to connect the unit.

#### PRELIMINARY CHECKS

After the system has been configured for troubleshooting, some checks should be performed:

- Have all peripheral boards not needed for system operation been removed from the computer? Remove any excess equipment.
- Are all cards remaining in the expansion slots correctly installed? Refer to the "After First Attempt" section of the Troubleshooting Non-Operative Systems chapter of this manual.
- 3. Check the switch settings on the main processor board; are they set properly for the system configuration? Refer to Chapter 8 if you need help with the switch settings.
- 4. Check all cables attached to the main processor board and cards in the expansion slots. Make sure that all cables are installed correctly.
- Perform a visible inspection of the circuit components, checking for any obvious signs of damage: bad solder connections, shorts, missing or damaged resistors/capacitors etc.
- Check the voltage levels to the Main Processor Board power connector. The system uses; 12V,-

12V, 5V, -5V and ground. Make sure that all installed peripherals are receiving the correct voltages (this includes the disk drives and video power connectors).

#### MAIN PROCESSOR BOARD COMPONENTS

For those not familiar with the Main Processor Board (MPB), it is recommended that some time should be taken to learn the location of the major board components. Figure 5-1 shows the placement of all the major components used by the NCR Personal Computer on the Alternate (Generic) Main Processor Board, whereas Fig. 5-2 shows the major components on the Early-type Main Processor Board.



Figure 5-1 Alternate\* Main Processor Board

\* Also called "Generic"



Figure 5-2 Early-type Main Processor Board

#### MEMORY ADDRESSING

When using the diagnostic diskette, it is helpful to understand the method of memory addressing used by the 8088 microprocessor. The 8088 divides memory up into segments and allocates 64K bytes to each segment. Using this method of memory segmentation it is possible to address 1 MB of system memory. To address a memory location it is necessary to specify its segment base address and its segment offset value. A typical example of a segmented address specification is shown in Figures 5-3 and 5-4 for the two boards. Both segment code and offset value are given in hexadecimal notation.

To construct the physical memory address it is necessary to place 0 to the right of the last digit in the segment base and add the offset value to form the 20 bit address. Figure 5-4 shows how the example in Figure 5-3 is converted to a 20 bit address. The address formed is the actual physical address of the memory location. A more thorough description of 8088 addressing can be found in one of the reference sources for the Intel 8088 microprocessor.

| nost significant<br>20C2:010F<br>    offset value<br>  segment base |
|---|
| Figure 5-3 Segment Address  |
| 0 added to segment base<br>20020 (segment)<br>+010F (offset)        |
| 20D3F (20 bit physical address)                                     |

Figure 5-4 Physical Address Construction

Figure 5-5 shows the physical locations of the banks of memory ICs on the two main processor boards.

(1)



Figure 5-5 Memory Segment Locations for the Alternate(1) and Early-type M2B (2)

#### ERROR CODES

This section describes the error reports from the NCR Personal Computer SERVICE DIAGNOSTICS program. The reports from the diagnostics are in coded form. This reduces the large overhead of program code required for text. Codes are exemplified in the accompanying text.

# RELATIONSHIP BETWEEN MAJOR CODES, DETAIL CODES AND REPAIR PROCEDURES

Error codes are broken down into MAJOR and DETAIL codes. In addition, ADDR(ess), EXP(ected) DATA, and OBS(erved) DATA information is also displayed where appropriate. The MAJOR codes are, in general, a description of the type of test being performed when an error was encountered. This code is a BCD value that is displayed on the CRT and also presented to the printer data port. Each MAJOR code may have several DETAIL codes associated with it. The DETAIL codes are specific descriptions of the error and are displayed as hex values on the CRT only. Data that is to be returned from a particular register or memory device is displayed in the EXP DATA and OBS DATA fields. If the OBS(erved) DATA does not match the EXP(ected) DATA then an error has been detected. The ADDR(ess) field is for memory related failures.

After every MAJOR CODE the "Repair Procedure" that is recommended to troubleshoot the faulty condition is presented. Additional "Repair Procedures" may follow specific DEVICE codes. On the Main Processor Board (MPB), each semiconductor component is identified by a "U" number. The repair procedures specify components on the MPB by the appropriate "U" number. Figure 5-6 shows the location of components on the MPB by "U" numbers. Equipment recommended for performing repairs includes: Multimeter, Digital Logic Probe.





(2)



Figure 5-6 MPB Component Identification for Alternate (1) and Early-type MPB (2)

#### FORMAT OF DISPLAYED ERROR CODES

FILENAME: MAJOR CODE: DETAIL CODE DATE TIME ADDITIONAL MESSAGE

Examples: FLEX:02:80Date Time This code represents: MAJOR: An error was returned while attempting to read a flexible disk drive. DETAIL: FDC failed to respond, Time out on FDC.

NOTE: FDC is an abbreviation, used frequently in this chapter, for Flexible Disk Controller.

FLEX: 04:00Date Time EXP DATA: AA, OBS DATA: A8

#### LIST OF MAJOR BRROR CODES

Figure 5-7 is a list of the MAJOR CODES that are used and a brief description of each.

| CODE        | DES      | CRIPTION     |           |                  |       |     |
|-------------|----------|--------------|-----------|------------------|-------|-----|
| System Memo | ry Errar | Codes:       |           |                  |       |     |
| MEMORY: 01  | RAM      | PARITY ERROR |           |                  |       |     |
| MEMORY:02   | RAM      | PATTERN EAR  | OR        |                  |       |     |
| MEMORY:03   | RAM      | ADDRESS ERRO | JR        |                  |       |     |
| MEMORY:04   | ROM      | CHECKSUM ERF | AOR       |                  |       |     |
| MEMORY:05   | RAM      | WALKING BIT  | ERROR     |                  |       |     |
|             | Figure   | 5-7 Maipr    | Error Cod | <b></b><br>вб (1 | of 31 | _ ± |

| CODE                          | DESU          | RIPTION               |          |             |        |  |
|-------------------------------|---------------|-----------------------|----------|-------------|--------|--|
| Flexible Dis                  | k Drive       | Error Codes:          |          |             |        |  |
| FLEX:01                       | FDC           | RESET EAROR           |          |             |        |  |
| FLEX:D2                       | FDC           | READ ERROR            |          |             |        |  |
| FLEX;09                       | FDC           | WRITE ERROR           |          |             |        |  |
| FLEX:04                       | FOC           | VERIFY ERROR          |          |             |        |  |
| FLEX:05                       | FDC           | FORMAT ERROR          |          |             |        |  |
| FLEX;06                       | FDC           | DATA ERROR            |          |             |        |  |
| FLEX:107                      | FDC           | reset error           |          |             |        |  |
| Keyboerd Err                  | or Code:      | 1                     |          |             |        |  |
| KEY9AD . 01                   | KEY           | BOARD ERROR           |          |             |        |  |
| Video Nonito                  | r Error       | Codes:                |          |             |        |  |
| MONO/COLOR; E                 | IT CHT        | ERROR                 |          |             |        |  |
| Peripherel E                  | irror Cod     | des:                  |          |             |        |  |
| PERIPH:D1                     | SER           | IAL COMMUNICAT        | IONS ERF | IOR         |        |  |
| PERIPH:02                     | PRI           | NTER PORT ERRO        | A        |             |        |  |
| PERIPH:03                     | PRI           | NTER ERROR            |          |             |        |  |
| Error Codes                   | of Meja       | r System Contr        | ollers:  | •           |        |  |
| CONTROL : D1                  | CPU ERR       | 08                    |          | U6*         | U125** |  |
| CONTROL ; 02                  | INTERRU       | PT CONTROLLER         | ERAOR    | U25         | U132   |  |
| CONTROL:03                    | INTERVA       | L TIMER ERROR         |          | U90         | U120   |  |
| CONTROL : 04                  | DMA CON       | TROLLER EAROR         |          | U91         | U122   |  |
| CONTROL:05                    | CPU CON       | TROLLER ERROR         |          | U7          | -      |  |
| CONTROL : 06                  | SERIAL        | PORT                  |          | U1          | U97    |  |
| CONTROL: 07                   | FLEXIBL       | E DISK CONTROL        | LER      | U <b>46</b> | U25    |  |
| CONTROL:08                    | CHT CON<br>CA | Troller (on ex<br>RD) | PANSION  | -           | -      |  |
| <ul> <li>Component</li> </ul> | t number      | for Alternate         | MPB      |             |        |  |
| ** Component                  | t number      | for Early-typ         | e MPB    |             |        |  |
| -                             |               |                       |          |             |        |  |

Figure 5-7 Mejor Error Codes (2 of 3)

| Error Codes of the Fixed Disk Subsystem<br>FIXED:01 FIXED DISK CONTROLLER<br>(ON EXPANSION BOARD)<br>SIXED:02 FIXED DISK NOT BEADY | FRAOR  |
|--|--------|
| FIXED:01 FIXED DISK CONTROLLER<br>(ON EXPANSION BOARD)<br>FIXED:02 FIXED DISK NOT BEADY  | ERROR  |
| (ON EXPANSION BOARD)<br>SIXED.02 SIXED DISK NOT BEADY  |        |
| STYPD-02 STYPD DISK NOT READY  |        |
|  |        |
| FIXED:03 FIXED DISK RECALIBRATE  | ERROR  |
| FIXED:04 FIXED DISK FORMAT ERRO  | R      |
| FIXED:05 FIXED DISK SEEK ERROR   |        |
| FIXED:08 FIXED DISK WAITE ERROR  | I      |
| FIXED:07 FIXED DISK READ ERROR   |        |
| FIXED:08 FIXED DISK DATA ERROR   |        |
| FIXED:09 FIXED DISK VERIFY ERRO  | A      |
| FIXED:1D FIXED DISK ERBOR [INTE  | ERNAL] |

Figure 5-7 Major Error Codes [3 of 3]

#### DETAIL ERROR CODES AND REPAIR PROCEDURES

Not every MAJOR error code will have associated DETAIL codes. The remainder of this chapter lists the MAJOR codes in sequence, followed by the DETAIL codes (if any) associated with each MAJOR code or group of MAJOR codes, followed by the recommended "Repair Procedures".

#### MEMORY ERROR CODES

#### RAM PARITY TEST

Format of code;

MEMORY:01:ww Date Time ADDR: ssss:0000 Where. ww = Detail code

Where, ww = Detail code ssss = Segment address of error 0000 = Offset address of error Detail code:

- 01 Parity error interrupt
- 02 Base RAM parity error
- 04 Expansion RAM error

These codes are logically "or"ed to present the final detail code.

Example: 03 Parity error & interrupt from the base RAM

or: 04 Parity detected from expansion RAM with no interrupt.

## Repair Procedure 01

The detail code will give the location of a parity failure (base or extended memory). Use the most significant digit of the segment code to identify which bank contains the defective RAM IC. This can be done by using Figure 5-5. Refer also to Figures 5-3 and 5-4 shown earlier in this chapter.

Locate the segment number in the table and find the data bit(s) that failed the RAM PATTERN TEST. The junction of the row and column will intersect the IC that is suspect. The locations of the ICs are found on both MPBs, the Alternate and Early-Type MPB, all other RAMs are located on the Memory Expander Card. Memory bit IC locations on the MPB are shown in Figure 5-8 and Figure 5-9, respectively. If the test indicates a parity failure and the RAM PATTERN TEST passes, then suspect the parity IC within the faulty segment.

|                       | SWITCH & (SWE) CONFIGURATION |                      |                 |                      |                    |                        | DATA BIT POSITION |                 |              |             |                 | PARITY       |                             |  |              |             |
|-----------------------|------------------------------|----------------------|-----------------|----------------------|--------------------|------------------------|-------------------|-----------------|--------------|-------------|-----------------|--------------|-----------------------------|--|--------------|-------------|
| [                     | đ                            | I                    |                 |                      | 2                  |                        | 3                 | ,               | •            | 3           | •               | 1            | •                           | ' י  | 6            | POSITION    |
| Seq:<br>mani<br>Caste | Wernory<br>Mange             | Seg-<br>ment<br>Code | Mamçıy<br>Pange | Seg-<br>meni<br>Coop | малючу<br>Валда    | 5×4-<br>meni<br>2034   | мень-у<br>Папра   |                 |              |             |                 |              |                             |  |              |             |
|                       | бан                          | 8 ; 8                | b¥⊀             | axc                  | 6∸#                | 0000<br>1974<br>3000   | 256×              | UBT             | u%6          | -05         | <b>U</b> 64     | , <b>u</b> u | 540                         | -a.  | 300          | 652         |
| -800                  | ·28#                         |                      |                 | 1300                 | · 28K              | +040<br>'0<br>7300     | 5124              | טיש             | 673          | UM          |                 | υ'n          | Gh                          | 070  | 1.69         | 68          |
| 3000                  | 192 P.                       |                      |                 | 2020<br>30<br>3000   | г<br>1844 н<br>1   | 6000                   | 310K              | 1.85            |              | uns         | viliz           | UBI          | <b>.</b> 60                 | U70  |              | JT          |
| 3020                  | 256K                         |                      |                 | 6000<br>10<br>8000   | вчак               | 9000                   | 64 <b>0</b> K     | 5.04            | נפט          | ¢êų         | เหล่า           | <b>1</b> 90  | u <b>a</b> s                | LIBS   | (087<br>]    | <b>u6</b> 6 |
| Se (                  | GTERS<br>DOD                 | - 4 <b>4</b> e<br>R: | inger y         |                      | 7///               | ///                    | ////              | I               |              |             | [               |              |                             | -  | ] [          |             |
|                       | -00                          | ! ,                  | 204             | 177                  | 1///               | 12                     | XII               | L = 1           | нь           | <b>C</b> 0  | FQ              | 61           | [                           | C0   | 1 188        | -8          |
|                       | \$00°                        | i                    | d-H             |                      | 2474               |                        | $V \square$       | 1               | 147          | G:          | FT              | E:           | 67                          | 7  | 111          | A7          |
|                       | eouo _                       | <u> </u>             | нілн            | VZZ                  | 222                | $\mathbb{C}\mathbb{Z}$ | YZZ               | <u></u>         | <u> </u>     | <u>06</u>   | i 76            | ES           | 26                          | CB   | 96           | - <u>^6</u> |
| Ľ                     | 7560                         | 1 3                  | 124             | 111                  | 111                | <u>~</u>               | X///              | <u></u>         | - HŚ         | QŞ          | - #5            | E.           | 35                          | <u> 65</u>   | <u>ne</u>    | -1          |
|                       | <b>5000</b>                  | <u> </u> 5           | 7EK             | YZZ.                 | 44                 | 11                     | V.:               | <u>, , ,</u>    | 14           | G.          | 24              | E1           | 1 51                        | 1.04   | 1 24         | ~~          |
| · ۱                   | 9000                         | *                    | HICHC           | 1//                  | X////              | Ÿ]]                    | X//               | <sup>22</sup> ( | , н <u>э</u> | പ           | -3              | E3           | D2                          | · C3   | BJ           | ~3          |
|                       | э                            | ,<br>()(F<br>[]]     | 7<br>017<br>0   | د<br>ب               |                    | 2<br>2                 | 3<br>64 2         | G               | <br>9<br>9 C | * _         | 51.5            |              | Ч<br>Ч                      | 28<br>2<br>276<br>356  | ر<br>در<br>ه | •           |
|                       | ı                            | :<br>orr<br>T        | 2<br>ON         | 9<br>238             | ВАЧК<br>1 (<br>0 ( | 2                      | с о               |                 |              | ₹<br>0<br>× | 4<br>256<br>756 |              | 9.40<br>1<br>55<br>55<br>55 | 11 0<br>12 12 0<br>12 12 0<br>12 12 12 12 12 12 12 12 12 12 12 12 12 1 | 0<br>0<br>0  | L<br>L<br>I |
| ł                     |                              |                      |                 |                      | Switz              | Becch                  | A 124441          | C anile         | waid         | n           |                 |              |                             |  |              |             |

Figure 5-8 RAM Memory IC Loactions for Alternate MPB

| Segment | Mema ry |      |            | De           | ate l | Bit         | osi  | tion        |             |             | Perity      |
|---------|---------|------|------------|--------------|-------|-------------|------|-------------|-------------|-------------|-------------|
| Code    | Renge   |      | 7 !        | 6            | 5     | 4           | 3    | 2           | 1           |             | IC POB      |
| adaa    | 64KB    |      | uto        | 020          | 030   | U36         | U44  | U53         | U62         | U71         | UBD         |
| 1000    | 12869   | 0n   | U9         | U19          | U29   | <b>U</b> 34 | U43  | U52         | U61         | 1070        | 179         |
| 2000    | 192KB   | MPB  | UB         | U18          | D58   | <b>U</b> 33 | U42  | U51         | <b>U6</b> 0 | U <b>69</b> | U78         |
| 3000    | 256KB   |      | ហ          | U17          | U27   | 1032        | U41  | U50         | U59         | U6 8        | U77         |
|         | i       |      | <u></u>    |              |       |             |      |             |             |             |             |
| 4000    | 320K8   |      | 1u63       | (US2)        | U51   | U50         | U49  | U4B         | U47         | U46         | U54         |
| 5000    | 384KB   | Dn   | <b>U44</b> | U49          | 042   | U41         | 040  | U39         | <b>U38</b>  | U971        | U <b>45</b> |
| 6000    | 448K8   | MEM, | 1135       | <b>t</b> /34 | U33   | 182         | U31  | <b>U3</b> 0 | U29         | U28         | U96         |
| 7000    | 512KB   | EP.  | 1156       | U25          | υ24   | U23         | N55. | V21         | 020         | U19         | U27         |
| 8000    | 640kb   | CARD | 8U         | 57           | 106   | U5          | 64   | បន          | ue          | uı          | U9          |

Figure 5-9 RAM Memory IC location for Early-type MPB,



Figure 5-10 RAM Memory IC Locations on Expansion Board



Figure 5-12 MPB Memory IC Locations for Early type MPB

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To check errors reported on the Memory Expander Board remove the expander board and set the configuration switches for the maximum memory on the MPB. If the system passes the parity check, the problem is on the Memory Expander Board and diagnostics can proceed (with the expander board reinstalled) as described above. This technique can be used to isolate RAM failures within any segment of memory. Set the configuration switches for the next lower increment and run the diagnostics until the problem is no longer observed or the switches are set for only 256 K of MPB memory, This technique can be used to isolate RAM IC failures within specific segments of memory, Figures 5-11 and 5-12, respectively, show the location of segments on both Main Processor Boards and on the Memory Expansion Board as well. If the diagnostics report a PARITY ERROR after the suspect RAM memory IC has been replaced, then it is necessary to evaluate the hardware associated with parity generation. Figure 5-8 and Figure 5-9, respectively, shows the placement of all major RAM memory hardware components on the Main Processor Board. Check the parity generator for proper input/output conditions.

#### RAM PATTERN TEST

| Format of | code:           |                            |
|-----------|-----------------|----------------------------|
| MEMORY:02 | :ww, Date       | Time                       |
| ADDKR:    | \$9599:0000     |                            |
| EXP DATA: | XXXX, OBS DATA  | : уууу                     |
| Where,    | WW              | = Detail code              |
| Ť         | 3989            | = Segment address of error |
|           | 0000            | = Offset address of error  |
|           | XXXX            | = Expected data            |
|           | УУУУ            | = Observed data            |
| Detail co | de:             |                            |
| 01        | Pattern Erro    | r Verified                 |
| 02        | Intermittent    | Error, Duplicated          |
| 04        | Intermittent    | Error, Not Duplicated      |
| These cod | es are NOT log: | ically "or"ed.             |

#### Repair Procedure 02

The RAM PATTERN TEST writes data patterns into memory and then reads the patterns back to verify that all the data bits in memory are functioning properly. This test will detect the presence of stuck data bits in a RAM or an intermittent bus driver. Comparing the expected data with the observed data will show the defective bit positions. If the segment address changes randomly with repeated execution of the RAM PATTERN TEST, then the problem is also associated with the addressing hardware and the RAM ADDRESS TEST should be used to provide further information. A pattern failure that reports a constant segment address can be checked using Repair Procedure. Intermittent failures that are reported as not Duplicated will require re-use of the RAM PATTERN TEST to determine if there is an actual problem. Intermittent failures reported as Duplicated could be the result of the RAM IC or the data bus to the RAM IC. Figures 5-13 and 5-14, respectively, show the location of RAM memory hardware on the Main Processor Board.



Figure 5-13 RAM Memory Components for Alternate MPB



Figure 5-14 NAM Memory Components for Early-type MBD

#### RAM ADDRESS ERROR TEST

Format of code: MEMORY:03:ww Date Time ADDR: sass:0000 EXP DATA: XXXX, OBS DATA: YYYY = Detail code where. WWW for DETAIL 01. = Segment of expected address XXXX = Segment of observed address уууу for DETAIL 02, sasa = Segment of address or error 0000 = Always zero = Expected address (A8 - A15) XXXX yyyy = Observed address (A8 - A15) for DETAIL 03. ssss = Segment address of error 0000 - Always zero = Expected address (AO - A7) XX = Observed address (AO - A7) у у

#### Detail code:

01 Segment conflict (A16 - 20A)

02 Upper address conflict (A8 - A15)

03 Low address conflict (A0 -A7)

#### Repair Procedure 03

RAM ADDRESS ERROR will return Detail codes that help isolate problems with the system address bus. The ADDRESS PATH is checked in the hardware for integrity along isolated bus components. The ADDRESS PATH is hardware divided as low order address (8 bits), high order address (8 bits), and segment address (4 bits). Each address path has its own data latches and bus drivers. The Detail code returned by the RAM ADDRESS TEST specifies the ADDRESS PATH in the hardware that could be defective. Comparing the expected address with the observed can provide indications of which address line(s) are failing. Evaluation of bus driver input/output characteristics is recommended; suspect intermittent IC failure.

#### RAM WALKING ONES TEST

Format of code:

MEMORY: 05:00 ADDR: ssss:0000 EXP DATA: xxxx, OBS DATA:yyyy

Where, ww = Detail code ssss = Segment address of error cooo = Offset address of error xxxx = Expected data yyyy = Observed

Detail Code: 00 ALWAYS ZERC

#### Repair Procedure 04

The RAM Walking Ones test writes walking one and zero patterns in memory, then reads the patterns back to verify that all data bits are functioning properly. This test is more extensive than the pattern test, with the exception that intermittent errors are not checked.

#### ROM CHECKSUM EAROR TEST

Format of code: MEMORY:04:00 EXP DATA: xx, OBS DATA: yy Where, xx = Expected checksum yy = Calculated checksum Detail code: NONE

## Repair Procedure 05

Make certain that the EPROM BICS IC (shown in Figure 5-14 and 5-15, respectively, is correctly inserted in its socket (U13 (U118). If the ROM CHECKSUM ERROR still occurs replace the EPROM BIOS IC. Should the

ROM test still report a checksum error the problem probably exists in an intermittent address or data bus component that connects the ROM to the 8088 microprocessor.



## Figure 5-15 ROM Hardware Components for Alternate MPB



Figure 5-16 ROM Hardware Components for Early-type MPB

## FLEXIBLE DISK DRIVE ERROR CODES

FLEXIBLE DISK DRIVE ERRORS, FLEX:01 to 07 (Refer to Figure 5-7)

Format of code:

FLEX:aa:ww, Date Time EXP DATA: xx, OBS DATA: yy aa = Major code WW = Detail code xx = Optional expected data yy = Optional observed data

NOTE: The EXP DATA and OBS DATA fields are displayed onmajor code FLEX:06 only.

Detail code:

The following detail codes apply to major codes FLEX:01 through 05 and FLEX:07.

- 88 Disk drive not ready
- 80 FDC failed to respond
- 40 Seek operation failure
- 20 FDC failure
- 10 CRC error on reading
- 09 Attempt to read accros 64k boundry (internal error)
- 08 Data overrun error
- 04 Requested sector not found
- 03 Attempt to write a write protected disk
- 02 Address mark not found on track
- 01 Bad command (internal error)

Accompanying message:

NOTE: This message is not displayed for major code FLEX:06.

DV RTY STO ST1 ST2 TRACK HEAD SECTOR DENSITY xx 
This data is the status returned from the FDC. Refer to Figure 5-17 for status code specifics.

|          |      | DV      | = | Drive m        | umber   |      |       |      |
|----------|------|---------|---|----------------|---------|------|-------|------|
|          |      | RTY     | = | Number o       | of retr | Y    |       |      |
| STATUS ( | CODE | STO     | = | Status i       | registe | n O  | from  | FDC  |
| STATUS ( | CODE | ST1     | = | Status P       | registe | r 1  | from  | FDC  |
| STATUS ( | CODE | ST2     | = | Status I       | registe | r 2  | from  | FDC  |
|          |      | TRACK   | = | Track selected |         |      |       |      |
|          |      | HEAD    | = | Head 1         | (H=O) o | n 2  | seled | sted |
|          |      | SECTOR  | = | Sector :       | selecte | ed . |       |      |
|          |      | DENSITY | = | Sector a       | density | ,    |       |      |

#### Repair Procedure 06

Failure in the Flexible Disk Drive system can be particularly hard to troubleshoot and repair. There is a general approach for solving disk drive problems that is applicable to most failures. Treatment of certain types of failures will follow the specified detail code,

| Brt<br>Number | Statue Jyle Ø  | Status Byte 1   | Ciplus Oyle 3  |  |
|---------------|--|---|--|--|
| 67            | Of and D6 Q<br>Normal Command Termination<br>DF 0 06 1<br>Abnormal Command Termination<br>b1 + 0 06 0              | Logic Level 4 Indicales<br>The FDC Tried to<br>Access a Noneuslem<br>Sector                                 | Nat Used<br>Loge P   |  |
| D4            | In alld Command Issued<br>D7 = 06 1<br>Absormal Termination Peady<br>Lens Changed State During<br>Command Seculion | Nor Usad<br>Logic 0   | Logic 1 During Read<br>by FOC an Deleted<br>Date Address Mark<br>was Encountered |  |
| D3            | Logic 1 Indicates<br>Compresion of the<br>Saes Commend   | FOC Developed #<br>Developed house (CRC)<br>Error II Sel to<br>Logic h                                      | Logic I II CRC<br>Error Faund in<br>Data Field (Bad Dala)                        |  |
| ы             | Set to Logic 1 If Fault<br>Signet Received Floor FOD<br>or Trees CO Not Found<br>After 17 Step Pulses              | Logic 1 Indicates That<br>FDC Not Serviced by Main<br>Systems During Date Transfer<br>and the FDC Timed Out | Logic § If the<br>Cylindau (Track) is<br>Not Found                               |  |
| 63            | Sei la Lagic I II the<br>FDO is Noi Paedy<br>When a ReacyWhie<br>Command is issued                                 | Not Usad<br>Logić G   | Lögic 1 if Scén<br>Commind 1)<br>Seliéfied (Equal)                               |  |
| 01            | Inscatat State of<br>Read/Write Head on<br>FDD at Interrupt  | Logic 1 Molicates No Date<br>Reformed for Sector Not<br>Found or Bed Address 10 Field                       | Lagic I II Scan<br>Command Daes Noi<br>Find the Sector on e<br>Treck             |  |
| 01            | Und Select 1<br>Onvel A DI Q<br>Onvel B OI D   | Logic I II FDD<br>Belwins Wide Protect<br>During e Write<br>Operation                                       | Lope 1 Bad Track<br>Information Desseled   |  |
| 06            | Unit Salact 2<br>Deve A CO 0<br>Deve B CO 1  | Logic 1 If FOC Does<br>Not Find the Address<br>Mark Alter Detecting<br>Index Mark Twos                      | Logic 1 II Bele<br>Address Merk Can Nor<br>Be Found                              |  |

FOC Frendle Disk Controller 200 Frendle Disk Onive

Figure 5-17 Flexible Disk Drive Status Codes



Figure 5-18 Flexible Disk Drive Components

There are two major subassemblies needed for correct disk drive operation. They are: Main Processor Hoard, disk drive unit. Refer to the "System Configuration" section in this chapter before starting the troubleshooting session. Figure 5-18 shows the placement of components on the Flexible Disk Drive. As a general method of isolating flexible disk drive failures the following technique can be used.

Because the computer uses drive A as the primary system drive, a failure of that drive (if there is a disk drive in that position) will prevent the computer from loading the operating system during any boot operation. For systems that have two flexible disk drives, the second drive can be strapped to be drive A. Figure 5-19 shows in detail the header jumper and other strapping blocks found on the disk drive (refer to Figure 5-18 for location). The NCR Personal Computer requires that two straps to be installed. They are the ML and one drive select strap. Disk drives are configured as drive A or B by installing a strap on DSO or DS1, respectively.

NOTE: To change the strapping it is necessary to remove the Flexible Disk Drive from the computer.



Figure 5-19 Disk Drive Strap Locations

To configure a different flexible disk drive to be drive A, remove the strap from DS† and reinstall on DSO. Remove the strap from DSO on drive A; this will prevent addressing conflicts.

NOTE: Do not install two disk drives that are both strapped as system drive A, address bus conflicts will result.

With a different drive reconfigured to operate as drive A attempt to restart the computer system. If the Flexible Disk Drive still fails to respond, the failure is not likely to be in the drive unit. Replace the straps as they were originally and insert the disk drives back into the computer cabinet. If switching the drives solves the problem then the specific detail code may provide information as to the cause of the failure. Should it be necessary to return the disk drive unit to a service center for repairs the operational disk drive (configured as drive A) can be used to provide a working computer system.

A terminating resistor pack must be installed on the disk drive attached to the last connected ribbon cable position. Figure 5-20 illustrates the ribbon cable; the location of the terminating resistor pack is shown in Figure 5-18. If only one flexible disk drive is installed, the resistor pack must be inserted in that drive. For systems with two flexible disk drives, only one resistor pack is used, i.e., on the drive at the end of the cable.

#### CAUTION

Improper installation of the resistor pack will cause unexpected results in the operation of the disk drive.


Figure 5-20 Ribbon Cable Termination



Figure 5-21 Flexible Disk Related Hardware, Alternate MPB



Figure 5-22 Flexible Disk Related Hardware, Early-type MPB

For more detailed information on troubleshooting the Flexible Disk Drive subassembly, refer to the specific detail code that was reported by system diagnostics.

DETAIL CODE 88

Suspect failure of disk drive unit. Refer to "Repair Procedure 05".

DETAIL CODE 80

Suspect intermittent failure of Flexible Disk Controller Card. Check control lines at the expansion slot. Recommend replacement of FDC.

DETAIL CODE 40

Suspect disk drive failure. Recommend Repair Procedure. DETAIL CODE 20 See device code 80.

# DETAIL CODE 10

Suspect disk drive unit out of alignment or defective. Perform data read/write test on alternate Flexible Disk Drive (if installed) to verify condition. If both drives fail CRC check then suspect the flexible disk drive controller.

DETAIL CODE 09

DMA segment addressing error, probably the result of a diagnostic software error. Suspect that the diagnostic disk is defective.

# DETAIL CODE 08

Suspect Main Processor Board Failure. Check DMA channel 2 (DRQ 2) for intermittent operation.

DETAIL CODE 04

Suspect disk drive failure. Check index sensor on drive (Figure 5-18) shows the location) for obstruction. Refer to Repair Procedure for verifying drive failure.

DETAIL CODE 03

Suspect Defective write protect sensor on the disk drive unit (Figure 5-18) shows the location). Check sensor for obstruction. You can measure resistance from write protect sensor (photo resistor) with diskette installed that is write protected and then not write protected. Resistance should change from high to low if the sensor is operational. Refer to Repair Procedure.

DETAIL CODE 02

Suspect defective drive; refer to Repair Procedure. If the drive is not defective then the problem is probably a bad flexible disk controller.

DETAIL CODE 01 Software error. Reboot system and perform test again. Suspect bad diagnostics program disk.

## KETBOARD ERROR CODES

### **KEYBOARD ERROR TEST**

Format of code:

- ERROR:01:ww Date Time EXP DATA:xx, OBS DATA: yy
- Where, ww = Detail code xx = Expected data from keyboard port yy = Observed data from keyboard port
- Detail codes:
  - 01 Keyboard code will not reset 02 No interrupt on keyboard
  - 03 Bad data on loopback of keyboard port
  - 04 Bad data from keyboard

## Repair Procedure 07

Check that the keyboard is connected properly to the main processor board connector. The pins of the keyboard connector should be checked for damage.

DETAIL CODE 01

Suspect that the CPU controller (U7 on the MPB) is defective.

# DETAIL CODE 02

The keyboard connector J3 should be checked for power to the keyboard. Pin 5 should read 5V and pin 4 should read 0V (ground). If the keyboard is receiving power the keyboard is either not sending data to the main processor board or the data path contains a defective component. Check the EXPand OBS DATA fields. If the OBS DATA returns a code of 55 the keyboard is operational and the data path (to U7) is not

defective. Figures 5-23 and 5-24, respectively, shows the main processor board components that comprises the keyboard interrupt signal path.

# DETAIL CODES 03 and 04

These codes indicate an internal keyboard failure of the CPU controller. Check inputs to U7 (U125) and proper operation of the keybaord clock.



Figure 5-23 Keyboard Related Hardware, Alternate MPB



Figure 5-24 Keyboard Related Hardware, Early-type MPB

### VIDEO DISPLAY ERROR CODES

### CRT ERROR TEST

Format of code:

MONO:01:ww Date Time EXP DATA: xx, OBS DATA: yy

#### OR

COLOR:01: ww Date Time EXP DATA: xx, OBS DATA: yy Where, ww = Detail code xx = Expected data from register

# yy = Observed data from register

## DETAIL code:

Monochrome crtc register access error 01 02 Monochrone erte cursor error 03 Monochrome crtc character set error 04 Monochrome crtc attribute error 06 Monochrome crtc scroll error 11 Color crtc register access error Color crtc cursor error 12 13 Color orte 80x25 character set error 14 Color crtc attribute error Color 640x25 mode error 15 16 Color crtc scroll error Color 320x200 graphics error - color set 0 20 21 Color 320x200 graphics error - color set 1 Color 640x300 graphics error 22 Color light pen test aborted (timeout) 30 Color light pen address failure 31

# Repair Procedure 08

Detail codes 01 through 06 refer to the errors that occur on systems that use a monochrome display. Detail codes 11 through 22 are associated with systems that use a color video display.

If you have another video display controller card, try to install it. Should this not solve the problem, remove the controller card from its expansion slot location and re-install it in another expansion slot location. This will correct a problem that is the result of a bad expansion slot connector. If the video display is operational and will perform some functions (such as inverting the display) then the character generator ROM on the controller card is probably defective. Errors that are reported for the monochrome/color video display are usually the result of a bad controller card. To check the possibility of a bad data path to the controller card from the Main Processor Board, a memory expander card can be placed into the expansion slot occupied by the video controller card and the diagnostics test for RAM failures can be executed. To use this technique a BCD display must be installed for error message output. or output should be redirected to a printer.

If no error messages are generated, the data path to the expansion slot is functional. If the video display is operational, but the display does not stabilize, perform the necessary adjustments to the video display unit.

#### WARNING

The video display adapter/controller board contains HIGH VOLTAGE elements.

If the problem is the result of a bad video display unit and not the digital circuitry, check for the proper voltages to the analog video circuits. If the display is out of alignment, refer to the Chapter 7 "Adjustments" of this manual for more information.

- DETAIL CODES 01, 02, 06, 11, 12 Suspect a defective CRT Controller card, probably the CRT Controller chip.
- DETAIL CODE 03, 13

Suspect defective character generator ROM on the CRT controller card or defective character code RAM. Perform CRT RAM test to verify CRT character code integrity.

- DETAIL CODE 04, 14 Recommend use of the CRT RAM Error diagnostics test to obeck for possible defective attribute RAM IC.
- DETAIL CODE 20, 21 Check the color adjustment to the CRT.

# CRT RAM ERROR TEST

8Format of code: MONO:02:ww Date Time ADDR: ssss: oooo, EXP DATA: xxxx; OBS DATA: yyyy OR COLOR:02:ww Date Time

| ADDR : | 5355: 0 | 0000 | , EXF   | DATA:   | xxxx,    | OBS | DATA: | уууу |
|--------|---------|------|---------|---------|----------|-----|-------|------|
| Phere, | WW      | = D  | etail d | eode    |          |     |       |      |
|        | 3533    | = S  | egment  | address | of erro  | r   |       |      |
|        | 0000    | = 0. | ffset a | lddress | of error | •   |       |      |
|        | XXXX    | = E  | xpected | data    |          |     |       |      |
|        | УУУУ    | = 0  | bserved | l data  |          |     |       |      |

## Detail codes:

- 01 Monochrome pattern error
- 02 Monochrome address error
- 11 Color pattern error
- 12 Color address error

## Bepair Procedure 09

Refer to Repair Procedure 08. Use the memory expansion board to test the CRT controller card data path. If the memory expansion board produces a RAM failure error, the hardware data path to the expansion slot is suspected to have failed. Otherwise suspect a defective RAM IC on the CRT controller card.

# DETAIL CODE 01 and 11

These codes refer to the monochrome and color units, respectively. They indicate an intermittent failure of a line driver to the RAM IC or a stuck data bit within the RAM. If the flexible disk drive controller or the memory expander card (if installed) do not produce an error in the diagnostics test the CRT RAM IC is most likely the cause of the error condition.

## DETAIL CODE 02 and 12

Suspect intermittent failure of an address latch. Check the address bus to the main Processor Board expansion slot for proper operation (particular attention should be given to address lines 18 and 19). The failure is probably the result of a defective component on the CRT controller card.

### PERIPHERAL BEROR CODES

SERIAL COMMUNICATIONS ERROR TEST

Format of code:

- PERIPH:01:ww Date Time EXP DATA :xx, OBS DATA: yy
- Where, ww = Detail code xx = Optional expected data yy = Optional observed data
- NOTE: EXP DATA and OBS DATA are in reference to the registers in error. If a register is not in question these fields are not displayed.

Detail Code:

The detail code is a bit sensitive code. It is displayed on the CRT as a hex code with bit 7 as the MSB and bit 0 as the LSB. An "x" represents the "Don't Care State". The detail codes are to construct the error code for all serial communication failures. Block 1 codes represent failures of the 8250 serial communications chip or the line drivers used to transmit or receive data from the serial (RS 232) port: To form the error code the detail codes for block 1 diagnostics are logically "or" ed. Observe that the following bit designation is used.

| bits | 0,1 | communications channel selected  |
|------|-----|----------------------------------|
| bits | 2,3 | error during transmit or receive |
| bits | 4   | internal/external loop back test |
| bits | 5,6 | control/status errors            |
| bita | 7   | time out or receive data error   |

NOTE: See Figure 5-25 for status/control definitions

|              | (1.5B)                |                       |                   |                      | 1                  |                |                  | (MAB)         |
|--------------|-----------------------|-----------------------|-------------------|----------------------|--------------------|----------------|------------------|---------------|
|              | Bit =0                | Bit #1                | B-1 #2            |                      | ∟ ≞                | Bil #5         | Brt #5           | 64 #7         |
| Реснуе       | Oate Bit              | Raiz Bri              | Qata Bil          | Data Bil             | <br>  Claba Bil    | Dala Bri       | Llaua B-t        | )<br>Cala Bil |
| Fieg Ster    | \$                    |                       | 2                 | د                    | 6                  | 5              | 6                | 7             |
| Pead Only    |                       |                       |                   |                      |                    |                |                  |               |
| 1/gnpmir     | Cata Bic              | Data B-I              | Data Bil          | Data Bil             | Oata Bil           | Data Br        | Date Bri         | Data Bu       |
| Register     | c                     |                       | 2                 | 3                    | · ·                | 5              | 6                | 7             |
| (Write Only) |                       |                       | İ                 |                      |                    |                |                  | 1             |
| -17E         | Logici<br>When        | Direirun<br>Eriar     | Data<br>  Parily  | Logis 1<br>Indicates | Break<br>Internapi | T/ansmi<br>How | Transmit<br>Shut | · ·           |
| Status       | Dala 1<br>Bason       |                       | Error<br>When Set | No Stop<br>Ref Form  | l · ···            | Register       | Register<br>Frac | r<br>I        |
| Верыен       | 1                     |                       | 10 Logic 1        | 0.2 0.               | l                  | Flag           |                  |               |
| Vodem        | Rel etts              | Renecis               | Jse <sup>1</sup>  | User                 | Logic 1            |                |                  | -             |
| Control      | OI DTR                | M 915                 | Oulput            | Quipat               | Injengi            |                |                  |               |
| Régister     | 1.08                  | Line                  |                   |                      | Oragenostics       |                |                  |               |
| Modern       | Defines               | Releats               | Ring              | Receive              | Comparent          | Complement     | Complement       | Complement    |
| Status       | CT5                   | Comrol                | Convol            | Senal                | Inpul              | Input          | Indicator        | Line Signal   |
| Register     | Signa                 | Stalus                | Sialos            | Status               | - Signa'<br>       | Signal         | INDOL            | 1°PL-         |
| Imerrual     | Lugic 1               | Lagici                | Lonic             | Logic 1              |                    |                |                  |               |
| Enable       | Oate                  | Transmit              | Receive           | Modem                |                    | 1              |                  |               |
| Reg ster     | Available<br>niethipi | Register<br>Empty all | Interrupt         | Status<br>herrapi    |                    | r              |                  |               |
| Interrupt    | Logic                 | Interrupt             | Interrupt         |                      |                    |                |                  |               |
| ·0           | ior<br>Johef Lapt     | 5                     | 1 1               |                      |                    |                |                  |               |
| Register     | Pending               |                       |                   |                      |                    |                | r                |               |

Figure 5-25 Serial Communications Status (1 of 2)

5-39

| BLOCK I                           | Bit | ,  | ŀ | 4 | 4 | 1 | 2 | 1 | 0   |
|-----------------------------------|-----|----|---|---|---|---|---|---|-----|
| COM1 channel inflection           |     | •  |   | x | L | • |   | â | 1   |
| Error on channel during transmit  |     |    |   |   | L | • |   |   | z I |
| Error on channel during receive   |     |    |   |   | Ŧ | 1 | 0 |   | 5   |
| Channel loop back internal        |     |    | з |   | 1 |   |   |   | 5   |
| Channel loop back externel        |     |    |   |   | ۰ | 1 | ۹ |   |     |
| LOOP MODEM CONTROL to status ever |     | 0  | • | Ð |   | × |   | z |     |
| MODEM STATUS will not repart      |     |    | • |   |   | 2 |   | x |     |
| MODEM STATUS with NOL MI          |     | 0  |   | D |   |   | h | 4 | ۹.  |
| LINE STATUS error                 |     | 0  |   | 1 |   |   |   | 4 |     |
| Channel lime out error            |     | ٦. | 0 | ۰ |   |   |   |   |     |
| Receive data error                |     | •  | ٥ | 1 | • | • | L | • | L   |

| BLOCK 2  |   |   |    |    |    |    |   |     |
|--|---|---|----|----|----|----|---|-----|
| Ossail codes essociated with block 2 are indicative of serial communications<br>interrupt fedures or a fedure to select the serial communications chip completely. |   |   |    |    |    |    |   |     |
| Channel interrupt enter on wrong channel.  |   |   |    |    |    |    |   |     |
| Interrupting channel indicated.  |   | 1 | 1  | D. | Ο. | •  | • | •   |
| eg.  |   |   |    |    |    |    |   |     |
| No COM Internet  |   | ٦ |    | ۵  | ÷. | ۵  | 4 | 0   |
| Chennel Interrupt on CDM1  | • |   |    |    | D  | ė. | 0 | 1   |
| Channel Marilupt on COM2   | 1 |   |    | ۰  | 0  | ٥  |   | •   |
| Bad INTERAUPT ID status before interrupt   | 1 |   | ۰. | •  | 0  |    |   | ĸ   |
| Bad INTERRUPT ICI status aller interrupt   | 1 |   | ۰. | 0  | 1  | e. |   | • 1 |
| on COMI  |   |   |    |    | 0  |    |   |     |
| Select ener COM  | 1 | 1 | 1  | ٩  | 1  | •  | 0 | 1   |

Figure 5-26 Serial Communications Status (2 of 2)

# Repair Procedure 10

Before starting to troubleshoot the communications channel it is recommended that the system configuration switches may be checked for the proper setting. It is also necessary to install a wrap plug or Turnaround Test Plug (Part No. 017-0044311) for the RS-232 port. If you do not have such a Test Plug construct one using an RS-232 female connector with the pins jumpered as in Figure 10-27. If you have not already done so, refer to the "System Configuration" section of this chapter. Most of the bit sensitive codes that are specified in the error message refer to the failure of the 8250 communications IC or one of the associated line drivers. This test can be used to determine a peripheral device (modem, printer, etc.) connected to the serial (RSA-232) port is defective. Figure 5-25 shows the location of hardware used by the serial communications port. To test the data bus to the 8250 serial communications IC a printer can be attached to the computer's printer

port (J2) and diagnostics check run on the printer. If the printer fails the problem is probably an intermittent bus driver on the data bus. This test can be used since both printer and serial port use the data bus.

NOTE: Remove any equipment attached to the RS-232 port and install the loop-back connector BEFORE running the diagnostics test.

> Connect:2 to 3 4 to 5 to 8 5 to 80 to 22



Figure 5-27 Turnaround Test Adapter (Soldering Side)

DETAIL CODE (BLOCK 1)

If an internal error is returned by the detail code (bit 4 at logic 1) then suspect a failure of the 8250 serial communications IC. Errors that occur during external loop back checks require that the line drivers from/to the 8250 be checked fo proper operation. Figure 5-23 provides more information for determining the actual cause of an internal failure.

# DETAIL CODE (BLOCK 2)

The serial interrupt can be detected as the output of the tri-buffer U23 (U12). It is routed to the interrupt controller IC through switch block A on the main processor board. For interrupt failures check that the system switches are set for serial port enabled. If the switch is properly set, proceed from the output of U23

(U12) (pin 8) to determine if the interrupt signal is operational (refer to Figures 5-26 and 5-27, respectively)



Figure 5-28 Serial Communications Port Hardware for Alternate MPB



Figure 5-29 Serial Communications Port Hardware, Early-type MPB

# PRINTER (PARALLEL) PORT ERROR TEST

Format of code: PERIPH:02:ww Date Time EXP DATA: xx, OBS DATA: yy ww = Detail code Where. xx = Expected data vv = Observed data Detail code: Printer data port error 01 02 Printer control port error Printer status port error 03 04 No interrupt on printer port

# Repair Procedure 11

To perform the diagnostics test on the printer port it is necessary to install the Turnaround Test Plug, Part No. 017-004431. If such a wrap plug is not available it is necessary to construct a wrap plug according to Figure 5-30. The location of hardware components used by the printer port is shown in Figures 5-31 and 5-32,



Figure 5-30 Printer Port Loop Back Connector (Front Side)

# DETAIL CODE 01

If the printer data path is defective the diagnostics test with loop back connector installed will show the failure (invalid data) in the EXP DATA and OBS DATA fields. For valid data the expected and observed fields will be the same. Should the test fail then the problem is resident on the main processor board.



Figure 5-31 Printer Port Hardware Components Alternate MPB



Figure 5-32 Printer Port Hardware Components Early-type MPB

To check the integrity of the data bus used to communicate with the printer port a loop back connector can be placed on the RS 232 SERIAL PORT (J10) and diagnostics performed on the serial port. If the serial port also fails the diagnostics test the problem is a bad bipolar ROM (U9, which generates chip selects for serial and printer ports) or a defective component on the data bus.

# DETAIL CODE 02 and 03

Printer control and status is a Main Processor Board Fallure and requires the analysis of printer port hardware. The problem might be a defective data bus line driver or data latch. Suspect the C data bus routed from the 8255

((PIO, U35 (U46)) through line driver U61 (U47) and data latch U69 (U56) to the printer port. Refer to Detail Code 01.

# DETAIL CODE 04

Suspect Interrupt #7 not operational at interrupt controller ((8259, U25 (U132)). Check the operation of -ACK from the printer port (J2 pin 5). Check U23 (U12\*) (tri-state buffer for interrupt 7) for proper operation.

# PRINTER ERROR TEST

Format of code;

PERIPH:03:ww Date Time

Where, ww = Detail code

Detail code: D0 = 1 : Printer time out D3 = 1 : I/O error D4 = 1 : Printer select D5 = 1 : Out of paper D6 = 0 : Ack (internal) D7 = 0 : Printer busy

# Repair Procedure 12

Refer to previous Repair Procedure 11. Check the installation of the printer; suspect bad cable connection or printer switch settings.

# MAJOR SYSTEM CONTROLLER ERROR CODES

Diagnostic Error Codes 01, 03, 04, 05, 06, 07 are associated with failures on the main processor board.

To analyze the error condition requires the use of an oscilloscope. However, some of the components are installed in sockets and can be removed. Replacement of defective socketted components may alleviate the error condition.

### CAUTION

Care should be used in removing any socketted IC component or damage to the component/socket may result.

# CPU ERROR (8088)

Format of code:

CONTROL:01:00 Date Time

Detail code: NONE

## Repair Procedure 13

Check the B088 U6 (U125) for correct installation. Determine if the ready line of U6 (U125) is active; this will suspend operation of the microprocessor. If this condition is observed then check RDY and DMA wait inputs into the CPU VLSI (U7/125). If one of these two control lines is not active then suspect that the clock IC (U7) or the processor (U6)(U125) is defective.

NOTE: A control line attached to a defective input that is also attached to the inputs of other gates can produce unpredictable results and conditions that are difficult to troubleshoot.

## INTERRUPT CONTROLLER (8259) TEST

Format of code:

CONTROL:02:www Date Time EXP DATA: xx, OBS DATA: yy

| Where, |       | ww = Detail code |           |       |      |          |
|--------|-------|------------------|-----------|-------|------|----------|
|        |       | xx =             | Expected  | data  | from | register |
|        |       | уу =             | Observed  | data  | from | regiser  |
| Detail | code: |                  |           |       |      |          |
| 10     |       | Failed           | read/writ | te to | mask | register |

## Repair Procedure 14

Check the bus connections to the 8259 Interrupt Controller IC.

## DETAIL CODE 10

Suspect 8259 (U25)(U132\*) data bus to processor connection; check data bus. The 8259 may be internally damaged resulting in intermittent failure; replace U25 (U132\*).

## INTERVAL TIMER (8253) TEST

Format of code:

CONTROL:03:ww Date Time EXP DATA: XXXX, OBS DATA: YYYY

Where, ww = Detail code xxxx = Expected data from register yyyy = Observed data from register

### Detail code:

1x Failed counter read/write test x = counter 2 2x Timer too fast x = counter 0,1,2 3x Timer too slow x = counter 0,1,2 5x No output from timer 0 x = counter 1,2

# Repair Procedure 15

# DETAIL CODE 1x Suspect intermittent failure of the 8253 timer

IC U30 (U120). Recommend check of the B data bus for proper operation.

DETAIL CODE 2x, 3x, 5x Check the clock inputs to the 8253 U30 (U120) Suspect divide by 2 flip flop and the timer chip (U30)(120).

#### DMA CONTROLLER (8237) TEST

Format of code; for Detail 1x Time CONTROL:04:1x Date EXP DATA: xx, OBS DATA: yy Where. xx = Expected data from register yy = Observed data from register for DETAIL 70, CONTROL:04:70 Date Time EXP DATA: xxxx, OBS DATA: yyyy xxxx = expected segment Where. yyyy = observed segment Detail code: DMA controller failed internal register 1x read/write test x = register 2 through 7 70 DMA failed addressing test.

# Repair Procedure 16

DETAIL CODE 1x Suspect intermittent failure of 8237 U31(U122).

# DETAIL CODE 70

DMA failed addressing test. Evaluate the EXP and OBS DATA fields to determine if the address failed along an eight bit address path. If the lower address bus (bits 0 - 7) has been altered then suspect line driver U32 (U75). For alterations of the high address bus (bits 8 -15) suspect address latch U12 (U126). If Detail Code 70 is returned and the EXP and OBS data fields match then suspect U11.

# CPU CONTROLLER ERROR TEST (VLSI DEVICE)

Format of code:

CONTROL:05:ww Date Time EXP DATA: xx, OBS DATA: yy ww = Detail code xx = Expected data from port yy = Observed data from port

DETAIL code: 11 Port failed read/write test to PIO control register (Port B).

# Repair Procedure 17

The system PIO uses port B for the following control functions: control the speaker on the Main Processor Board, enable RAM parity, read the sense switches, and keyboard control.

Because port B is used to control several different devices, the diagnostics test for those devices should return an error if there is a failure on one of its control lines. Run RAM PARITY TEST and KEYBOARD TEST using the diagnostics. To test the speaker depress any printing character key on the keyboard and hold. When the end of line is reached the speaker will click repeatedly until the key is released. The speaker should emit a constant tone. Intermittent failure is indicated by a random break in the tone. All of these tests should be run repeatedly to check for intermittent component

failure. A failure detected by using this technique can isolate which line on the PIO is failing. Check CPU Controller.

SERIAL PORT (8250) TEST FAILED LINE CONTROL REGISTER READ/WRITE TEST Format of code: CONTROL:06:ww Time Date EXP DATA: xx, OBS DATA: yy Where, ww = Detail code xx = expected data from register yy = observed data from register Detail code: 01 Read/write to divisor latch high 02 Read/write to divisor latch low 03 Read/write to line control register

04 Read/write to modem control register

## Repair Procedure 18

Suspect defective 8250 (U1, U37) or intermittent failure of data bus. Perform the PRINTER PORT TEST. The printer uses the C data bus to communicate with the microprocessor. If the printer port passes (several runs of the test) suspect the 8250 (U1)(U37) serial communications IC. A failure reported by the Printer Port Test indicates a failure of the data bus; check line driver U43 (U94) for intermittent failure. Refer to "Repair Procedure 07" for troubleshooting the serial communications port.

# PLEXIBLE DISK CONTROLLER (8272) TEST

Format of code:

CONTROL:07:ww Date Time EXP DATA: xx, OBS DATA: yy

Where,

- ww = DETAIL code
  - xx = Expected data from register
  - yy = Observed data from register

Detail code:

- 10 No ready status from controller
- 11 Bad status returned after invalid command sent
- 12 Incorrect STATUS 0 returned from controller after invalid command sent

# Repair Procedure 19

Suspect defective flexible disk drive controller IC. Refer to "Repair Procedure 08" for more information on troubleshooting flexible disk drives.

# CRT CONTROLLER (6845) TEST

Format of code:

CONTROL:08:ww Date Time EXP DATA: xxxx, OBS DATA: yyyy

Where, ww = Detail code xxxx = Expected data from register yyyy = Observed data from register

Detail code:

| 0E | Cursor address register read/write error<br>- 14 (mono)  |
|----|--|
| OF | Cursor address register<br>read/write error - 15 (mono)  |
| 1E | Cursor address register read/write<br>error - 14 (color) |
| 1F | Cursor address register read/write error<br>- 15 (color) |

# Repair Procedure 20

Suspect defective 6845 CRT Controller chip. Replace the 6845 Controller IC or the CRT Controller Card.

Refer to "Repair Procedure OS" for more information on troubleshooting Video (CRT) systems.

## FIXED DISK CONTROLLER ERROR CODES

# FIXED DISK CONTROLLER ERROR TEST (ON EXPANSION BOARD)

Format of code:

| FIXED:01:ww | Date  | Time   |       |
|-------------|-------|--------|-------|
| HEAD        | TRACK | SECTOR | CYCLE |
| XX          | XXXX  | XX     | XXXX  |

Where, ww = Detail code xx = location on disk related to error

Detail code:

| 01 | Command error (internal)        |
|----|---------------------------------|
| 02 | Address error                   |
| 04 | Sector error                    |
| 05 | Reset error                     |
| 07 | Initialization error            |
| 09 | DMA across 64K error (internal) |
| 0B | Bad track                       |
| 10 | Uncorrectable data error        |
| 11 | ECC soft error                  |
| 20 | Controller error                |
| 40 | Seek error                      |
| 80 | Timeout error                   |
| BB | Undefined error (internal)      |
| FF | Read status failed              |

## Repair Procedure 21

Check the power and ribbon cable connectors to the fixed disk drive. Re-install the fixed disk controller card in a different Expansion slot. Perform diagnostics test on the flexible disk drive and the CRT unit. If both pass the diagnostic test then the problem is a defective fixed Disk or fixed disk controller card.

Some error conditions can be corrected by reformatting the fixed disk. If the fixed disk drive is reformatted all data previously stored on the drive is lost.

# FIXED DISK ERROR TEST

(Refer to Figure 5-7)

Format of code:

| FIXED:a | a;ww  | Date   | Time  |  |  |
|---------|-------|--------|-------|--|--|
| HEAD    | TRACK | SECTOR | CYCLE |  |  |
| XX      | XXXX  | XX     | XXXX  |  |  |

NOTE: The additional messages do not appear for all error codes.

| Where, | aa | Ξ | Major code       |         |          |
|--------|----|---|------------------|---------|----------|
|        | WW | = | Detail code      |         |          |
|        | XX | = | location on disk | related | to error |

### Detail code:

- 01 Command error (internal)
- 02 Address error
- 04 Sector error
- 05 Reset error
- 07 Initialization error
- 09 DMA across 64K error (internal)
- OB Bad track
- 10 Uncorrectable data error
- 11 ECC soft error
- 20 Controller error
- 40 Seek error
- 80 Timeout error
- BB Undefined error (internal)
- FF Read status failed

# Repair Procedure 22

Major error codes FIXED:02 through FIXED:08 indicate failures that occur in the fixed disk drive assembly. (For failure of the fixed disk controller, a major error code of FIXED:01 is returned.) To verify the

operation of the main processor board bus signals insert another controller card (CRT, memory expander) in the expansion slot occupied by the fixed disk controller card and perform diagnostics tests for the newly installed card. If the diagnostics tests pass then the problem resides on the fixed disk controller card or the fixed disk drive subassembly. Check the fixed disk drive for proper installation and strapping.

DETAIL CODE 01

An invalid command was issued to the fixed disk. Check the cable connections to the drive unit. Suspect failure of the fixed disk controller card.

### DETAIL CODE 02

Probable failure of the fixed disk controller card.

DETAIL CODE 04

Suspect failure of the fixed disk drive. Reformatting the drive might correct the condition.

## DETAIL CODE 05

Suspect failure of the fixed disk drive. Refer to "Repair Procedure 22" for checking the RESET signal to the expansion slot.

DETAIL CODE 07

Suspect failure of the fixed disk controller card.

## DETAIL CODE 09

Software failure. Repeat the test; if the test fails again, try another diagnostics diskette.

DETAIL CODE OB

Suspect damage to fixed disk drive. Attempt to reformat the drive unit.

DETAIL CODE 10 Refer to DETAIL CODE OB.

### DETAIL CODE 11

Refer to DETAIL CODE OB.

# DETAIL CODE 20

The fixed disk controller card failed while the diagnostics were testing the drive unit. Suspect defective fixed disk controller card.

### DETAIL CODE 40

Refer to DETAIL CODE OB.

# DETAIL CODE 80

The fixed disk drive failed to respond to the fixed disk controller. Check the drive for correct installation.

# DETAIL CODE BB

Perform the diagnostics test again. Suspect failure of fixed disk controller card or the diagnostics test diskette.

#### DETAIL CODE FF

Suspect failure of the fixed disk controller card.

### VIDEOTEX ERROR CODES

### MODEM INTERFACE ERROR TEST

\*\*\* ERR: 26:ww

## VLP INTERPACE ERROR TEST \*\*\* ERR: 26:ww

Where, ww = Detail Code

# Repair Procedure 23 and 24

If detail codes 01 and 02 are returned, the Videotex controller board is defective. A new board must be installed.

## Repair Procedure 25

Check for the installation of the Turnaround Test Plug (Part No. 017-0044311). If the error persists, a new Videotex controller board must be installed.

TEST PICTURE ERROR

\*\*\* ERR: 26:ww

Where, ww = DETAIL code

## Repair Procedure 26

If detail code 10 is returned, the EUROM, Videotex controller board or the switchboard may be defective.

## Removal/Replacement

This chapter describes how to remove and replace the major assemblies of the NCR Personal Computer PC 41.

The description includes all assemblies that are used in the computer, therefore be sure that the correct procedure is used since not all features are in every system.

In some of the figures assemblies are not shown for reasons of clarity. It is only necessary to remove what is described in the text.

When a complete assembly is replaced, contact your supplier or NCR office for information concerning the possible repair of the defective assembly.

#### WARNING

Disconnect the power cord before removing the cabinet, Dangerous voltages are present when working with the covers removed. Only trained engineers should attempt to work on the computer with the covers removed.

Do not leave the computer unattended with power applied and the covers removed.

### CABINET

To remove the cabinet, refer to Figure 6-1 and use the following procedure:

- Be sure that the ON/OFF switch is in the OFF position
- 2. Disconnect the power cord (A).

# REMOVAL/BEPLACEMENT

3. Remove the two retaining screws (B).

Slide the cabinet top backwards and upwards.

To re-assemble, use the above procedure in the reverse order.



Figure 5-1 Cabinet Removal

## CARD RACK ASSEMBLY

To remove the card rack assembly, refer to Figure 6-2 and use the following procedure.

- Be sure that the ON/Off switch is in the OFF position.
- Make a note of their positions and disconnect the cables from the back of the card rack assembly.



Figure 6-2 Card Rack Removal

- Turn the securing screw (A) a quarter turn counter-clockwise.
- 4. Pull out the card rack assembly.

### NOTE

The cable connecting the disk drives to the card rack assembly is long enough to allow complete withdrawal of the assembly and further work on the assembly.

5. To completely disconnect the card rack assembly, make a note of their positions and disconnect the broad ribbon cables from the disk drives.

To re-assemble, use the above procedure in the reverse order.

### NOTE

The ribbon cable is color coded on the lower edge, be sure that the connectors to the disk drives are reconnected the correct way.

When adding option boards to a system the following limitations must be considered:

- 1. A maximum of seven boards may be installed into the card rack assembly.
- 2. The shape of the card rack assembly limits the installation to three full length boards in the upper slot positions; and four shorter length boards in the lower slot positions. You can install shorter length boards in the upper slot positions.

### PRINTED CIRCUIT BOARDS

All printed circuit boards (except power supply and deflection board) are contained in the card rack assembly. To remove the PCBs, remove the card rack assembly and use the following procedures.

## **Option PC Boards**

- Remove the screw that secures the option PCB to the card rack assembly, see Figure 6-3.
- 2. Remove the plastic retaining strip
- Carefully pull the PCB out of the card rack assembly.

To replace a PCB, use the above procedure in the reverse order. Be sure that the connector on the PCB is fully seated into the connector on the main processor board.



Figure 6-3 Option Board Removal

# Main Processor Board

To remove the main processor board, remove all option PCBs, and use the following procedure. This procedure applies to both versions, to the Alternate and Early-type MPB as well.

- Remove the 5 screws and lift off the metal cover (A) as shown in Figure 6-4. When removing an Alternate MPB the Speaker Connector JP7 has to be disconnected before removing the metal cover.
- 2. Disconnect all ribbon cables from the MPB. Alternate MPB: Disconnect flexible disk drive connector from J1\* Disconnect Serial/Parallel Connector from J2 Disconnect Fan Connector from JP7

Early-type MFB: Disconnect Flexible Disk Drive connector from P12 DisconnectSerial/Parallel connector from P13

marked on MPB



Figure 5-4 Main Board Cover Removal

- Remove the two screws fastening the MPB to the rack.
- 4. Unscrew the power connector (A) on the top right of the MPB, see Figure 6-5.
- 5. Removefan(B) on the front panel of the rack.
- 6. Using a small pair of pointed pliers, squeeze the lugs on each of the plastic stand-offs (C) together and carefully lift the board over the stand-offs, see Figure 6-5.
- When the board has been lifted over the lugs of all stand-offs, then the board can be completely removed.
To replace the main processor board, use the above procedure in the reverse order.



Figure 6-5 Board Removal

## DISK DRIVES

To remove the disk drives, first remove the cabinet and card rack assembly, refer to Figure  $\delta-\delta$  and use the following procedure:

 If you have not already done so, disconnect the broad ribbon cable (A) from the disk drive (two cables on fixed disk drives).



Figure 6-6 Disk Drive Removal

- Disconnect the power cable (B) from the disk drive.
- 3. Remove the knurled screw (C).
- Remove the two screws (D) from the top of the disk drive.
- 5. Slide the disk drive out through the front of the cabinet.

To replace a disk drive, use the above procedure in the reverse order.

NOTE: Unless the disk drive strapping is changed, flexible disk drives are not interchangeable. Always replace the drive into the original location.

If a replacement disk drive is being installed then this must be strapped correctly, refer to the Chapter 8 "Strappings".

#### ON/OFF SWITCH

To remove the ON/OFF switch, remove the cabinet and card rack assembly, refer to Figure 6-7 and use the following procedure.

- Be sure that the ON/OFF switch is in the OFF position, and that the power cord is disconnected.
- Remove the two screws from the back of the ON/OFF switch.
- Remove the switch cover the switch can now be pulled away from the back of the cabinet. Make a note of their positions and unsolder the wires.
- 4. Remove the ON/OFF switch.

To replace the switch, use the above procedure in the reverse order. Be sure that the wires are soldered onto the correct terminals.



Figure 6-7 ON/OFF Switch Removal

## BRIGHTNESS AND VOLUME CONTROLS

To remove either the Brightness or Volume controls, remove the cabinet and card rack assembly, use the following procedure:

- Be sure that the ON/OFF switch is in the OFF position, and that the power cord is disconnected.
- Using a small screwdriver push the control knob off the spindle as shown in Figure 6-8.
- From the front of the computer unscrew the control knob securing nut.
- 4. The control knob can now be pulled away from the back of the cabinet. Make a note of their positions and unsolder the wires.
- 5. Remove the control knob.

To replace the control knob, use the above procedure in the reverse order. Ensure that the wires are soldered onto the correct terminals.



Figure 6-8 CRT Control Removal

## MONOCHROME CRT/POWER SUPPLY ASSEMBLY

This part of the computer may be removed as a complete assembly (CRT and power supply), or the power supply may be removed separately. Remove the cabinet, refer to Figure 6-9 and use the following procedure.

#### COMPLETE ASSEMBLY

- Disconnect the three connectors J2,5,4 (A) from the left side of the power supply board.
- Disconnect the CRT cable (B) from the back of the card rack assembly.
- 3. Loosen the 6 screws (C) and slide the assembly to the back, then lift out the complete assembly.

To re-assemble the complete assembly into the computer, use the above procedure in the reverse order.





## POWER SUPPLY

The power supply may be removed from the CRT assembly either after the CRT assembly has been removed from the computer, or while it is still in the computer. To remove the power supply, refer to Figure 6-10 and use the following procedure.



Figure 6-10 Monochrome Power Supply Removal

- 1. Disconnect all connectors (A) from the left side of the power supply board.
- Remove the two screws (B) and disconnect the Ground wire (C).
- Slide the power supply board towards the back of the CRT assembly and lift out.

To re-assemble the power supply into the CRT assembly use the above procedure in the reverse order. Be sure that the ground wire is reconnected and securely fastened.

#### MOBOCRROME DEFLECTION BOARD

#### WARNING

Very high voltages are present when the unit is operating. Before removing the CRT High Tension lead, the CRT tube should be discharged by connecting a 1 megohm resistor between the high tension connector and ground for a minimum of ten seconds, see Figure 6-11.



Figure 6-11 Discharging CRT

The monochrome deflection board can be removed with the CRT assembly still fitted into the computer, to do so refer to Figure 6-12 and use the following procedure.

- Disconnect the high tension lead (A) from the back of the CRT tube.
- Disconnect all connectors (B) from the deflection board.
- 3. Push the plastic clips (C) out of the frame, remove, and lift out the deflection board.
- 4. Remove small PCB from rear of CRT,



Figure 6-12 Monochrome Deflection Board Removal

To replace the deflection board, use the above procedure in the reverse order. Be sure that the high tension lead is fully seated into the CRT tube.

## MONOCHROME CRT

#### WARNING

Very high voltages are present when the unit is operating. Before removing the CRT High Tension lead, the CRT tube should be discharged by connecting a 1 megohm resistor between the high tension connector and ground for a minimum of ten seconds, see Figure 6-11.

To remove the CRT, remove the cabinet, discharge the CRT tube, use the following procedure:

- 1. Remove the complete CRT/Power Supply assembly.
- Disconnect the connector (A) from the deflection board, see Figure 6-13.



Figure 6-13 Monochrome CRT Removal

- Disconnect the high tension lead (B) from the back of the CRT tube.
- Disconnect the small PCB (C) from the end of the CRT tube.
- Disconnect the ground wire (D) from the back of the CRT tube.
- Support the CRT tube, remove the four screws (E) and carefully lift out the CRT tube.

To replace the CRT tube use the above procedure in the reverse order. Be sure that the high tension lead is fully seated into the CRT tube.

#### COLOR CRT/POWER SUPPLY ASSEMBLY

To remove this assembly, remove the cabinet, and use the following procedure:

- 1. Disconnect the three connectors (A) from the left side of the power supply board (Figure 6-14).
- Disconnect the CRT cable (A) from the back of the card rack assembly, see Figure 6-15)



Figure 6-14 Color CRT/Power Supply Removal

- 3. Disconnect the connector (B) from the deflection board.
- 4. Remove the two screws (C)
- 5. Loosen the four screws (D) and slide the assembly to the back, then lift out the complete assembly.



Figure 6-15 Power Supply Removal

To re-assemble the complete assembly into the computer, use the above procedure in the reverse order.

## POWER SUPPLY

To remove the power supply, it is first necessary to remove the complete CRT/Power Supply assembly. Then, Crefer to Figure 6-16, and use the following procedure:

- Disconnect the connectors (A) from the power supply board.
- 2. Disconnect connector (B) from the power supply.
- 3. Remove the four screws (C) that secure the power supply to the base of the assembly.
- Slide the power supply out, under the front of the CRT tube.



Figure 6-16 Color Power Supply Removal

To replace the power supply, use the above procedure in the reverse order.

## COLOR DEFLECTION BOARD

## WARNING

Very high voltages are present when the unit is operating. Before removing the CRT High Tension lead, the CRT tube should be discharged by connecting a 1 megohm resistor between the high tension connector and ground for a minimum of ten seconds, see Figure  $\delta-11$ .

The color deflection board can be removed with the CRT assembly still fitted into the computer, to do so refer to Figure 6-17 and use the following procedure.

- Disconnect the high tension lead from the back of the CRT tube.
- Disconnect 4 connectors J1,2,3,115 (A) from the deflection board.
- Disconnect connector (B) from the deflection board



Figure 6-17 Color Deflection Board Removal

- 4. Remove the screw (C) from the lower right corner of the deflection board.
- The deflection board is protected by a shielding grid which is attached by means of three screws.
- Remove the two screws (D) from the bracket that secures the deflection board to the frame.
- Break the adhesive bond (A) between the end of the CRT tube and the small PCB. disconnect the small PCB from the end of the CRT tube.
- Push the plastic clips (E), see figure 6-17 out of the frame, remove, and lift out the deflection board.

To replace the deflection board, use the above procedure in the reverse order. He sure that the high tension lead is fully seated into the CRT tube.



Figure 6-18 Color Deflection Board Removal

## COLOR CRT

#### WARNING

Very high voltages are present when the unit is operating. Before removing the CRT High Tension lead, the CRT tube should be discharged by connecting a 1 megohm resistor between the high tension connector and ground for a minimum of ten seconds, see Figure 6-11.

To remove the CRT, remove the cabinet, discharge the CRT tube, and use the following procedure:

- 1. Remove the complete CRT/Power Supply assembly.
- Disconnect the connector (B) from the deflection board, see Figure 6-18.

- Disconnect the high tension lead from the back of the CRT tube.
- 4. Break the adhesive bond (A) from the end of the CRT tube and the small PCB, disconnect the small PCB from the end of the CRT tube.
- 5. Disconnect the single wire B from the back of the small PCB.
- Disconnect connector E from Power Supply, see Figure 6-17.
- Support the CRT tube, remove the four screws from the front of the CRT, and carefully lift out the CRT tube.

To replace the CRT tube use the above procedure in the reverse order. Be sure that the high tension lead is fully seated into the CRT tube.

Chapter 7

Adjustments

#### MONOCHROME CRT

The "Utilities" part of the Level 1 Diagnostics Disk contains a " Monochrome Alignment" phase. The screens provided in this phase can be used to help in adjusting the monochrome and color CRT display.

The positions of the adjustment controls are shown in Figure 7-1.



Figure 7-1 Positions of Adjustment Controls

#### WARNING

Very high voltages are present when the unit is operating. Although these voltages carry low current, extreme care should be used when performing adjustments when the unit is operating. All adjustments should be made with plastic adjusting tools. Before removing the CRT high tension lead, the tube should be discharged by inserting under the cap a screwdriver that is connected to ground through a 1 megohm resistor. To be sure that the CRT is fully discharged the connection to ground should be maintained for at least ten seconds, see Figure 7-2.



Figure 7-2 High-Voltage Discharge

# DISPLAY OUT OF FOCUS



Adjust focus trimmer (A).

DISPLAY TOO DIM



Adjust brightness trigmer (B).

# DISPLAY ROLLS



Turn trimmer (C) until display stops rolling, next turn trimmer in the opposite direction display just starts to roll in the opposite direction, now set the trimmer to the mid-point of these two positions.

# DISPLAY TOO SHORT/LONG



Adjust height trimmer (D), "Character Field Size".

# DISPLAY NOT LINEAR



Adjust linearity trimmer (E).

# DISPLAY TOO WIDE/NARROW



Adjust coil (F), see "Character Field Size". DISPLAY TILTED



Loosen the locking screw (G) and rotate the yoke (H) assembly, tighten the locking screw, see Figure 7-1.

## CHARACTER FIELD SIZE



Figure 7-3 Character Field Size

With all adjustments correctly set, a full screen display of 25 lines each with 80 characters, occupies an area of 222 mm (8.7 in.) by 167 mm (6.6 in.), centered on the CRT screen as shown in Figure 7-3.

## COLOR CRT

#### NOTE

Before carrying out these adjustments, the CRT assembly must reach normal operating temperature. The unit should be switched on for at least half an hour with the brightness control turned to maximum.

To display the desired test screens, use the CRT Adjustment Routines of the Level 1 Diagnostics. With the  $\langle F6 \rangle$  key pressed in the Main Menu you enter the Utilities Menu (see Chapter 4). When selecting "5" in the Utility Menu you enter the Alignment Phase enabling you to perform your adjustments. The following screen is displayed: The NCA Personal Computer Page 2 of 2 Copyright 1985 NCR Corp. COLOR ALIGNMENT AVAILABLE SELECTIONS: 1 Linearity 2 Focua 3 Restar 4 Color Bara 5 Purity 8 Blank Borean Note: After alignment screen is displayed proof any key to continue. (F3) Return to previous menu. Press selection number and (ENTER) to choose E com Aveilable Selections SELECTION->

For any trimmer adjustments use only plastic tools.

#### WARNING

Very high voltages are present when the unit is operating. Before removing the CRT High Tension lead, the CRT tube should be discharged by connecting a 1 megohm resistor between the high tension connector and ground for a minimum of ten seconds, see Figure 7-4.



Figure 7-4 High-Voltage Discharge

# EORIZONTAL DEFLECTION

# OSCILLATOR ALIGNMENT

- To enter the Color Alignment Utility proceed as previously described, then select "3", "4" and (F4> to obtain an all white picture to begin your adjustments (refer to Figure 7-5).
- Turn H HOLD (VR502 in a clockwise direction until you see changes (overlapping) on the right edge of the white picture. Mark this position on the tube.
- Turn H Hold in a counterclockwise direction until you see changes (overlapping) on the left edge of the white picture.

Mark this position on the tube.

4. Position the white picture between these marks.

#### PICTURE WIDTH

- Having obtained the Color Alignment Utility select "1" to obtain an all white test grid. Then begin your adjustments (refer to Figure 7-5).
- Adjust H WIDTH (L552) until the width of the picture is 217mm +/-4mm (8.5in. +/-0.16in.).



Figure 7-5 Deflector Board Ajustments and Test Points

#### HORIZONTAL POSITION

- Having obtained the Color Alignment Utility select "1" to obtain an all white test grid. Then begin your adjustments (refer to figure 7-5).
- 2. Adjust H POSI (S591) and H PHASE (VR501) until the display is horizontally centered on the CRT screen.

NOTE: This coil reaches extremely high operating temperatures.

## VERTICAL DEFLECTION

## OSCILLATOR ALIGNMENT

- To enter the Color Alignment Utility proceed as previously described, then select "3", "4" and <F4> to obtain an all white picture to begin your adjustments (refer to Figure 7-5).
- Turn the V HOLD (VR 401) counterclockwise until the picture starts to roll.
- Now turn the V HOLD clockwise until the picture stops rolling, then continue turning in a clockwise direction for approximately 30 degrees.

## LINEARITY

- To obtain a white test grid on the screen select "1" from the Alignment Utility and refer to Figure 7-5.
- Adjust V HEIGHT (VR 403) until the picture height is approximately 150mm (5.9in.).
- 3. Adjust V LIN (VR 402) to obtain optimum linearity.

# PICTURE HEIGHT

- To obtain a white test grid on the screen select "1" and refer to Figure 7-5.
- 2. Adjust HEIGHT (VR 403) until picture height is 163mm +/- 4mm (6.4in.+/- 0.16in.).

## VERTICAL POSITION

- To obtain a white test grid on the screen select "1" and refer to Figure 7-5.
- Adjust V POSITION (S491) until the display is vertically centered on the CRT screen.
  - NOTE: 1f necessary, repeat Linearity, Picture Height, and Vertical Position adjustments.

#### CUTOPF ADJUSTMENT

1. To enter the Color Alignment Utility proceed as previously described, then select "3", "4" and  $\langle F4 \rangle$  to obtain an all white picture to begin your adjustments (refer to Figure 7-6).



Figure 7-6 Video Adapter Board Adjustments

- Turn VR6B1(blue),VR6G1(green), VR6R1(red), and SCREEN fully counterclockwise.
- 3. Turn VR6R2 and VR6B2 to their mid-positions.
- Set the Sub-Brightness (VR201) and the external Brightness control to minimum.
- Set the Service switch to the service position (left position).
- Adjust SCREEN until one of the color lines is just visible.
- Turn the potentiometer for the color that can be seen (VR6B1 for blue, VR6G1 for green, or VR6R1 for red) 1/4 turn clockwise.
- Turn SCREEN counterclockwise until the line is just visible.

- Adjust the two remaining color potentiometers until all colors have the same intensity and produce a white line.
- Set the Service switch to the normal position (mid- position).
- 11. Turn external brightness control to maximum.
- 12. Adjust SCREEN clockwise until the background is just visible, then turn the screen counterclockwise until the background frame is just invisible.

## WHITENESS

- 1. To enter the Color Alignment Utility proceed as previously described, then select "3", "4" and  $\langle F4 \rangle$  to obtain an all white picture to begin your adjustments (refer to Figure 7-6).
- Adjust VR6R2 and VR6B2 until a standard even whiteness is obtained.
- Confirm that the white display does not change when the External Brightness control is turned.

# SUB-BRIGHTNESS

- Select "6" from the Alignment Utility to obtain a blank (dark) screen. Write a dark screen, then set using potentiometer "screen" the background dark; no retrace lines shall be visible. (No frame visible on CRT).
- To enter the Color Alignment phase proceed as previously described, then select "3", "4" and <F4> to obtain an all white picture to begin your adjustments (refer to Figure 7-5).
- Set "external brightness" control and "subbrightness" control (VR 201) to maximum (fully counterclockwise).
- 4. Set "contrast" potentiometer (FZC)
- Select "3", "4" and <F4> from the Alignment Utility to obtain an all white picture to begin your adjustments (refer to Figure 7-6).

- Set meter to 1mA range and connect meter between Testpoints 12 - 92.
- Turn the "External Brightness" and Contrast (VR202) control to maximum.
- Slowly turn Sub-Brightness (VR201) until the current is 400 microamperes.

#### FOCUS

- Display a full screen (25 lines, 80 characters per line) of H<sup>\*</sup>s, and refer to Figure 7-10.
- 2. Set "External Brightness" control to maximum.
- Adjust FOCUS for optimum focusing over the entire CRT screen. Pay particular attenttion to the focus of the middle H in the top row.

#### CONVERGENCE

These adjustments are factory set and are not normally required in the field. If making these adjustments, be sure that the system is not near any magnetic fields, and that the CRT screen faces either north or south.

The CRT is automatically degaussed on switch on. If necessary, switch off and wait at least fifteen minutes, then switch on again.

#### CAUTION

Do not touch the purity magnets.

#### CONVERGENCE ADJUSTMENTS

These adjustments are factory set, they are not normally necessary in the field and should only be made if absolutely necessary. Degauss the system before carrying out these adjustments.

# ADJUSTMENTS

- To obtain a violet grid pattern, select " 3", "4" and (F4> in the Color Alignment Utility and refer to Figure 7-6.
- 2. Loosen magnet look ring.
- Adjust D12 so that the red and blue vertical lines are superimposed on each other (violet lines).



Figure 7-7 Convergence Adjustments

- Adjust c4 and d12 together so that the horizontal red and blue lines are superimposed on each other (violet horizontal lines).
- If necessary make minor adjustments to d12 to ensure that all grid lines are violet.

- 6. To obtain a white grid pattern on screen select "3", "2" and  $\langle F4 \rangle$  in the Alignment Utility.
- Adjust e6 so that the vertical violet and green lines are superimposed on each other (white vertical lines).
- Adjust f9 and e5 together so that the horizontal violet and green lines are superimposed on each other (white horizontal lines).
- If necessary, make minor adjustments to e6 to ensure that all grid lines are white.
- 10. Retighten the lock ring.

# ADJUSTMENTS FOR VIDEOTEX MONITOR (BTI) 008-0072370

#### GENERAL.

- 1. Pre-adjust the chassis
- 2. Perform only the adjustments here described.
- 3. Do not change all other adjustments.

#### TOOL AND TEST PROGRAM

Flexible disk with Service Diagnostic Test Program, HF adjustment tool

## TEST PREPARATIONS

- Set potentiometer VR601-603 on central position. Power on. (Heat-up time 10 minutes).
- 2. Check operating voltage at MP 101, Ub = 113V
- 3. Load Diagnostic Test Program.
- 3. Bring brightness and contrast to maximum.
- 4. Short resistor R 131 against ground
- Adjust for a nearly steady display using potentiometer VR 302 (horizontal hold) and/or VR 304 (vertical hold). Then remove short-circuited

resistor: display must now synchronize.

Caution: Monitor automatically switches from 50 to 60 Hz.

# FOCUS

- 1. Bring brightness and contrast to maximum.
- Adjust VR 109, potentiometer for focus to optimal line focus. Pay attention to border contrast.

## DISPLAY GEOMETRY

Use VR 303 for vertical, VR 301 for horizontal shift, VR 305 for height control, VR 306 for width control, VR 307 (o/w correction) for both display contours.

## COLOR PURITY

Select red display area via Diagnostic Test Program. Turn down brightness. Display area has to lighten monochrome red.

Otherwise if color patches are visible the tube has to be degaussed by using the degaussing coll provided for that purpose.

## LUMINANCE ALIGNMENT

- Set contrast coarse regulator VR518 and the potentiometers VR601, 602, 603 in central position.
- Set brightness to minimum, contrast to maximum and display area to high intensity (white).
- Set a voltage of max. 3 V using VR 518 (subcontrast intensity Y) on pin 6 of IC 501.
- Adjust VR 108 screen approx. 10 degrees from right margin. Pay attention to minimum brightness on the screen. Probably you may adjust VR 108.

## CRROMINANCE ALIGNMENT

Color alignment: Potentiometer: VR 11 BLUE 601 VR 13 RED 602 VR 12 GREEN 603 These potentiometers are on the video amplifier board on the CRT yoke.

- Adjust these potentiometers to obtain a white display. (The impression has to be white. On half intensity the impression should be grey and not blue.
- 2. Check another time line focus. You may adjust

focus at max. brightness and contrast.

#### FBAS CHANNEL-R1.

- Load videotex test program and switch on FBAS channel via keyboard.
- Connect FBAS signal to socket. Check display geometry and display quality. If necessary, slightly adjust.

# ADJUSTMENTS FOR VIDEOTEX MONITOR (BTX) 008-0072370



Figure 7-8 Adjustment Points

# ADJUSTMENTS FOR VIDEOTEX MONITOR (BTX) 008-0072196

#### GENERAL

- 1. Pre-adjust the chassis
- 2. Perform only the adjustments here described
- 3. Do not change all other adjustments.

## TOOL AND TEST PROGRAM

Flexible disk with Service Diagnostic Test Program, HF adjustment tool.

#### TEST PREPARATIONS

Please don't readjust any potentiometer or coil not described in the present procedure. Load disk with Diagnostic Test Program and select the monitor test for linearity.

#### HOBIZOBTAL WIDTH IN 23KHZ MODE

Adjust VR902 so that 105V is obtained at Test Point 99. Adjust L552 to obtain 217 mm horizontal width. Short circuit Test Point 8A and 8B Adjust VR 505 so that an approximately stable picture is obtained.

## VERTICAL HEIGHT AND LINEABITY

Adjust VR402 vertical height to 163 mm Adjust VR 405 for vertical symmetry.

#### FOCUS

Select pattern for focus adjustment and adjust potentiomneter for best average focus (positioned at FBT)

#### CUTOPP AND BRIGHTNESS ADJUSTMENT

Adjust VR6R1, VR6B1, VR6G1 which results in RGB Cuttoff. Adjust these potentiometers with an approximately dark screen for dark grey. Display a full brightness white raster. Switch RGB signal off by removing signal connector. Switch S651 to RGB side, a central horizontal line is then displayed. Adjust VR654, screen positioned at FBT, so that a

AD JUSTMENTS

green line faintly brightens. Adjust VR 652 and VR 653, VR 6R2, VR 6B2 so that the lines becomes white. Cancel S651 and switch RGB signal on.

Adjust VR 6R3 and VR 6B3 so that the raster becomes 6550 degrees K + 7 M.P.C.D white.

#### CONTRAST

Turn external Contrast and Brightness potentiometer in maximum position.

#### STZE

Load VIDEOTEX System from operating system disk in drive A. Load VIDEOTEX Diagnostic Program from disk in drive B. The program concerned is named T1. Picture 1 could be used for size adjustment.

Display a picture by means of the 15KHz mode:

- 1. Adjust height by VR 404
- 2, Adjust H-phase by VR 503
- 3. Adjust H-Hold by VR 506
- 4. Adjust H-width by VR 904

Display a picture by means of the 18KHz mode

- 1. Adjust height by VR 403
- 2. Adjust H-phase by VR 502
- 3. Ajust H-Hold by VR 504
- 4. Adjust H-width by VR 903

Adjust all these potentiometers so that there is no difference between their mode.
### Strapping Information

This chapter provides strapping information for the various FCBs used in the NCR Personal Computer 4i.

### MAIN PROCESSOR BOARD

Your NCR Personal Computer 4i may be provided either with an Early-type MP8 or Alternate MPB also called "generic", because this board can also be installed in other PC models such as the PC 6.

### CONFIGURATION SWITCHES

#### ALTERNATE MAIN PROCESSOR BOARD

Two switch blocks (SW1 and SWA) and one jumper are used to define the system configuration. The location of these switches and jumpers are shown in Figure 8-1. There are many other jumpers on the board required for factory set up and they must not be changed.



Figure 8-1 Switch Locations on Alternate MPB

### STRAPPING INFORMATION

Switch SW1 This block of eight switches defines the following functions:

- o Switch 2 Coprocessor
- o Switch 5 and 6 type of display adapter
- o Switches 1,2,3,4 and 8 are not used in the PC 41

Switch SWA This second block of eight switches defines the following functions:

- o Switches 1 and 2 Memory capacity of the MPB.
- o Switch 3 Serial port
- o Switch 4 Parallel port
- o Switch 5 HIPO feature
- Switches 6, 7 and 8 not used on the PC4i.

#### Switch Settings

To set either switch SW1 or SWA, it is necessary to pull out the card rack assembly, see Figure 8-2.



Figure 8-2 Locations of Switches

Depending on how many option boards are installed, it may also be necessaey to temporarily remove some of these boards.

To set the switches press the end of the rocker switch which corresponds to the desired setting. Use a ball-point pen or similar object to set the switches, see Figure 3-3.



Figure 8-3 Setting the Switches



The settings for the switches block SW1 are shown in Figure 8-4.

Figure 8-4 SW1 Switch Positions

The settings for switch block SWA are shown in Figure B-5.



\* A Memory Expansion Board can be used with these settings.

Figure 8-5 SWA Switch Positions

# STRAPPING INFORMATION

# JUMPER SETTINGS

| JUMPERS        | FUNCTION   |
|----------------|--|
| JP-1<br>JUMPER | Power good jumper<br>1.Open when no popwer good signal is<br>available from power supply.<br>2. Closed when power supply has a power<br>good signal<br>FUNCTION  |
| JP-2           | Factory set, has to remain in closed position  |
| JP-3           | Factory set, has to remain in closed position  |
| JP-4           | Test Input of 8088 processor.<br>1. Open conforms to normal operation.<br>2. Closed conforms to test operation.  |
| JP-5           | Factory set, has to remain in closed position.   |
| ЈсР-б          | Desclects the floppy disk controller on<br>the Alternate MPB.<br>1. Open deselects the on-board floppy.  |
| NOTE:          | When a separate flexible disk<br>controller board is installed (for<br>example, when the system<br>uses a 1.2MB flexible disk drive) then<br>the jumper JP6 must be removed to<br>disable the standard controller. |
| JP-7           | Speaker header for Model 41  |
| JPX            | Factory set, has to remain in closed position.   |
| JPY            | Factory set, has to remain in this position  |

8-6

### BARLY-TYPE MAIN PROCESSOR BOARD

If your system does not contain an Alternate Main Processor Board either an Early-type MPB (1) or Early-type MPB (2) may have been installed into the system. Both main processor boards have one switch block and several jumpers on board. The two boards are similar in design and differ only slightly in their jumper locations.



Figure 8-6 Early-type Main Processor Board

# SWITCH CONFIGURATIONS OF EARLY-TYPE MAINBOARDS



Figure 8-7 Switch Positions (Early-Type MPB1)



Figure 8-8 Switch Positions (Early-Type MPB2)

Some systems are delivered with a different strapping on the MPB. On these systems the strap J/P 5 is replaced by three straps - J/P1, J/P2, and J/P3.

| Flexible Disk Drives Enabled | – J/P | 1 Closed |
|------------------------------|-------|----------|
| Serial Port Enabled          | - J/P | 2 Closed |
| Parallel Port Enabled        | — J/P | 3 Closed |

### STRAPPING INFORMATION

# SWITCHING CONFIGURATIONS OF PCBs

### MEMORY EXPANSION BOARD



Figure 8-9 Switch Position on Memory Expansion Board

# ALPHA DISPLAY ADAPTER

.

| English Closed<br>French<br>German<br>Italian<br>Spanish | Danish<br>Finnish<br>Norwegian<br>Swedish | Open |
|--|---|------|
|--|---|------|

Language Selection JP2



Figure 8-10 Location of Jumpers

### GRAPHIC CONTROLLER BOARD

Set J1 open for color CRT and set J1 closed for monochrome CRT. For jumper location and language selection see figure below:

Strapping for Graphic Controller Board 017-0044063:

| English<br>German<br>French<br>Italian<br>Spanish | Closed | Danish<br>Finnish<br>Norwegian<br>S <del>we</del> dish | Open |
|---|--------|--|------|
|---|--------|--|------|



Figure 8-11 Locations of Jumpers

# STRAPPING INFORMATION

Strappings for Graphic Controller Board 017-0043980

| Language  | Ju                       | Int bet                  |
|---|--------------------------|--------------------------|
|   | ci.                      |                          |
| U.S. English<br>Scandinavlen<br>Other Languages | Closed<br>Open<br>Closed | Open<br>Closed<br>Closed |



Figure 8-12 Locations of Jumpers

### IN-HOUSE DLC ADAPTER

The Jumpers W1 and W2 are usrd to select Interrupt Request lines as shown below:

| Jumper |    |        | Selection |        |    |        |  |  |
|--------|----|--------|-----------|--------|----|--------|--|--|
|        | W1 |        |           |        | W2 | 2      |  |  |
| A<br>B | -  | B<br>C | J         | A<br>3 | -  | B<br>C | IRQ3 Interrupt Vector 11<br>IRQ2 Interrupt Vector 10 |  |

The jumpers W1 and W2 are factory installed between A and B.

# STRAPPING INFORMATION

# VIDEOTEX ADAPTER BOARD

Before installing these boards ensure that the strapping is set correctly as shown in the following tables:

Strappings for Videotex Adapter Board 017-0040364C

| X1 | 1-2      | 2K RAM (factory set)             |
|----|----------|----------------------------------|
|    | 2-3      | 8K RAM                           |
| X2 | 1-2      | 2K HAM (factory set)             |
|    | 2-3      | OK RAM                           |
| ХЭ | DCD.A/   | Jumper elways instelled          |
| X4 | DGR. B/  | Jumper elways installed          |
| X5 | CT6.A/   | Jumper always installed          |
| Xe | 1-12     | IROR (factory set)               |
|    | 2-11     | 1803                             |
|    | 3-10     | 1R04                             |
|    | 4- 9     | IRQ5                             |
|    | 5- B     | 1806                             |
|    | 6-7      | 1R07                             |
| X7 | DCD.8/   | Jumper always installed          |
|    |          | (part of artwork)                |
| XB | Jumper r | ot installed - Video Long Player |
|    | Jumper i | instelled — Videorecorder        |



Figure 8-13 Locations of Jumpers

Strappings for Videotex Adapter Board 017-0044050

| X1 | 1-2             | 2K RAM   |
|----|-----------------|--|
|    | 2-3             | SK RAM [factory sat]   |
| X2 | 1-2             | 2K RAH   |
|    | <del>2-</del> 3 | 8K RAM (fectory set)   |
| XB | 1-12            | IRO 2 (factory set)  |
|    | 2-11            | IRQ 3  |
|    | 3-10            | IRO 4  |
|    | 4-9             | IR0 5  |
|    | 5-8             | IRO 6  |
|    | 6-7             | IRO 7  |
| XØ | 1-2             | Phase Lock Loop filter, switching over without<br>strapping for VLP, whereas using strapping for<br>video recorder |
| XS | 1-5             | A19 Addrese strapping  |
|    | 2-8             | A1B  |
|    | 3-7             | A17 [factory set]  |
|    |                 | 147  |



Figure 8-14 Location of Jumpers

# FLEXIBLE DISK CONTROLLER BOARD 1.2MB



|                 | <b>P</b> U | 19 H | ADDRESS          | 218  | ×1 |
|-----------------|------------|------|------------------|------|----|
| CILPCAIGH PG/XT | DFF        | DN   | 3F Ban 277, 1F 7 | OFF. | 9  |
| PC 4 38H AT     | 0H         | OFT  | 171 277, 177     | ġ,   | 9  |

Figure 8-15 Locations of Jumpers

## HARD DISK CONTROLLER BOARDS

The following Controller Boards are available:

008-0072640;



|            |                | Drive C Drive O<br>Switch 1 and 2 Switch 3 and 4 |          |           |            |
|------------|----------------|--|----------|-----------|------------|
| Şeagale    | 10 MB<br>20 MB | ON<br>OFF  | ON<br>ON | ON<br>OFF | N N<br>N N |
| Miniscribe | 20 MB          | OFF  | OFF      | OFF       | OFF        |
| NEC        | 20 MB          | QN   | OFF      | ÓN        | OFF        |

Figure 8-16 Location of switches

008-0072641:





Switch Settings:

| W1 | 1-2 | W5 |     |
|----|-----|----|-----|
| W2 | 1-2 | W6 | 2-3 |
| W3 | 1-3 | ₩7 |     |
| WЦ | 2-3 |    |     |

SW1 Jumper Block Description

| Pos     | iti | l<br>I  |              |
|---------|-----|---------|--------------|
| 12      | 1   | 11      | l Drive      |
| Drive C | I   | Orive D | t Type       |
| 1 1     | 1   | 11      | 1 57 225     |
|         | Т   |         | ! MITSUBISHI |
| 10      | Т   | 10      | 1 NEC D 5126 |
|         | Т   |         | I.           |
| 0 0     | Т   | 0 0     | L ST 212     |
|         | Т   |         | 1 NEC D 5124 |
|         | Т   |         | 1 BASE 5188  |

008-0072642:



Figure 8-18 Location of Switches

Switch Settings:

|     | W5 | 1-2 | W1  |
|-----|----|-----|-----|
| 2-3 | W6 | 1-2 | ₩2  |
|     | W7 | 1-3 | W3  |
|     |    | 2-3 | 114 |

SW1 Jumper Block Description

| Position |                             |        | <br>_1                    |  |
|----------|-----------------------------|--------|---------------------------|--|
| 1 2      | 12   11<br>Tive C   Drive D | 1 1    | l Drive                   |  |
| Drive C  |                             | і Тура |                           |  |
| 1 1      |                             | 1 1    | l Miniscribe              |  |
|          | 1                           |        | I 3425 TEAC <b>SD</b> 250 |  |
| 01       | 1                           | 01     | I ST 225                  |  |
| 10       | Т                           | 10     | 1 NEC D 5126              |  |
|          | Ι                           |        | I                         |  |
| 0 D      | ŧ                           | D 0    | 1 51 212                  |  |
|          | Т                           |        | I NEC 55124               |  |
|          | Т                           |        | I BASF 6189               |  |

Legend: 0 = Jumper = Switch closed 1 = No Jumper = Switch open

Two jumpers must be set on the Jumper block SW1 see Figure below.



# FLEXIBLE DISK DRIVE JUMPERS

The flexible disk drives of the system are selected by connecting a jumper between certain contacts of the header jumper. The following diagram illustrates the location of the header jumper on the flexible disk drive unit, and the labels of the various jumper connections



Figure 8-20 Jumper Header Configurations for Flexible Disk Drives

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Early-type Main Processor Board (1 of 14)



Early-type Main Processor Board (2 of 14)



#### Early-type Main Processor Board (3 of 14)

.



Early-type Main Processor Board (4 of 14)



Early-type Main Processor Board (5 of 14)



Early-type Main Processor Board (6 of 14)



Early-type Main Processor Board (7 of 14)



Early-type Main Processor Board (8 of 14)



Early-type Main Processor Board (9 of 14)



Early-type Main Processor Board (10 of 14)



Early-type Main Processor Board (11 of 14)


Early-type Main Processor Board (12 of 14)



Early-type Main Processor Board (13 of 14)



Early-type Main Processor Board (14 of 14)

Alternate Main Processor Board



LOGIC DIAGRAMS

|        | PIN NUMBERS |         |                                | PIN NUMBERS |    |     |        | PIN NUMBERS |       |         |          | PIN NUMBERS |    |          |        | PIN NUMBERS |    |     |        |
|--------|-------------|---------|--------------------------------|-------------|----|-----|--------|-------------|-------|---------|----------|-------------|----|----------|--------|-------------|----|-----|--------|
| U      |             |         |                                | U           |    | 1   |        | U           |       | The new | DENO     | U           |    |          |        | U           |    | CND |        |
| NUMBER | +5          | GND     | UNUSED                         | NUMBER      | *5 | GND | UNUSED | NUMBER      | +5    | GND     | UNUSED   | NUMBER      | *5 | GND      | UNUSED | NUMBER      | +5 | GND | UNUSED |
| 1      | 40          | 20      |                                | 37          | 14 | 7   |        | 73          | 8 .   | 16      |          | 109         |    |          |        | 145         | L  |     |        |
| 2      | 14          | 7       |                                | 38          | 14 | 7   |        | 74          | . 4 3 | 14      |          | 10          |    |          |        | 146         |    |     |        |
| 3      | 20          | 10      | 12.12                          | 39          | 14 | 7   |        | 75          |       |         |          | 111         |    |          |        | 147         |    |     |        |
| 4      | 15          | Б       |                                | 40          | 14 | 7   |        | 76          |       |         |          | 112         |    |          |        | 148         |    |     | L      |
| 5      | 40          | 1.20    |                                | 41          | 14 | 7   |        | 77          |       |         |          | 113         |    |          |        | 149         |    |     | I      |
| 6      | 40          | 1,20    |                                | 42          | 50 | 10  |        | 78          |       |         |          | 114         |    |          |        | 150         |    |     |        |
| 7      | 30 59       | 26,9,43 |                                | 43          | 20 | 10  |        | 79          |       |         |          | 115         |    |          |        | 151         |    |     |        |
| 8      |             |         | -12V - PN9 14<br>-12V - PN9 14 | 44          | 16 | 8   |        | 80          |       |         | 1        | 116         |    |          |        | 152         |    |     |        |
| 9      | 50          | 10      |                                | 45          | 20 | 10  |        | 81          |       |         | 1        | 117         |    |          |        | 153         |    |     | [      |
| 10     | 20          | 10      |                                | 46          | 40 | 20  |        | 82          |       |         | <u> </u> | 811         |    |          |        | 154         |    |     |        |
| TT:    | 14          | 7       |                                | 47          | 19 | 42  |        | 83          |       |         | 1        | 119         |    | 1        |        | 155         |    |     |        |
| 12     | 20          | 10      |                                | 48          | 14 | 7   |        | 84          |       | 11      |          | 120         |    |          |        | 156         |    |     |        |
| 13     | 28          | 14      |                                | 49          | 14 | 7   |        | 85          | _     |         |          | 121         |    |          |        | 157         |    |     |        |
| 14     | 28          | 14      |                                | 50          | 14 | 7   |        | 86          |       |         |          | 122         |    |          |        | 158         |    |     |        |
| 15     | 14          | 7       |                                | 51          | 14 | 7   |        | 87          |       |         |          | 123         |    |          |        | 159         |    |     | 1      |
| 16     | 15          | 8       |                                | 52          | 20 | 10  |        | 88          |       |         | <u> </u> | 124         |    | <u> </u> |        | 160         |    |     |        |
| 17     | 16          | 8       |                                | 53          | 14 | 7   |        | 89          | -     |         |          | 125         |    |          |        | 161         |    |     |        |
| 18     | 16          | 8       |                                | 54          | 14 | 7   |        | 90          |       |         |          | 125         |    | <u> </u> |        | 162         |    |     |        |
| 19     | 16          | 8       |                                | 55          | 14 | 7   |        | 91          |       |         |          | 127         |    |          |        | 163         |    |     |        |
| 20     | 14.         | 7       |                                | 56          | 14 | 7   |        | 92          |       |         |          | 128         |    |          |        | 164         |    |     |        |
| 21     | 16          | 8       |                                | 57          | 14 | 7   |        | 93          | 34.6  | 107     |          | 129         |    |          |        | 165         |    |     |        |
| 22     | 20          | 10      |                                | 58          | 20 | 10  |        | 94          | 8     | 15      |          | 130         |    |          |        | 166         |    |     |        |
| 23     | 14          | 7       |                                | 59          | 8  | 15  |        | 95          | 14    | 7       |          | 131         |    |          |        | 167         |    |     |        |
| 24     | 15          | 8       |                                | 60          | i. | 1   |        | 96          | 14    | 7       |          | 132         |    |          |        | 168         |    |     |        |
| 25     | 28          | 14      |                                | 61          |    |     |        | 97          | 5     | 13      |          | 133         |    |          |        | 169         |    |     |        |
| 26     | 50          | 10      |                                | 62          |    | + + |        | 98          |       |         |          | 134         |    |          |        | 170         |    |     |        |
| 27     | 20          | 10      |                                | 63          |    | + + |        | 99          |       |         |          | 135         |    | 1        |        | 171         |    |     |        |
| 28     |             |         |                                | 64          | _  | + + |        | 100         |       |         |          | 136         |    |          |        | 172         |    |     |        |
| 29     | 20          | 10      |                                | 65          |    |     |        | 101         |       |         |          | 137         |    |          |        | 173         |    |     |        |
| 30     | 24          | 12      |                                | 66          |    |     |        | 102         |       |         |          | 1138        |    |          |        | 174         |    |     |        |
| 31     | 31          | 20      |                                | 67          |    |     |        | 103         |       |         |          | 139         |    |          |        | 175         |    | +   |        |
| 32     | 20          | 10      |                                | 68          |    |     |        | 104         |       |         |          | 140         |    |          |        | 176         |    |     |        |
| 33     | 14          | 7       |                                | 69          |    |     |        | 105         |       |         |          | 141         | _  |          |        | 177         |    |     |        |
| 34     | 14          | 7       |                                | 70          |    |     |        | 106         |       |         |          | 142         |    |          |        | 178         |    |     |        |
| 35     | 28          | 10      |                                | 71          |    |     |        | 107         |       |         |          | 143         |    |          |        | 179         |    |     |        |
| 36     | 20          | 10      |                                | 72          | -  | 16  |        | 108         |       |         |          | 140         |    |          |        | 180         |    |     |        |

Logic Diagram



Alternate Processor Mainboard (2 of 14)

Logic Diagram



Alternate Main Processor Board (3 of 14)



Alternate Main Processor Board (4 of 14)



Alternate Main Processor Board (5 of 14)



Alternate Main Processor Board (6 of 14)

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Alternate Main Processor Board (7 of 14)

Logic Diagram





Alternate Main Processor Board (9 of 14)



Alternate Main Processor Board (10 of 14)





Alternate Main Processor Board [11 of 14]



DO NOT POPULATE

# Alternate Main Processor Board (12 of 14)

A-28











÷+5V

- IAO CHCK 5

- HRO IN CHIE

+DR01 12

+ DR02 12

+DR0312

+INT22

+INT3

+INT4 2

+ INTS

+ INI6

+ INT72

-54.

-124.

+124.

+54.

+54.

C1.9.10.11.14 - 34

. 1.F

36.37.39.41-76

GND

GND



|                                     | -   |
|-------------------------------------|-----|
| ÷ J6-12 J1-23÷ ÷ J1                 | -3  |
| → J6-14 J1-25 <del>&lt; + J</del> 1 | -5  |
| → J5-16 J1-27 ÷ J1                  | -7  |
| ÷ J6-18 J1-29÷ ÷ J1                 | -9  |
| → J6-20 J1-31 <del>↔ J</del> 1      | -11 |
|                                     | -13 |
| ÷ J6-24                             | -15 |
|                                     | -17 |
|                                     | -19 |

| + BAEN       |           | 11.2 %                 | + TAFN 2 14    |
|--------------|-----------|------------------------|----------------|
| - BAEN       |           | JI-C                   | -TAENCOLC      |
| -DMA AEN     |           | JII-4                  | -JOHA AFNIC 10 |
| +HI DA       |           | J11-6 :                | +TH DA DIC     |
| SHACI K      | -(311-7   | J11-8 :                | PBOC-0 K       |
| 3 DE ADY     | -: J11-9  | J11-10 :               | T DEADY        |
| SI PHE AUT   | -(J11-11  | J11-12:                | TIMA DCADY 2   |
| 5 PURA READY | -;J11-13  | J11-14 :               | TUNA HEADT 12  |
| IE HRO       | -;J11-15  | J11-16:-               | IHHO (S)       |
|              |           | .111-18:-              | +DMA CLK2 3    |
| E +NMI       |           | 111-20                 | +TNMI (3)      |
| PARITY OUT   | 111-21    | 111-22>                | +TPO TO        |
| 2.NC         |           | JII-22                 | NC             |
|              | -: 311-53 | J11-24 ;               | NC             |
| 8 -HIPO INSI | -: J11-25 | J11-25 ÷-              | NC             |
| 51+M PARITY  | : J11-27  | J11-28 -               | []             |
| 21-50        |           | .111-30                | -LOCK          |
| 5-51         |           | 111-32                 | RAMCS          |
| 5-52         |           | UN DA 4                | - DACKA        |
| - HTPO       |           | J11-34 (               | APNMIT         |
| 6            | :J11-35   | J11-36 <del>&lt;</del> | 610            |
| I GND        | ;J11-37   | J11-38 ÷               | GNU            |
| 5]+0C514     | ;J11-39   | J11-40+                | GND 1          |
|              |           |                        | Ŧ              |
|              |           |                        |                |

NOTES LIMESS OTHERWSE SPECIFIED THE WALK OFF ALL RESATTORS IN IN OWNER WAY, 5%, ALL CAPACITORS ARE IN WERDFARADS 2. FOR FRUITED WRING BOARD SEE ISO-ODOD SOG 3 PROVIDE MOUNTING PADS FOR .200 LEAD SPACING 4 NEWLEST REFERENCE DESGNATOR NUMBERS USED CTY, CH, CRI, NA L2 QI, REI, RPII, SWE, UTT, TA, JP7

## Wiring Diagram



Wiring Diagram (1 of 3)





Wiring Diegrem (3 of 3)

#### **Graphics Controller Board**





Graphics Controller Board (1 of 5)



NUR BESTÜCKT BET KS12 (64K RAM) ONLY MOURTED IN CONJUNCTION WITH KSIZ ( 64 K RAM)



Graphics Controller Board (3 of 5)



Graphics Controller Board [4 of 5]

A-38



Graphics Controller Board (5 of 5)

Alpha Controller Board



- # 3 ITEM 77 GLUED ON SOLDERSIDE
- ✤ 2 NICHT BESTÜCKT:C1, JP1, JP3, P2 NOT MOUNTED:C1, JP1, JP3, P2
- + 1 SCHEMATIC NO.: 017-0040060-F



Alpha Controller Board (1 of 4)





Alpha Controller Board (3 of 4)



+ 2 ALLE WIDERSTANDSWERTE SIND IN OHM

Alpha Controller Board (4 of 4)

ALL RESISTANCE VALUES ARE IN OHM 1 ASSY-NO.: 017-0040424 - E

### Flexible Disk Drive



Flexible Disk Drive (1 of 8)



()

Flexible Disk Drive (2 of 8)



Flexible Disk Drive (3 of 8)

 $\bigcirc$ 



Flexible Disk Drive [4 of 8]


Flexible Disk Drive (5 of 8)



NOTE: RESISTORS ARE ¼W, UNLESS OTHERWISE SPECIFIED.



Flexible Disk Drive (7 of 8)



Flexible Disk Drive (8 of 8)

Flexible Disk Drive 1.2 NB



Fixed Disk Drive



Fixed Disk Drive (1 of 2)



Fixed Disk Drive (2 of 2)

Nonochrome CRT



Nonochrome Power Supply



Color CRT



Color Power Supply



## **External Nonochrome Monitor Controller**



External Monochrome Monitor Controller (1 of 4)



External Monochrome Monitor Controller (2 of 4)

LAYOUTS AND SCHEMATICS



External Monochrome Monitor Controller (3 of 4)



External Monochrome Monitor Controller (4 of 4)

## External Color Monitor Controller



External Color Monitor Controller [1 of 6]



External Color Monitor Controller (2 of 6)



External Color Monitor Controller (3 of 6)

LAYOUTS AND SCHEMATICS



External Color Monitor Controller (4 of 6)



External Color Monitor Controller (5 of 6)



External Color Monitor Controller (6 of 6)

Nemory Expansion Board

| C C C C C C C C C C C C C C C C C C C | $\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 $ |  |
|---------------------------------------|--|--|
|                                       | $\begin{array}{c} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 $ |  |



Memory Expansion Board <1 of 4]



Memory Expansion Board (2 of 4)



Memory Expansion Board (3 of 4)



Memory Expansion Board (4 of 4)

DLC Inhouse I/F Board





DLC Inhouse I/F Board (1 of 3)



DLC Inhouse I/F Board (2 of 3)



DLC Inhouse I/F Board (3 of 3)

Videotex Adapter Board





Videotex Adapter Board (1 of 5)





Videotex Adapter Board [3 of 5]



Videotex Adapter Board (4 of 5)



Videotex Adapter Board (5 of 5
Videotex Switchboard





Videotex Switchboard

## Wiring Diagram Videotex



Wiring Diagram Videotex (1 of 3)



MODEM D1200 S; MODEM D-BT 03; VIDEO, DISK (VLP); C-3277-130X

Wiring Diagram Videotex (2 of 3)



Wiring Diagram Videotex (3 of 3)

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Keyboard





Keyboard Schematic