

MP046 USER'S MANUAL
SIS 486DX PCI/ISA MOTHERBOARD

JUMPERS AND CONNECTORS REFERENCE

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Before installing the mother board, make sure the jumpers setting are set for your configuration. The Function of each jumpers are as follows:

CPU 5pe Selector.....	JP2,7,8,9,15,16,17,18,19,20,21,22
CPU Voltage Selector.....	JP46,47,48,49
CPU Clock Selector.....	JP30,31,32
PCI Clock Selector.....	JP33
CACHE Option.....	JP2
P24D L1 CACHE WB/WT.....	JP2
P24T L1 CACHE WB/WT.....	JP3
Charge/Discharge CMOS.....	JP6,35
External Battery Connector.....	J3
HDD Connector.....	J4,5
HDD LED Connector.....	JP10,11
Internal IDE Selector.....	JP12,13
Keyboard Connector.....	J1
Power Supply Connector.....	J2

NOTE:

Red	Jumpers Represent	Voltage	Selector
white	Jumpers Represent	CPU	Speed
yellow	Jumpers Represent	Clock	Selector
Blue	Jumpers Represent	Cache	Option
Black	Jumpers Represent	Other	Option

JUMPER SETTINGS

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CPU Type Conogrnation

	INTEL	486SX	486DX	INTEL	P24T	P24D	AMD
AMD	AMD	CYRIX	CYRIX				
	486DX	SL ENH	486DX2	486DX	3V		DXL/DX
DX2	DX4	DX/DX2	DX/DX2				
				SL ENH			L2
	3V	3V	4V				
JP2	OPEN	OPEN	OPEN	OPEN	OPEN	1-2	OPEN
2-3	1-2	OPEN	OPEN				
JP3	OPEN	OPEN	OPEN	OPEN	1-2	OPEN	OPEN
OPEN	OPEN	OPEN	OPEN				
JP7	OPEN	3-4	3-4	3-4	3-4	1-2,3-4	OPEN
OPEN	OPEN	2-3	2-3				
JP8	OPEN	OPEN	OPEN	OPEN	1-2	OPEN	3-4
OPEN	OPEN	2-3	2-3				
JP9	1-2, 3-4	2-3	1-2,3-4	1-2,3-4	1-2,3-4	1-2,3-4	1-2,3-4
1-2,3-4	1-2,3-4	1-2,3-4	1-2,3-4				
JP15	3-4	OPEN	3-4	3-4	2-3	3-4	1-2,3-4
3-4	3-4	3-4	3-4				
JP16	2-3	2-3	2-3	2-3	1-2	1-2	2-3
2-3	2-3	2-3	2-3				
JP17	OPEN	OPEN	OPEN	OPEN	1-2	1-2	OPEN
OPEN	OPEN	2-3	2-3				
JP18	OPEN	1-2	1-2	1-2	1-2	1-2	2-3 1-2
OPEN	OPEN	1-2	1-2				
JP19	OPEN	OPEN	OPEN	OPEN	OPEN	1-2	OPEN
OPEN	OPEN	OPEN	OPEN				
JP20	OPEN	2-3	2-3	2-3	2-3	2-3	4-5
OPEN	OPEN	1-2	1-2				
JP21	OPEN	1-2	1-2	1-2	OPEN	2-3	OPEN
OPEN	OPEN	OPEN	OPEN				
JP22	OPEN	4-5	4-5	4-5	1-2	4-5	OPEN
OPEN	OPEN	2-3	2-3				
JP46	OPEN	OPEN	OPEN	CLOSE	OPEN	OPEN	OPEN

		7 & MALL)
	1-2	CACHE Support 8 TAG+1 ALT (-RAS Not Support)
JP29	OPEN	Default (NOT FOR END USER USED)
JP34	1-2 2-3 (DEFAULT)	ACLK and CPU CLK same phase ACLK Delay for CPUCLK
JP38	OPEN (DEFAULT)	SMOUT Connector (NOT FOR END USER USED)
JP39	1-2 (DEFAULT) 2-3	CLK Down Controlled by SMOUT I (NOT FOR END USER USED) CLK Down Controlled by STPCLK# (AMD 486DXL is not included)
JP40	1-2	Default (NOT FOREND USER USED)
JP41	OPEN (DEFAULT)	Reserved (NOT FOR END USER USED)
JP42	OPEN (DEFAULT)	Reserved (NOT FOR END USER USED)
JP43	2-3	Default (NOT FOR END USER USED)
JP45	SHORT OPEN (DEFAULT)	CYRIX M9 2X CLK Other CPU
JP46	SHORT	3.3 VOLT (INTEL Low Voltage CPU)
JP47	SHORT	3.45 VOLT (AMD Low Voltage CPU)
JP48	SHORT	4 VOLT (CYRIX Low Voltage CPU)
JP49	1-2, 4-5 2-3, 5-6	Low Voltage CPU 5 VOLT CPU
J1		K/B Connector
J2		Power Supply Connector
J3		External Battery Connector
J4		IDE 1 Connector
J5		IDE 2 Connector

Power Supply Connector J2. NOTE: Wrong connection will damage the mainboard.

External Battery Connector J3 NOTE: Note of the POLARITY. PM 1 for (+) PM 4 for (-)

Turbo LED (Note of the POLARITY)

OPEN	NORNAL SPEED	LED WILL LIGHT OFF
CLOSE	TURBO SPEED	LED WILL LIGHT ON

NOTE: If KEYBOARD is used to change the'speed TURBO LED will not change.

Turbo Switch 1-2 Normal
 2-3 Turbo

ON BOARD HARD DISK
IDE 1: FOR PRIMARY MASTER
 FOR PRIMARY SLAVE

IDE 2: FOR SECONDARY MASTER
 FOR SECONDARY SLAVE

IF EXTERNAL IDE CONTROLLER IS USED. SET BIOS SETUP CHIPSET FEATURES SETUP ON BOARD 496 IDE PORT: DISABLE

IF MODE 3 HARD DISK IS USED. Set CHIPSET Features Setup IDE HDD Block mode to enable.If ordinary hard disk set IDE HDD Block mode to disable.

MEMORY CONFIGURATION

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The system board Memory can be expanded from 1MB to 256MB. Memory can be installed by using 256K by 32/36 bits, 512K by 32/36 bits, 1M by 32/36 bits, 2M by 32/36 bits, 4M by 32/36 bits, 8M by 32/36 bits, 16M by 32/36 bits, SIMM RAM Module.

DRAM Controller.

- Supports 8 Banks Non-Interleaved Access for Single and Double Sided SIMMs up to 256 MBytes.
- Support "AUTO-DETECT" DRAM configuration.
- Programmable driving current for the DRAM signals.

Before running the main-board with the software program run BIOS SETUP-"LOAD SETUP DEE"AULT",setup default is the best setup configuration for the main-board.

AWARD BIOS SETUP

Award BIOS support plug and play function and has a built-in setup program that allows the users to modify the basic system configuration. This type of information is stored in CMOS so that it retains the setup information when the power is turned off.

1. Turn on or reboot the system. when the below message appears at the bottom of the screen during the POST (Power On Self Test) press DEL key to enter setup.
2. Press the DEL key to enter the Award BIOS program and the main menu will appear on the screen. The main menu allows you to select from ten setup functions and two exit choices.
3. For the safe, please select "LOAD SETUP DEFAULTS" for BIOS setup.

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