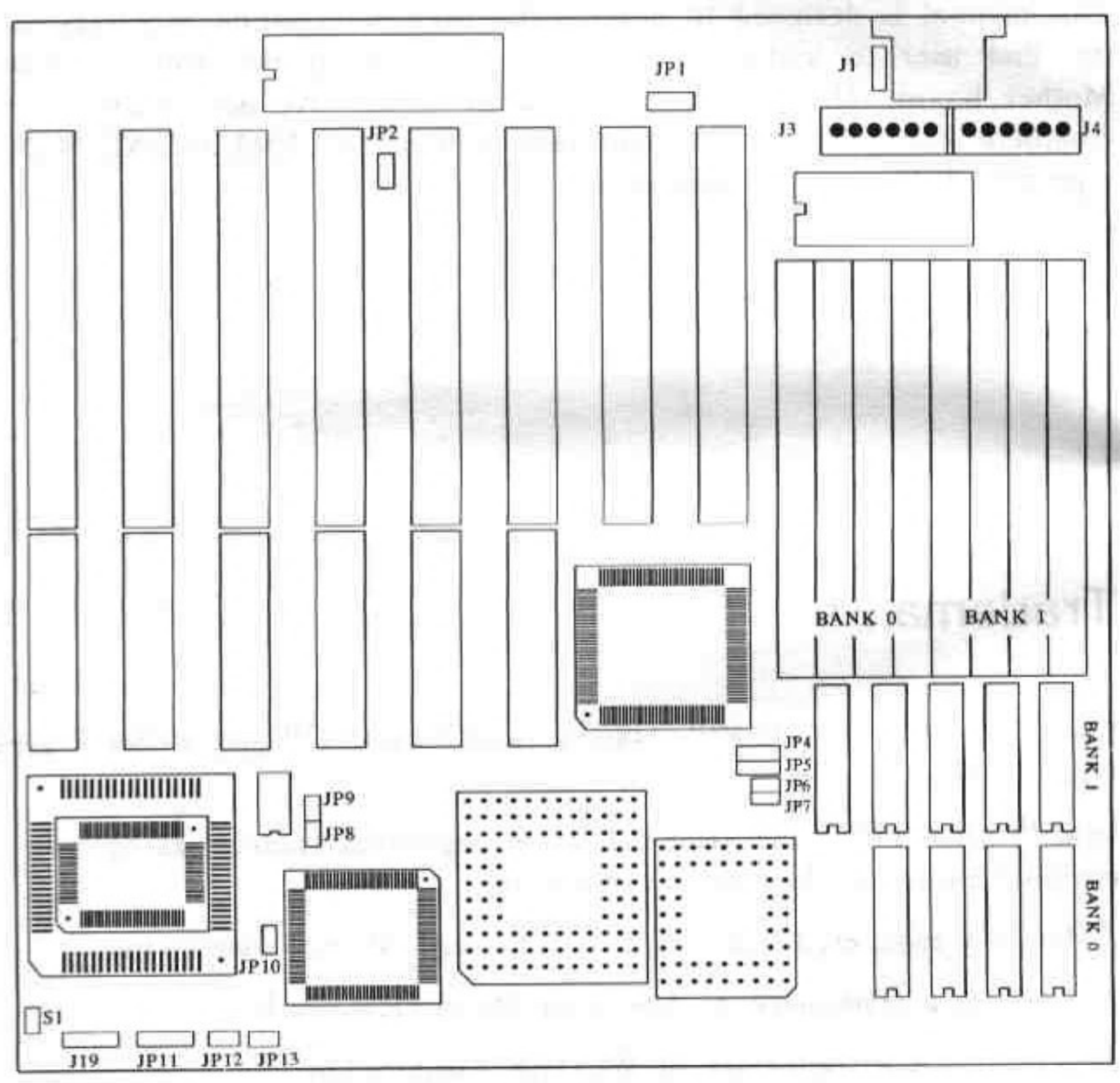


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Introduction

Component Layout

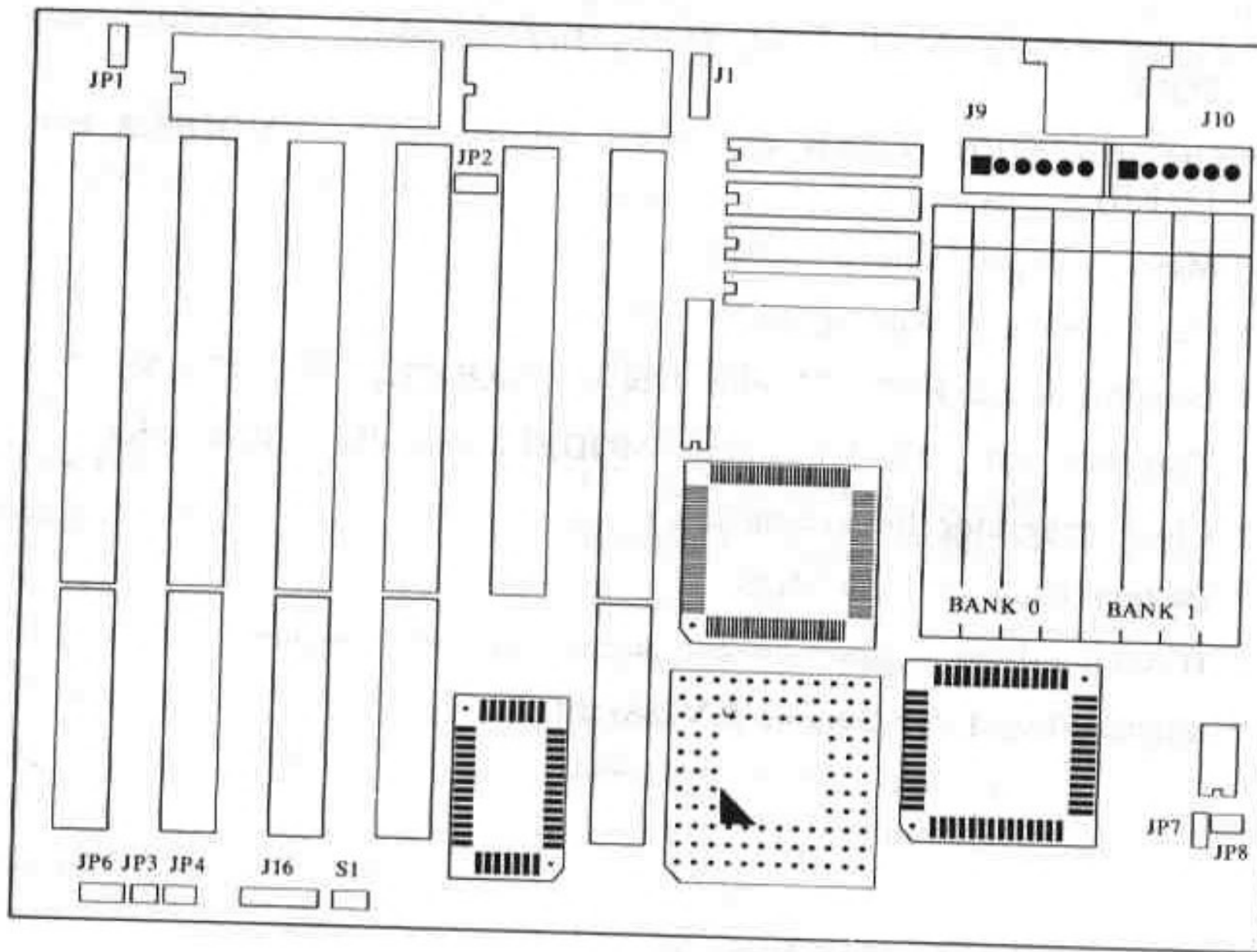
MODEL 1 (MA014)



Introduction

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MODEL 2 (MA013)





Mother Board Features

- * 495SLC 160 Pin Chipset
- * Support 80386-33/40 MHz PGA or PQFP CPU (MODEL2 PQFP)
- * Optional 64KB ,128KB or 256KB external cache (MODEL2 128KB)
- * Memory expandable to 32MB on board
- * Co-processor socket option
- * Dimension :22 x 22 CM with 4 layer (MODEL2 22 x 17 CM)
- * Two 8bit slot , Six 16bit slots (MODEL2 One 8bit , Five 16bit)
- * Clock generator for system clock
- * Battery back-up for CMOS
- * Hidden refresh support to enhance system performance
- * Shadow RAM for System / Video BIOS

MODEL1 JP1 (MODEL2 JP2) Reset / Charge CMOS

CMOS Reset/Charge is used to reset and charge CMOS. If you reset the CMOS all data in the CMOS will be erased. To reset CMOS, place a jumper cap in pin 1 & 2 then turn on the power supply for 2 seconds then turn off the power supply. In this procedure CMOS is reset, all the data in the CMOS is erased. Place the jumper in pin 2 & 3 for CMOS charge, then run setup.

1 (JP2) 1-2 Reset CMOS

1 (JP2) 2-3 Charge CMOS

MODEL1 JP2 (MODEL2 JP1) Mono / Color Monitor selector

Mono / Color Monitor selector is used to select the type of video display you are using. The choice is between CGA and MONO. CGA is only for CGA display. MONO is for all other displays including EGA, VGA and MONO. The default is MONO.

2 (JP1) OPEN MONO

2 (JP1) CLOSE COLOR

JP4, JP5, JP6, JP7 Cache Option (MODEL1 only)

	JP4	JP5	JP6	JP7	BANK 0 (U43-U46)	BANK 1 (U34-U37)	TAG RAM U33
8K	2-3	2-3	OPEN	OPEN	8K * 8	8K * 8	8K * 8
32K	1-2	1-2	CLOSE	OPEN	32K * 8		8K * 8
32K	2-3	2-3	CLOSE	CLOSE	32K * 8	32K * 8	32K * 8

1 JP8, JP9 (MODEL2 JP7,JP8) Clock Generator Frequency

Generator Frequency selector is used to select different frequency of the clock generator. The frequency is depend of the MHz of you are using.

FREQUENCY	JP8 (JP7)	JP9 (JP8)
10MHz	CLOSE	OPEN
12MHz	OPEN	CLOSE
15MHz	OPEN	OPEN

Enable/Disable PQFP CPU (MODEL1 only)

When you mount a 80386 PQFP CPU in your main board, this function is used to enable or disable the 80386 PQFP CPU with out pulling out from the board. If you are not using the 80386 PQFP CPU leave this jumper JP10 OPEN.

OPEN	ENABLE	80386 PQFP CPU
CLOSE	DISABLE	80386 PQFP CPU

Connectors

The main board have eight connectors. Six of them resemble jumper connectors without caps. The seventh is a large double connector for the power supply and the last is for the keyboard connector.

3) Turbo LED connector will be connected to the turbo LED of your computer case. The LED will light up when the system is running in high processing speed. (Note the positive and negative of the LED)

4) Keylock and Power LED connector will be connected to your computer case. Keylock is used to lock the keyboard. Power LED will light up when you turn on your power supply.

5) Speaker connector will be connected to the speaker of your computer case.

6) Power Supply connector is connected from the output of the power supply. Most of the power supply has two connectors which will be connected to the main board. Each connector has six wires, two of the wires are black. To connect to the main board, make sure that the black wires are in the middle. Wrong connection will cause damage of the main board.

7) Keyboard connector. This is used for inputting signal from the keyboard.

Memory Configuration

The system board Memory can be expanded from 1MB to 32MB. Memory can be installed by using 256K, 1M and 4M SIMM RAM module.

MEMORY SIZE	BANK 0	BANK 1
	256K	
	256K	256K
	1M	
	1M	1M
	4M	