

## M918 Cache Memory Configurations

TYPE	TOTAL CACHE	BANK 0 (U19, 22, 24, 27)	BANK 1 (U18, 21, 23, 26)	TAGRAM (U17)	PIECES
A	128K	32K x 8		32K X 8	5
B	256K	32K x 8	32K x 8	32K X 8	9
C	256K	64K x 8		32K X 8	5
D	512K	64K x 8	64K x 8	32K X 8	9
E	512K	128K x 8		32K X 8	5
F	1024K	128K x 8	128K X 8	64K X 8	9

## Cache Memory Jumper Configurations

TYPE	CACHE	JP13	JP14	JP15A	JP15B	JP15C
A	128K	1 - 2	1 - 2	OPEN	OPEN	OPEN
B	256K	2 - 3	2 - 3	OPEN	OPEN	SHORT
C	256K	1 - 2, 3 - 4	1 - 2	OPEN	OPEN	SHORT
D	512K	2 - 3, 4 - 5	2 - 3	OPEN	SHORT	SHORT
E	512K	1 - 2, 3 - 4, 5 - 6	1 - 2	OPEN	SHORT	SHORT
F	1024K	2 - 3, 4 - 5, 6 - 7	2 - 3	SHORT	SHORT	SHORT

Note: information provided by Thomas Hönl, [go.to/th2](http://go.to/th2)