

***EM-586EBX***  
**Embedded Single Board Computer  
with LCD/SVGA/Sound/Lan & Flash  
Disk for ZIF Socket 7 Processor**

This device complies with Part 15 of the FCC Rules  
Operation is subject to the following two conditions:

1. This device may not cause harmful interference , and
2. This device must accept any interference received, Including interference that may cause undesired operation.

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Ver. No.1.1  
LEI-UM-EM586EBX-001  
Date : 10.29.1999

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The EM-586EBX is an embedded single board computer with full functionality, LCD/SVGA, LAN, Sound output & a PC/104-plus expansion bus for Intel Pentium Processor. It consists of an on-board SVGA & Audio output, a PCI Ethernet Interface and a PC/104 expansion bus. The Intel 430TX establishes a new class of low-cost, high-performance system that offers all the functions of an industrial computer on a single board, and fits in the space of a 5.25-inch floppy drive (only 5.75" x 8"). The EM-586EBX has an on-board 2<sup>nd</sup> level cache RAM of 512KB for maximum performance. This design eliminates system conflicts and end-user configuration problems.

For the embedded board market this means more memory, more processing might, and more I/O can be packed tighter. The form factor dispenses with the complexity, cost, and bulk of conventional motherboards, backplanes, and card cages. The embedded single board computer in EM-586EBX also comes with the most popular PC/104-plus self –stacking bus connector that suits the small form-factor board standards (compact PC/104-plus 3.6 x 3.8-inch self-stacking, modular format.). Today, the basic PC architecture is used in such diverse embedded applications as vending machines, communications devices, portable systems, and medical equipment. Among PC/104-plus standardizes the repackaging of desktop PC functions to satisfy the ruggedness, reliability, and size constraints of embedded systems. PC/104-plus lets you combine special-purpose embedded functions with the basic PC computing core.

Other on-board features include four serial ports (RS-232 and RS-232/422/485), one multi-mode parallel (ECP/EPP/SPP) port, a floppy drive controller and a keyboard interface and a PS/2 mouse interface. The built-in high speed PCI IDE controller supports both PIO and bus master modes. Up to two IDE devices can be connected, including large hard disks, CD-ROM drives, tape backup drives and other IDE devices. Its 6-layer printed circuit board combines with noise-tolerant and low power consumption CMOS technology applied on the board makes EM-586EBX able to withstand any harsh industrial environments very well.

## 1.1 SPECIFICATIONS

Processor	: Support Intel Low-power Embedded Pentium Processor and Intel Pentium P54C/P55C, AMD K5/K6, Cyrix M1/M2 series processor
Chipset	: Intel 430TX chipsets, CHIPS 69000, Genesys 518SM, Realtek 8139A, ESS 1938 sound chip, Winbond 977EF & 877TF I/O compatible chips
System Memory	: One 168-pin DIMM socket that support up 256MB of SDRAM / EDO DRAM
Cache Memory	: 512KB pipelined burst synchronous cache
BIOS	: 256KB Licensed Award Flash BIOS
Flash Memory Disk	: Reserved socket for DiskOnChip from M-System, support up to 144 MB flash memory Disk or 32KB SRAM
VGA/LCD Controller	: C&T 69000 LCD/CRT chipset on-die 2MB video memory
Ethernet Controller	: On-board Realtek 8139A, support 100 BASE-TX
Sound Output	: On-board ESS 1938 PCI interface
IDE Drive Interface	: TWO PCI IDE ports, support up to four IDE devices Ultra DMA/33
Floppy Drive Interface	: One FDD port, support up to two floppy devices
Serial Port	: Four COM ports for three RS-232 and one selectable RS-232/422/485
Parallel Port	: One multi-mode parallel port (EPP/ECP/SPP)
RTC Battery	: Dallas RTC battery or compatible
DMA	: 8 DMA channels
Interrupts	: 16 levels of hardware interrupts
External Power Connector	: On-board 7-pin & 4-pin external power connector
PC/104-Plus Expansion Bus	: Built-in PC/104-Plus expansion Bus
Watchdog Timer	: 7 level time-out intervals (0.5/1/2/4/16/32/64 sec.)
Universal Serial Bus	: Support two USB ports
IR Interface	: Supports one IrDA header
Health Monitoring	: On-board Genesys 518SM, support CPU temperature alarm
Operating Temperature	: 0°C~60°C
Humidity	: 10%~90% RH
Dimensions	: 203 mm X 146 mm (8" X 5 <sup>3</sup> / <sub>4</sub> " inches)
Net weight	: 350 g (0.770 pounds)
GPS Interface	: Reserved Header for GPS Kit

## 1.2 PACKING CHECK LIST

Before you begin to install your card, please make sure that you received the following materials as listed below:

### ✦ Standard Packing :

<i>Item</i>	<i>Qty</i>	<i>Remark</i>
EM-586EBX Embedded Single Board Computer	1 pc.	EM-586EBX SBC
VGA/Audio/Lan/Genisys/Intel/BIOS Drivers	1 pc.	Drivers of CD-ROM
User' s manual	1 pc.	EM-586EBX manual

**Note : All Option Kits are to be purchased separately**

### ✦ Option (1) : EM-586EBXCB ( For V1.0 With P8/P9 Power Connector )

<i>Item</i>	<i>Qty</i>	<i>Remark</i>
EIDE cable	1 pc.	44-pin narrow header to 40-pin standard header
Floppy cable	1 pc.	34-pin standard header to 34-pin etch connector
Printer port cable	1 pc.	26-pin standard header to 25-pin female D-Sub (Attached to standard I/O card bracket)
CRT-2 cable	1 pc.	12-pin standard header to 15-pin/3-rows D-Sub
Game port cable	1 pc.	16-pin standard header to 15-pin/2-rows D-Sub
PC/104-plus Mounting kit	1 set	4 x brass spacer (25mm) 4 x null 4 x screws (M3 x 6)



### ✦ Option (2) : EM-586EBXCB ( For V1.1 With EBX Power Connector )

<i>Item</i>	<i>Qty</i>	<i>Remark</i>
EIDE cable	1 pc.	44-pin narrow header to 40-pin standard header
Floppy cable	1 pc.	34-pin standard header to 34-pin etch connector
Printer port cable	1 pc.	26-pin standard header to 25-pin female D-Sub (Attached to standard I/O card bracket)
CRT-2 cable	1 pc.	12-pin standard header to 15-pin/3-rows D-Sub
Game port cable	1 pc.	16-pin standard header to 15-pin/2-rows D-Sub
EBX Power Transfer Cable	1 pc	7-pin+4-pin to P8/P9 12 pin Power Transfer Cable
PC/104-plus Mounting kit	1 set	4 x brass spacer (25mm) 4 x null 4 x screws (M3 x 6)

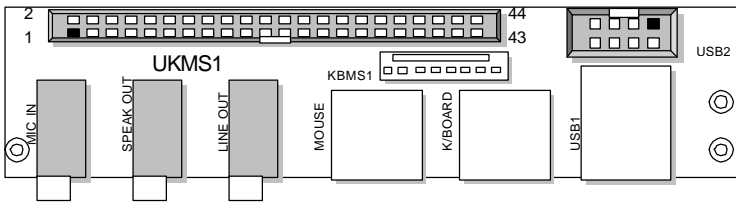





✦ **Option (3) : EM-UKMS-02 kit**


<i>Item</i>	<i>Qty</i>	<i>Remark</i>
 EM-UKMS-02 (USB/Keyboard/Mouse/Sound cabled kit )	1 set	1 x 44-pin to 44-pin thin flat ribbon female cable 1 x U-K-M-S board 92.92 x 27.00 mm (with 3 x Sound phone jack, 1 x 6-pin PS/2 Keyboard connector, 1 x 6-pin PS/2 Mouse and 2 x USB connector)
 Keyboard adapter cable	1 set	6-pin header to 5-pin AT keyboard connector

EM-UKMS-02 Kit Top View :

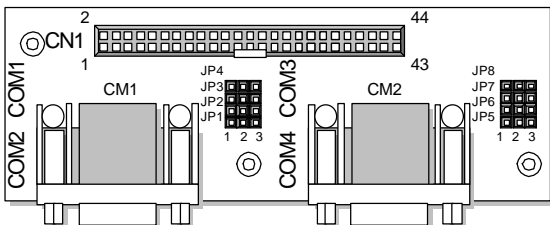


✦ **Option (4) : EM-COM-01 or EM-COM-02 kit**

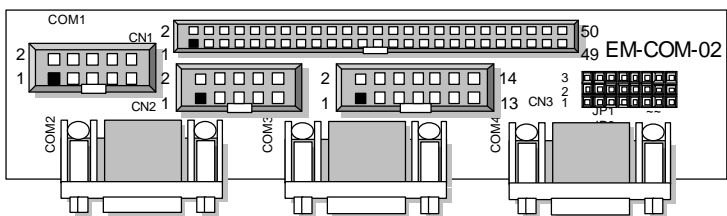
<i>Item</i>	<i>Qty</i>	<i>Remark</i>
 EM-COM-01 (COM cabled kit )	1 set	1 x 50-pin to 44-pin thin flat ribbon female cable 1 x Four COM port board 90.00 x 30.00 mm (with 4 x 9-pin COM port female D-sub connector)

<i>Item</i>	<i>Qty</i>	<i>Remark</i>
 EM-COM-02 (COM cabled kit )	1 set	1 x 50-pin to 50-pin thin flat ribbon female cable 1 x Four COM port board 111.58 x 30.00 mm (with 3 x 9-pin COM port female D-sub connector, and 1X10-pin COM port standard header)

EM-COM-01 Kit Top View :



EM-COM-02 Kit Top View :



✦ **Option (5) : LCD Adapter Kit : [ for example : LK-XXX (XXX=001,002,..)]**

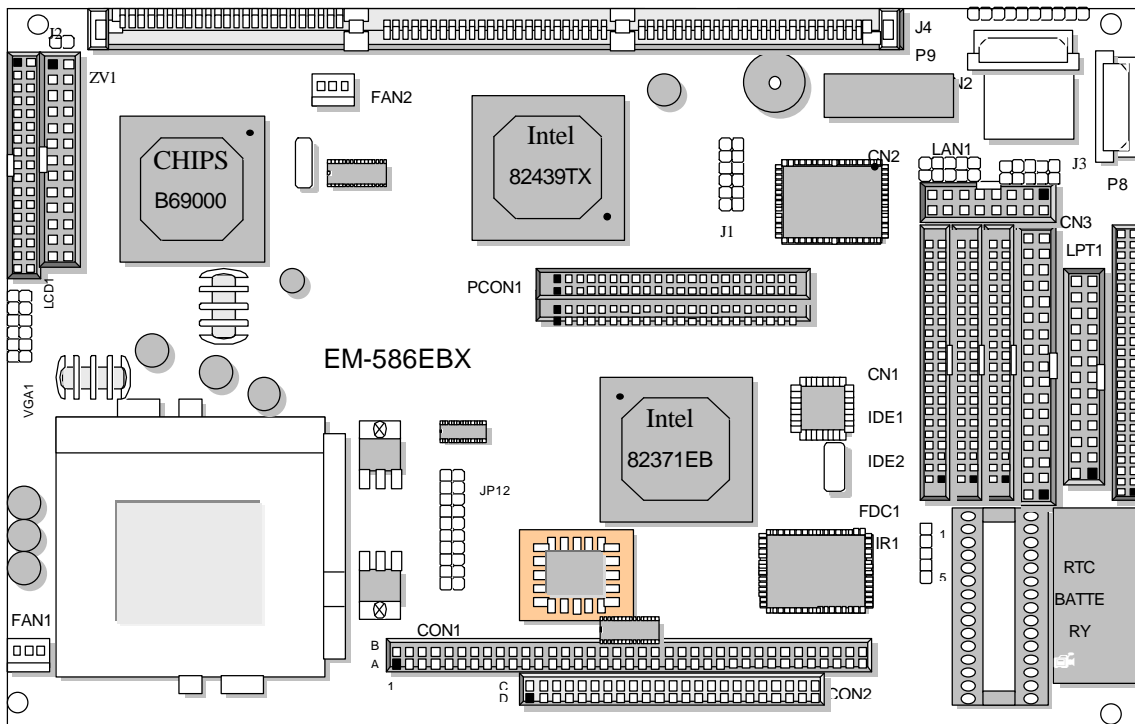
<i>Item</i>	<i>Qty</i>	<i>Remark</i>
🔌 LCD Adapter	1 pc.	Please check LCD adapter list user' s manual
📖 LCD adapter list user' s manual	1 pc.	LCD adapter list manual
🔌 LCD/Flat-Panel adapter cable (An Option Kit, please check your requirement with the LCD Adapter List)	1 set	Please check LCD adapter list user' s manual

**CHAPTER 2.**

**JUMPER SETTINGS AND CONNECTORS**

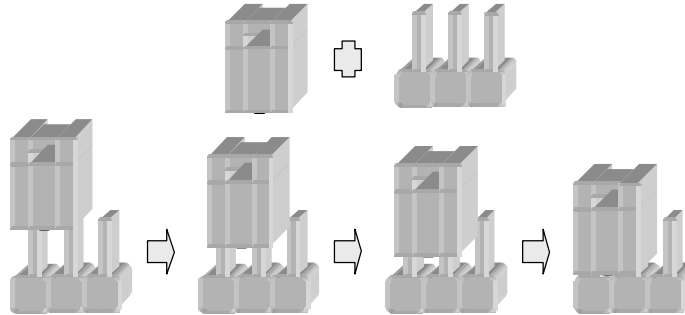
The Figure below shows the jumpers and connectors location on the EM-586EBX:

**2.1 BOARD OUTLINE OF EM-586EBX**



## 2.2 JUMPER SETTING OVERVIEW

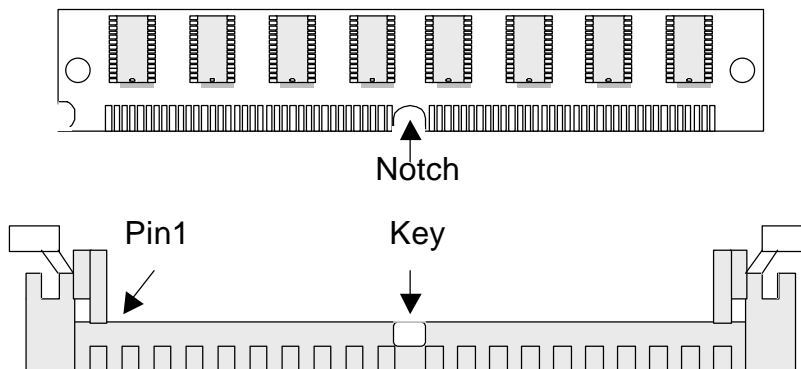
In order to select the operation modes of your system, configure and set the jumpers on the your Embedded SBC to match the need of your application. To set a jumper, a black plastic cap containing metal contacts is placed over the jumper pins as designated by the required configuration as listed in this section. A jumper is said to be “ on ” or “ 1-2 ” when the black cap has been placed on two of its pins, as show in the figure below:



A pair of needle-nose pliers is recommended when working with jumpers. If you have any doubts about the best hardware configuration for your application, contact your local sales representative before you make any changes. In general, you simply need a standard cable to make most connections.

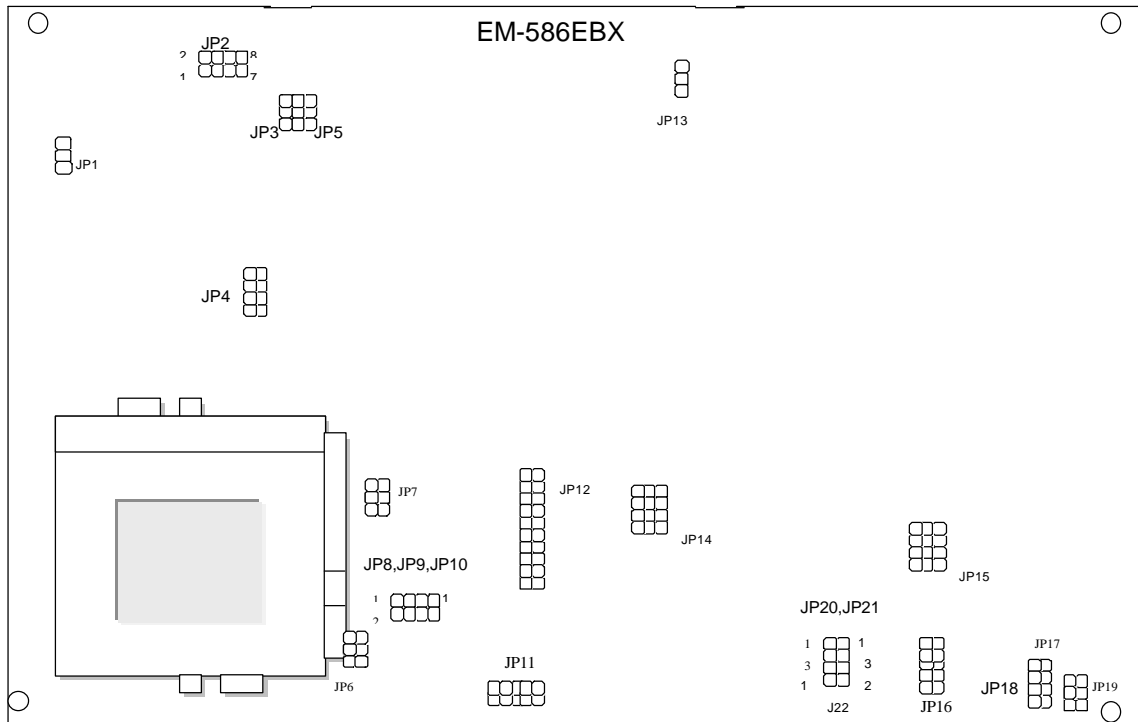
## 2.3 INSTALLING THE DIMM MODULE:

A DIMM module simply snaps into a socket on the system board. Pin1 of the DIMM module must correspond with Pin1 of the socket.



1. Position the DIMM above the socket with the “ notch ” in the module aligned with the “ key ” on the socket.
  2. Seat the module at a 90 angle into the bank. Make sure it is completely seated.
- Tilt  
the module upright until it locks in place in the socket.

## 2.4 JUMPER LOCATION FOR EM-586EBX

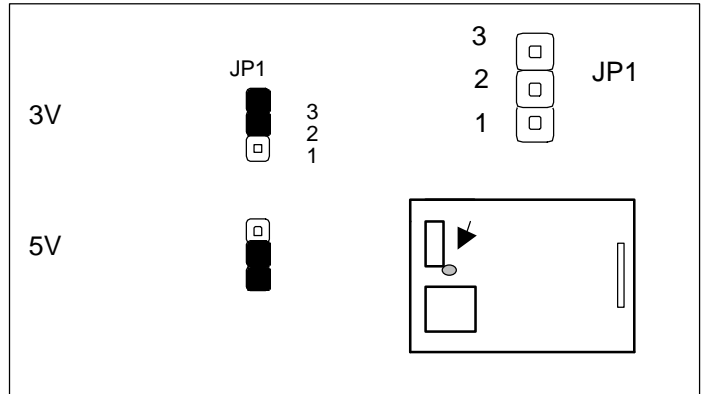
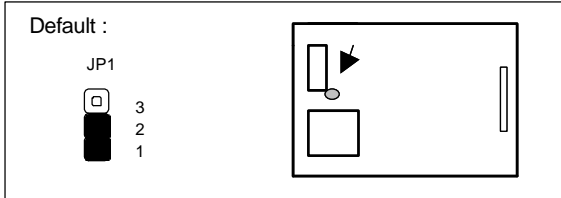


## 2.5 JUMPER SETTINGS SUMMARY FOR EM-586EBX

<b>JUMPERS</b>	
<b>LOCATION</b>	<b>FUNCTION</b>
JP1	Select Panel Voltage
JP2	Select Panel Type
JP3	Select CPU External Bus Clock
JP4	Select CPU Vcore Voltage
JP5	Select Pentium Desktop CPU or Pentium Mobile CPU
JP6	Select Internal CPU Clock Ratio
JP7	Select Vio Voltage
JP8	Default Setting (1-2)
JP9	Default Setting (1-2)
JP10	Select Software Watch-Dog or Hardware Watch-Dog
JP11	Digital Input / Digital Output Ports
JP12	GPS Connector
JP13	Select Pentium Desktop CPU or Pentium Mobile CPU
JP14	Select Watch-Dog Timer
JP15	Select Disable / Enable COM 2
JP16	Select M-System Address and SRAM Address
JP17	Clear CMOS Jumper
JP18	Select M-System or SRAM Type
JP19	Select COM 2 Type
JP20	Select M-System or SRAM Type
JP21	Select M-System or SRAM Type
JP22	Select M-System or SRAM Type
EM-COM-01 JP5~JP8	Select COM4 Type (EM-COM-01 JP1~JP4 Manufacturer Setting)
EM-COM-02 JP1~JP8	Select COM4 Type (EM-COM-02 JP1~JP8 Manufacturer Setting)

✦ **JP1 : Select Panel Voltage**

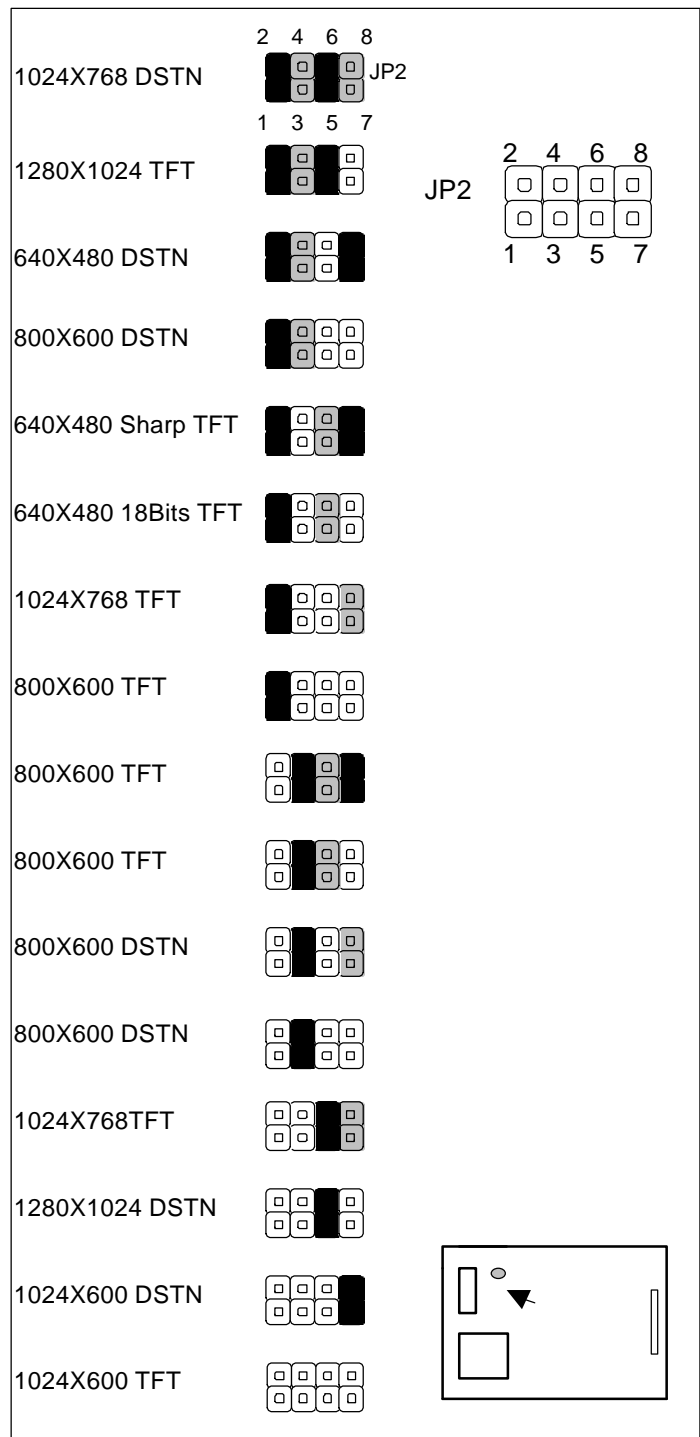
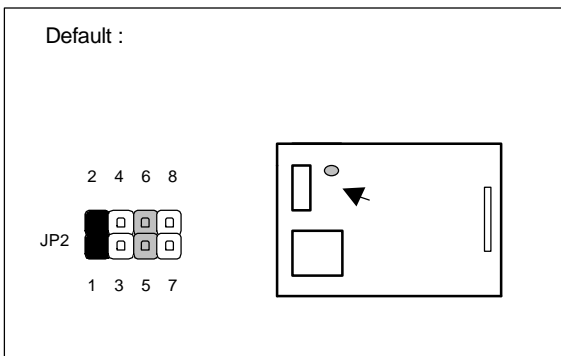
Panel Voltage	JP1
5V ( Default )	1-2
3.3V	2-3



✦ **JP2 : Select Panel Type**

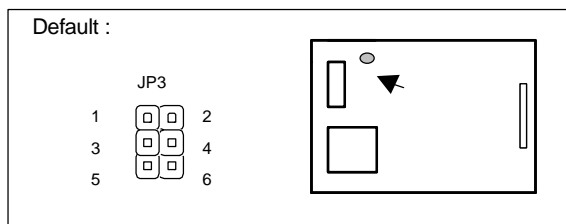
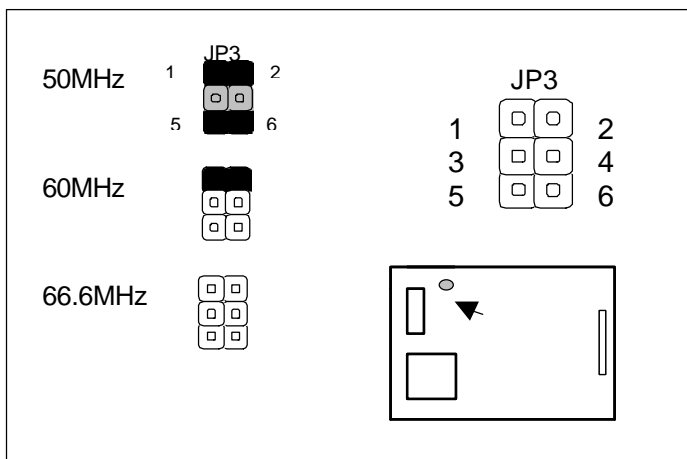
Panel Type	JP2
1024 X 768 DSTN	1-2,3-4,5-6,7-8
1280 X 1024 TFT	1-2,3-4,5-6
640 X 480 DSTN	1-2,3-4,7-8
800 X 600 DSTN	1-2,3-4
640 X 480 Sharp TFT	1-2,5-6,7-8
640 X 480 18 Bits TFT	1-2,5-6
1024 X 768 TFT	1-2,7-8
800 X 600 TFT	1-2
800 X 600 TFT	3-4,5-6,7-8
800 X 600TFT	3-4,5-6
800 X 600 DSTN	3-4,7-8
800 X 600 DSTN	3-4
1024 X 768 TFT	5-6,7-8
1280 X 1024 DSTN	5-6
1024 X 600 DSTN	7-8
1024 X 600 TFT	OFF

Note: Different type of LCD panel with the same resolution will have different jumper setting for selection.



✦ **JP3 : Select CPU External Bus Clock**

CPU External Bus Clock	JP3
50 MHz	1-2,3-4,5-6
60 MHz	1-2
66.6 MHz (Default )	OFF

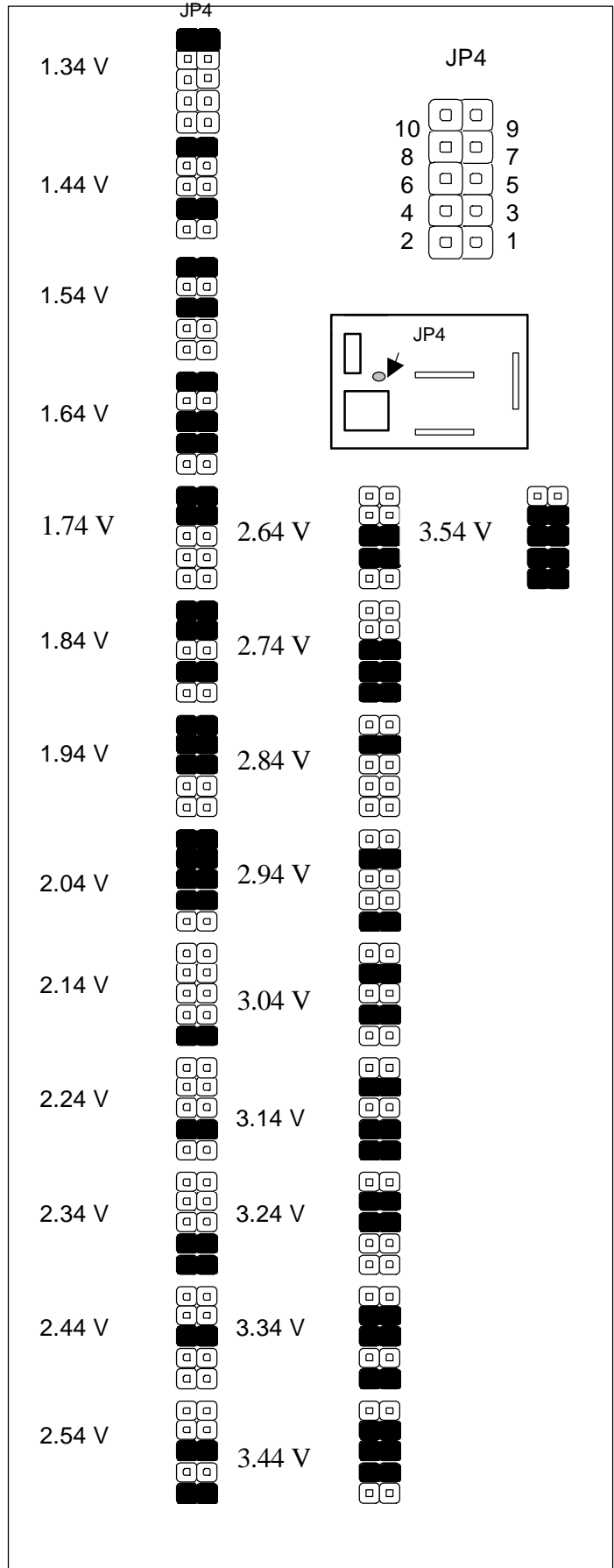
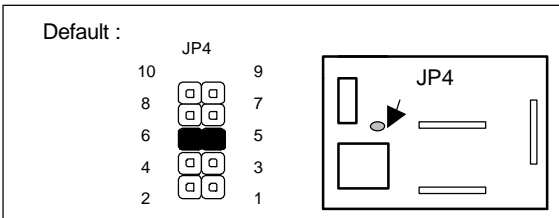


CPU Speed (MHz)	75	90	100	120	133	150	166	200	233
Clock setting	50	60	66.6	60	66.6	60	66.6	66.6	66.6
Frequency ratio	1.5	1.5	1.5	2	2	2.5	2.5	3	3.5



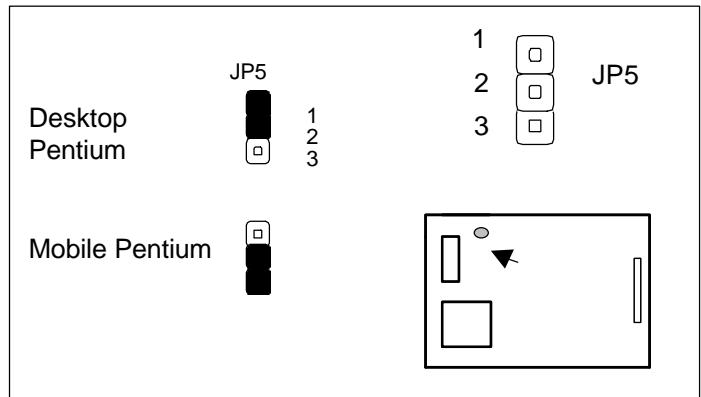
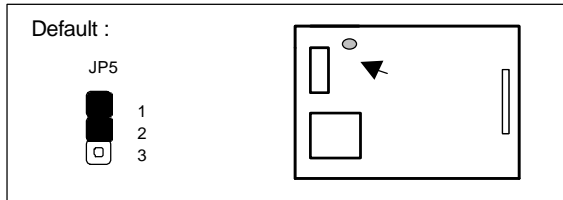
✦ **JP4 : Select Vcore Voltage**

Vcore Voltage	JP4
1.34 V	9-10
1.44 V	3-4,9-10
1.54 V	5-6,9-10
1.64 V	3-4,5-6,9-10
1.74 V	7-8,9-10
1.84 V	3-4,7-8,9-10
1.94 V	5-6,7-8,9-10
2.04 V	3-4,5-6,7-8, 9-10
2.14 V	1-2
2.24 V	3-4
2.34 V	1-2,3-4
2.44 V	5-6
2.54 V	1-2,5-6
2.64 V	3-4,5-6
2.74 V	1-2,3-4,5-6, 7-8
2.84 V (Default)	7-8
2.94 V	1-2,7-8
3.04 V	3-4,7-8
3.14 V	1-2,3-4,7-8
3.24 V	5-6,7-8
3.34 V	1-2,5-6,7-8
3.44 V	3-4,5-6,7-8
3.54 V	1-2,3-4,5-6,7-8



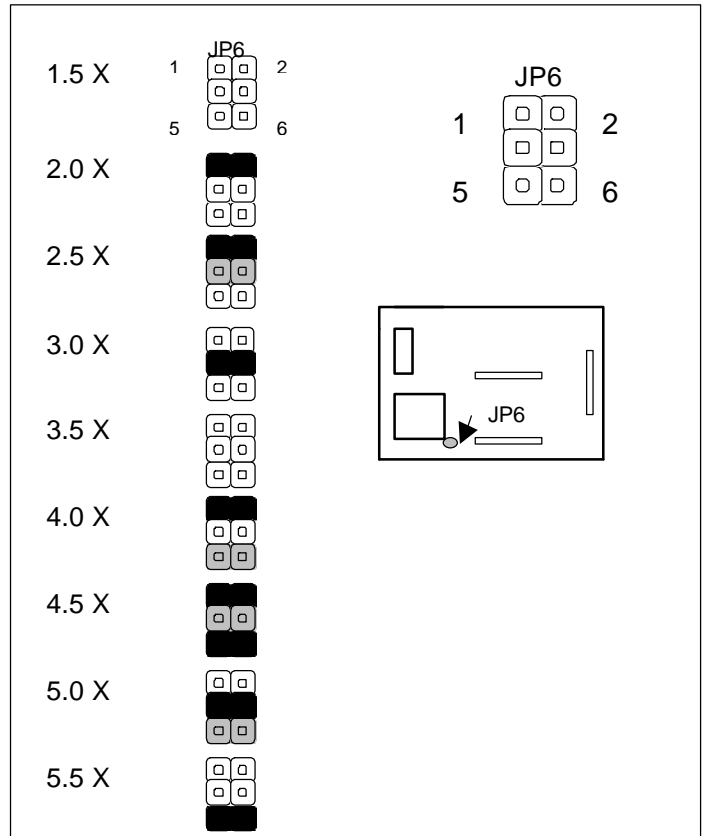
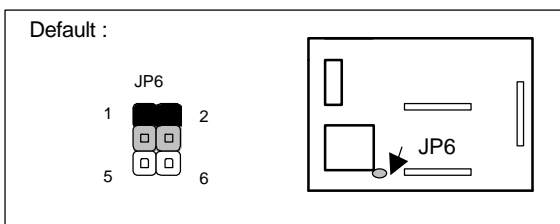
✦ **JP5 : Select Desktop Pentium CPU or Mobile Pentium CPU**

	<b>JP5</b>
Desktop Pentium	1-2
Mobile Pentium	2-3



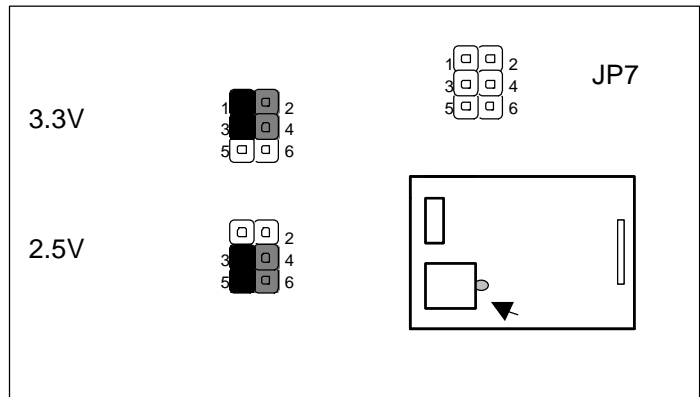
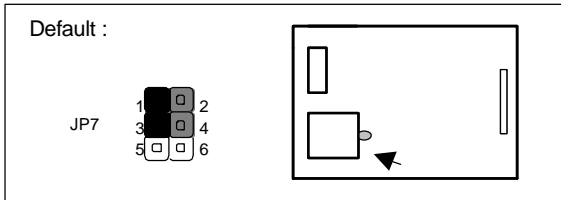
✦ **JP6 : Select Internal CPU Clock Ratio**

<b>Internal CPU Clock Ratio</b>	<b>JP6</b>
1.5 X	OFF
2.0 X	1-2
2.5 X (Default )	1-2,3-4
3.0 X	3-4
3.5 X	OFF
4.0 X	1-2,5-6
4.5 X	1-2,3-4,5-6
5.0 X	3-4,5-6
5.5 X	5-6
Intel Mobile Pentium CPU 266MHz	1-2,3-4
Intel Mobile Pentium CPU 166MHz	1-2,3-4,5-6



✦ **JP7 : Select Vio Voltage**

Voltage	JP7
3.3V (default)	1-3,2-4
2.5V (Mobile CPU)	3-5,4-6



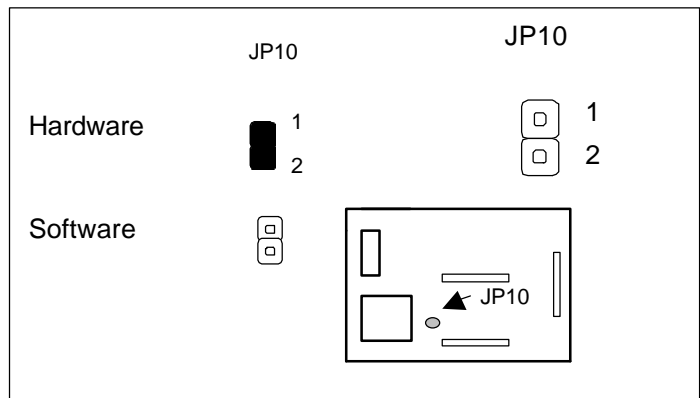
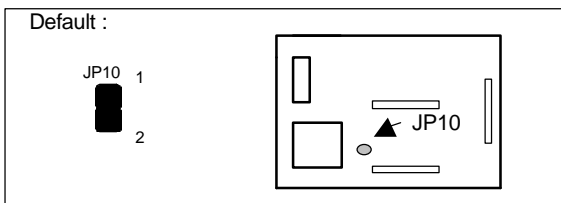
✦ **JP8 : Default Setting ( 1-2 )**

✦ **JP9 : Default Setting ( 1-2 )**

**Note :** Do not attempt to change the default setting of JP8 and JP9 unless the manufacturer would like to change the specification.

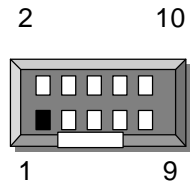
✦ **JP10 : Select Software Watch-Dog or Hardware Watch-Dog**

Type	JP10
Hardware ( Default )	1-2
Software	OFF

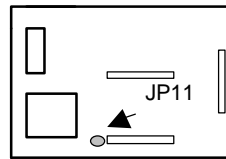


✦ **JP11 : Digital Input / Digital Output Ports ( Header )**

Pin No.	Description	Pin No.	Description
1	IN0	2	OUT0
3	IN1	4	OUT1
5	IN2	6	OUT2
7	IN3	8	OUT3
9	Ground	10	Ground

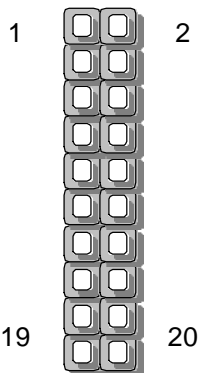


JP11

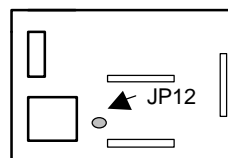


✦ **JP12 : GPS Connector**

Pin No.	Description	Pin No.	Description
1	VCC	2	VCC
3	NC	4	NC
5	RESET	6	NC
7	NC	8	NC
9	NC	10	GND
11	RX	12	TX
13	GND	14	NC
15	NC	16	GND
17	GND	18	GND
19	NC	20	NC

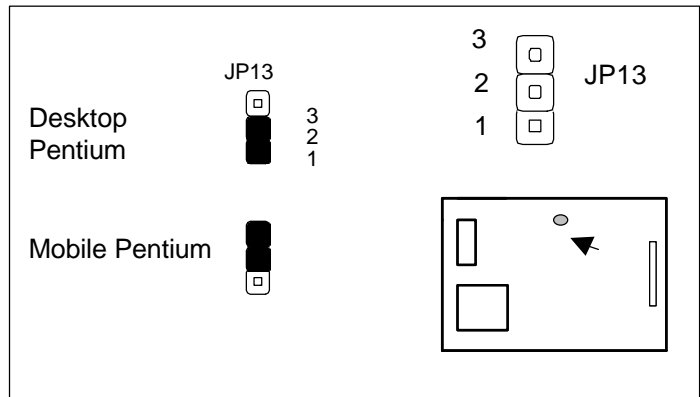
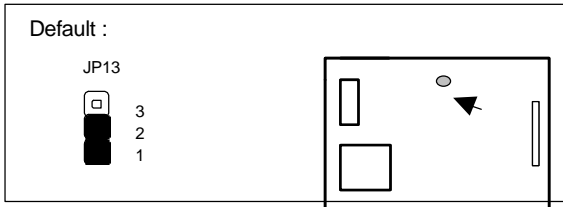


JP12



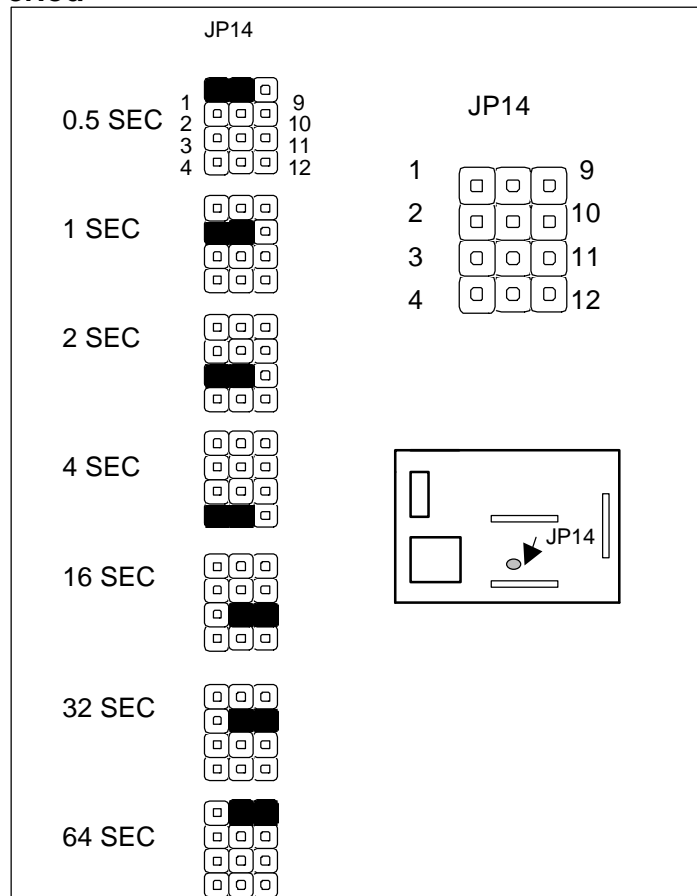
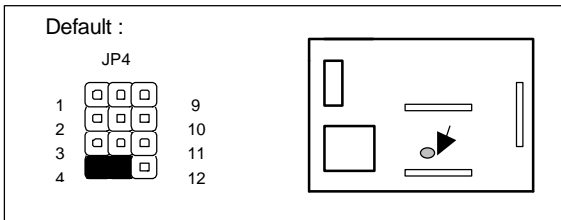
✦ **JP13 : Select Desktop Pentium CPU or Mobile Pentium CPU**

	<b>JP13</b>
Desktop Pentium	1-2
Mobile Pentium	2-3



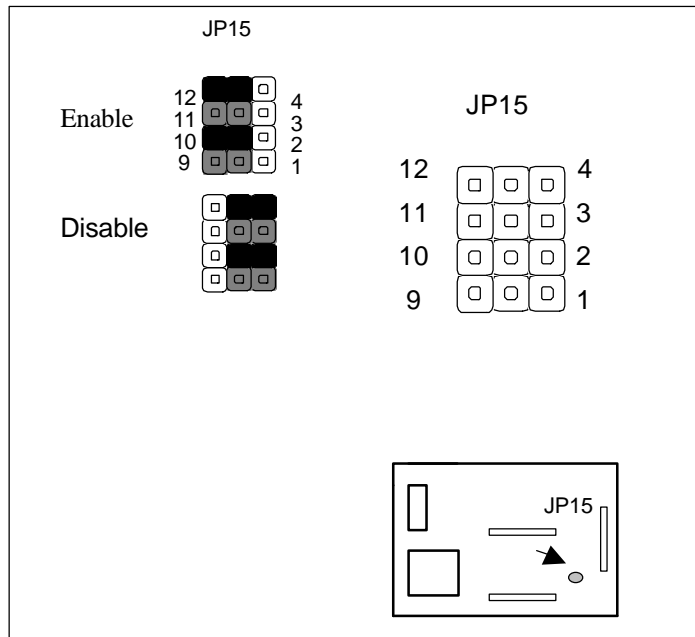
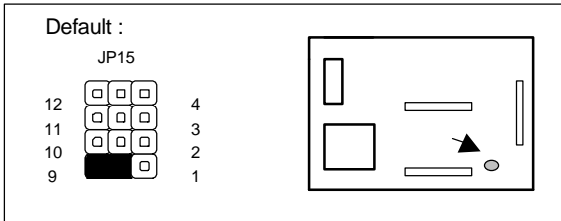
✦ **JP14 : Select Watch-Dog Time Out Period**

<b>Time Out Period</b>	<b>JP14</b>
0.5 sec	1-5
1 sec	2-6
2 sec	3-7
4 sec ( Default )	4-8
16 sec	7-11
32 sec	6-10
64 sec	5-9



✦ **JP15 : Select Disable or Enable COM2**

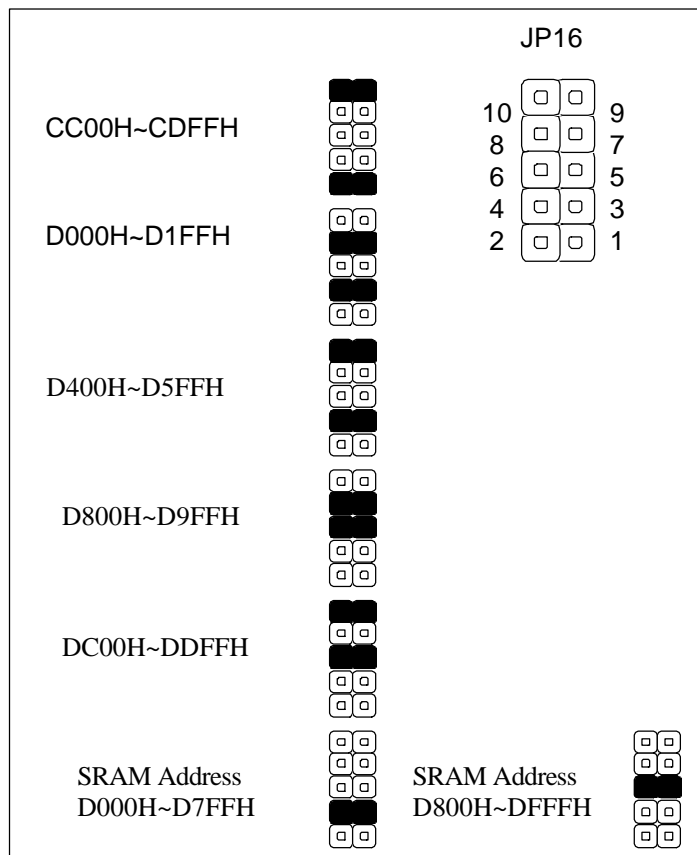
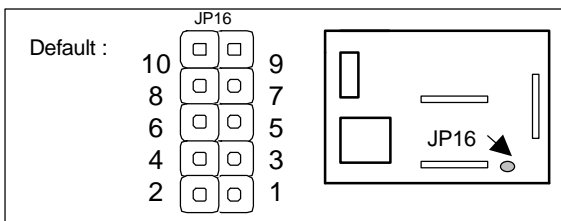
Disable/Enable COM2	JP15
Enable ( Default )	5-9,6-10, 7-11,8-12
Disable ( For GPS )	1-5,2-6,3-7,4-8



➤ **Add extra connector for GPS port to avoid interference component under the High-Limit-Area of GPS Module**

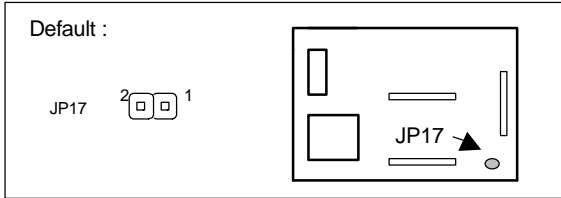
✦ **JP16 : Select Disk On Chip ( Flash Disk ) & SRAM Address**

Flash Disk Address	JP16
CC00H~CDFFH	1-2,9-10
D000H~D1FFH	3-4,7-8
D400H~D5FFH	3-4,9-10
D800H~D9FFH	5-6,7-8
DC00H~DDFFH	5-6,9-10
<b>Select SRAM Address</b>	
D000H~D7FFH	3-4
D800H~DFFFH	5-6



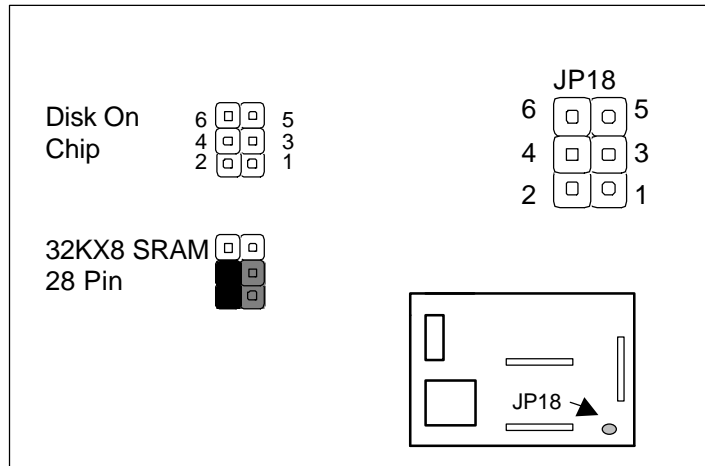
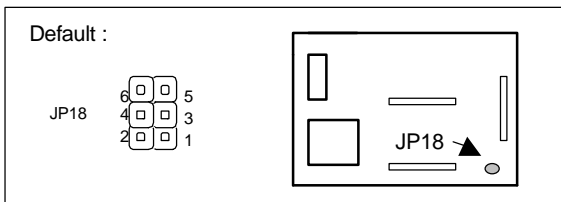
✦ **JP17 : Clear CMOS Jumper**

Clear CMOS Jumper	JP17
Normal	OFF
Clear CMOS	1-2



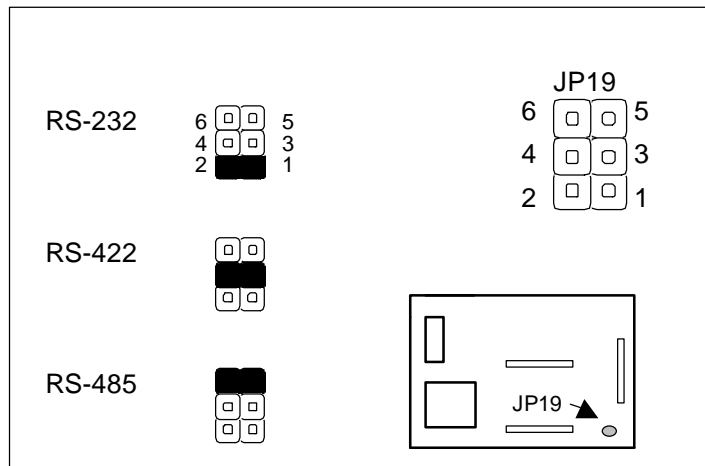
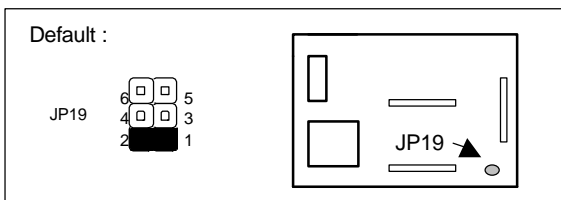
✦ **JP18 : Select M-System or SRAM Type**

Select Type	JP18
Disk On Chip	OFF
32K X 8 SRAM 28 Pin	1-3,2-4



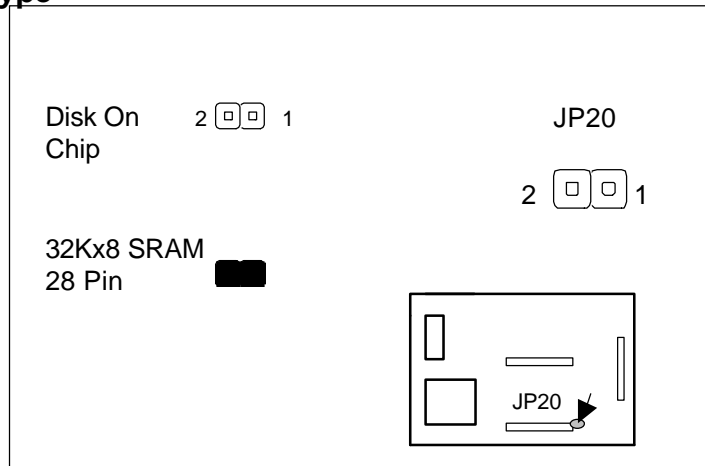
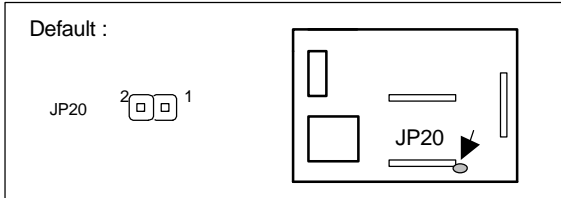
✦ **JP19 : Select COM2 Type**

COM2 Type	JP19
RS-232 (Default)	1-2
RS-422	3-4
RS-485	5-6



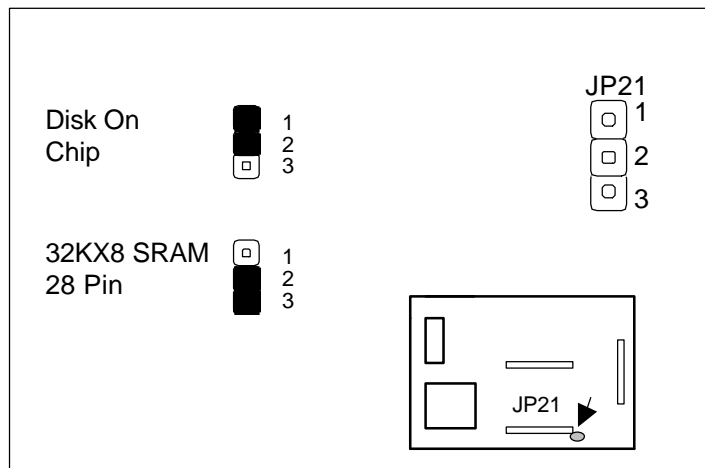
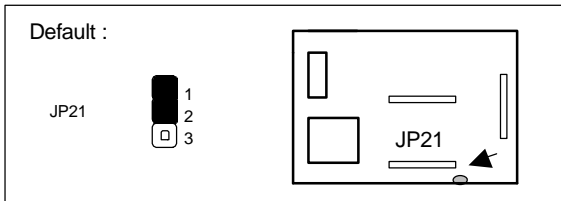
✦ **JP20 : Select M-System or SRAM Type**

Select Type	JP20
DISK On Chip	OFF
32K X 8 SRAM 28 Pin	1-2



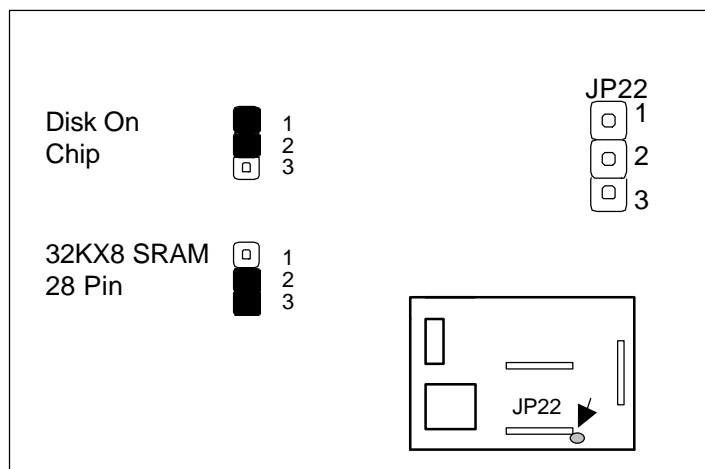
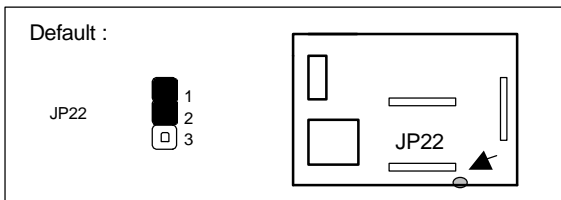
✦ **JP21 : Select M-System or SRAM Type**

Select Type	JP21
Disk On Chip	1-2
32K X 8 SRAM 28 Pin	2-3



✦ **JP22 : Select M-System or SRAM Type**

Select Type	JP21
Disk On Chip	1-2
32K X 8 SRAM 28 Pin	2-3





✦ **EM-COM-01 Jumper Setting :**

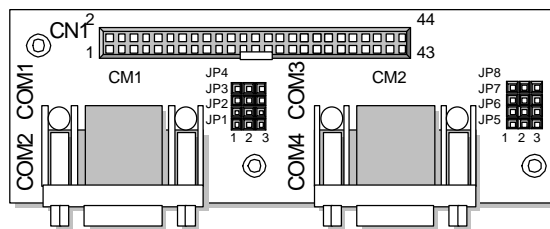
1. JP1~JP4 : Manufacturer Setting
2. JP5~JP8 : Select COM4 Type

COM4 Type	JP5	JP6	JP7	JP8
RS-232 (Default)	1-2	1-2	1-2	1-2
RS-422	2-3	2-3	2-3	2-3
RS-485	2-3	2-3	2-3	2-3

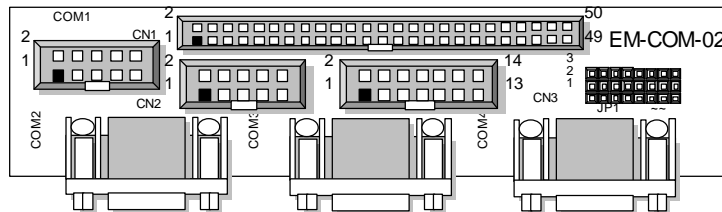
✦ **EM-COM-02 Jumper Setting :**

1. JP1~JP8 : Manufacturer Setting

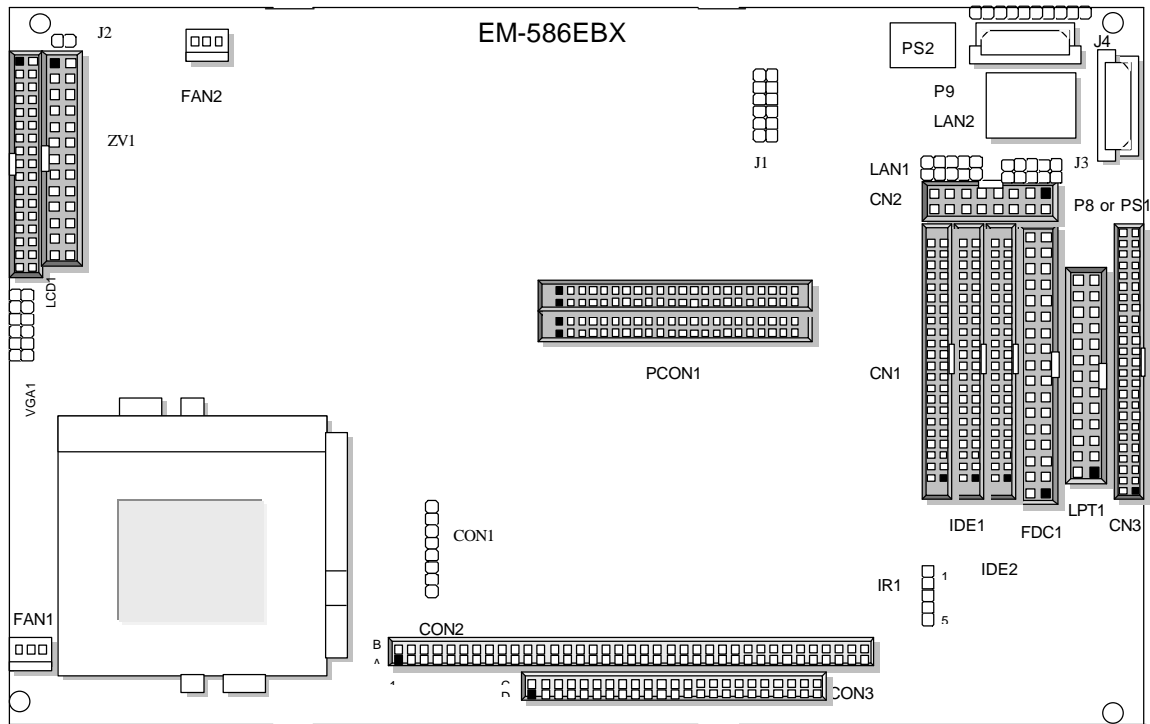
EM-COM-01 Kit Top View :



EM-COM-02 Kit Top View :



## 2.6 I/O Connector Location for EM-586EBX



## 2.7 I/O Connector Summary of EM-586EBX

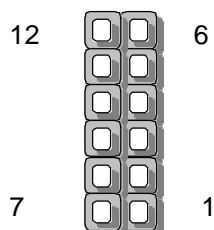
<b>CONNECTORS</b>	
<b>LOCATION</b>	<b>FUNCTION</b>
J1	LAN LED & Sound Switch
J2	SM_BOS Signal
J3	System Switch and LED
J4	System Switch and LED
IR1	Alternate IrDA
FAN1	FAN1 Connector
FAN2	FAN2 Connector
PS1	EBX Power Connector
PS2	-5V & -12V Power Connector
P8	External Power Connector
P9	External Power Connector
CN3	COM1~COM4 Connector ( Header )
CN1	CD_IN, MIC_IN, LIN_IN, Mouse, Keyboard, USB1, USB2 Connector
LCD1	Panel LCD Connector ( Header )
FDC1	Floppy Interface Connector ( Header )
LPT1	Parallel Port Connector ( Header )
IDE1,IDE2	EIDE Interface Connector ( Header )
VGA1	External VGA Connector ( Header )
LAN1,LAN2	LAN Connector ( Header,RJ45 )
CN2	GAME Port Connector ( Header )
CON2、 CON3	PC/104 Connector
ZV1	ZV Port Connector
PCON1	PC/104-Plus Connector
CON1	Manufacturer Setting ( For Pre-Load Watch-Dog on Factory )

## 2.8 I/O CONNECTORS DESCRIPTION

### ✦ J1 : LAN LED & Sound Switch

J1 Connector	Description
Pin 1 & Pin 7 of J1	10M LED
Pin 2 & Pin 8 of J1	RX-TX ACT LED
Pin 3 & Pin 9 of J1	100M LED
Pin 5 & Pin 11 of J1	VOL+ SW
Pin 6 & Pin 12 of J1	VOL- SW

J1 Pin No.	Signal	J1 Pin No.	Signal
1	10M	7	VCC
2	RX-TX	8	VCC
3	100M	9	VCC
4	Ground	10	NC
5	Ground	11	VOL+
6	Ground	12	VOL-



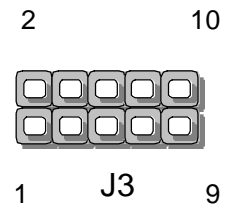
### ✦ J2 : SM\_BOS Signal

Pin No.	Description
Pin 1	SMBCLK
Pin 2	SMBDATA



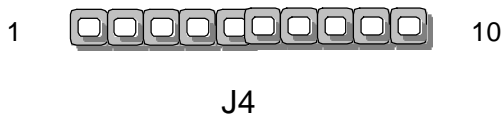
✦ **J3 : System Switch and LED**

J4 Connector	Description
Pin 1 ~ Pin 2 of J3	Power LED
Pin 3 ~ Pin 4 of J3	HDD LED
Pin 5 ~ Pin 6 of J3	Reset SW
Pin 7 ~ Pin 8 of J3	Used Internal Buzzer
Pin 7 ~ Pin 10 of J3	External Speaker



✦ **J4 : System Switch and LED**

J4 Connector	Description
Pin 1 ~ Pin 2 of J4	Power LED
Pin 3 ~ Pin 4 of J4	HDD LED
Pin 5 ~ Pin 6 of J4	Reset SW
Pin 7 ~ Pin 8 of J4	Used Internal Buzzer
Pin 7 ~ Pin 10 of J4	External Speaker



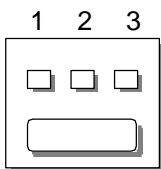
✦ **IR1 : Alternate IrDa**

Pin No.	Description
1	VCC
2	FIRRX
3	IRRX
4	GND
5	IRTX



✦ **FAN1 : FAN1 Connector**

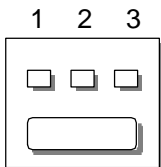
Pin No.	Description
1	Ground
2	+12V
3	FAN Status



FAN1

✦ **FAN2 : FAN2 Connector**

Pin No.	Description
1	Ground
2	+12V
3	FAN Status



FAN2

✦ **PS1 :Ext Power Connector ( P8/P9 and PS1 can' t exist at same time)**

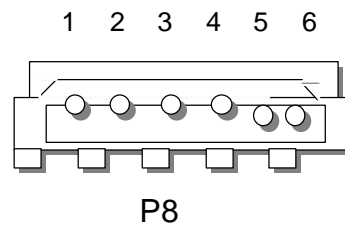
Pin-NO	PS1
1	+5V
2	GND
3	GND
4	+12V
5	NC
6	GND
7	+5V

✦ **PS2 :Ext Power Connector**

Pin-NO	PS2
1	GND
2	-5V
3	GND
4	-12V

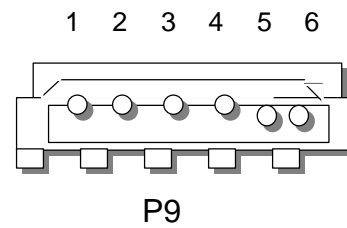
✦ **P8 : External Power Connector**

Pin No.	Description
6	GND
5	GND
4	-12V
3	+12V
2	VCC
1	PWG



✦ **P9 : External Power Connector**

Pin No.	Description
6	VCC
5	VCC
4	VCC
3	-5V
2	GND
1	GND

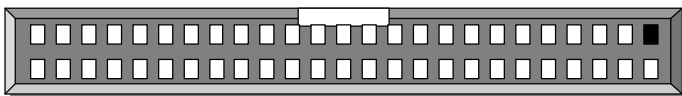


✦ **CN3 : COM1 ~ COM4 Connector**

Pin No.	Description	Pin No.	Description
1	DCD1#	2	DSR1#
3	SIN1	4	RTS1#
5	SOUT1#	6	CTS1#
7	DTR1#	8	RI1#
9	COMGND	10	COMGND
11	DCD4#	12	DSR4#
13	SIN4	14	RTS4#
15	SOUT4#	16	CTS4#
17	DTR4#	18	RI4#
19	COMGND	20	COMGND
21	DCD3#	22	DSR3#
23	SIN3	24	RTS3#
25	SOUT3#	26	CTS3#
27	DTR3#	28	RI3#
29	COMGND	30	COMGND
31	NC	32	NC
33	NC	34	NC
35	COMGND	36	COMGND
37	DCD2#	38	DSR2#
39	SIN2	40	RTS2#
41	SOUT2#	42	CTS2#
43	DTR2#	44	RI2#
45	COMGND	46	COMGND
47	485TXD+	48	485TXD-
49	485RXD+	50	485RXD-

49

1



50

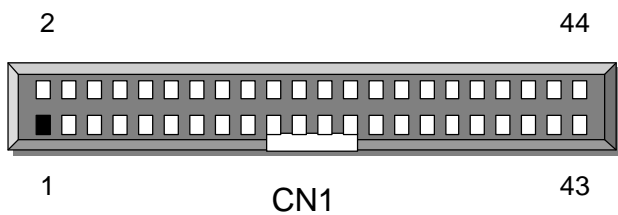
CN3

2



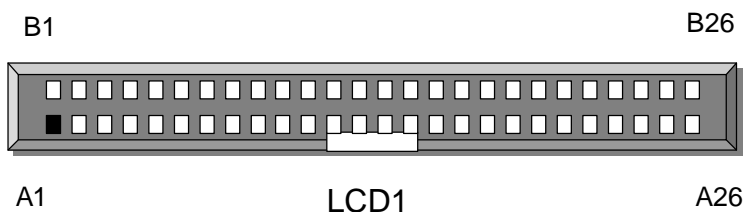
✦ **CN1 : CD\_IN, MIC\_IN, LIN\_IN, Mouse, Keyboard, USB1, USB2 Connector**

Pin No.	Description	Pin No.	Description
1	R-CDI	2	AGND1
3	L-CDI	4	AGND1
5	MIC-IN	6	AGND1
7	AGND1	8	LINER
9	LIN-L	10	AGND1
11	LINEL	12	LIN-R
13	AGND1	14	AGND1
15	AGND1	16	HO-L
17	AGND1	18	AGND1
19	HO-R	20	AGND1
21	AGND1	22	LO-R
23	AGND1	24	AGND1
25	LO-L	26	AGND1
27	KB_GND	28	VCCF
29	MSDATA	30	MSCLK
31	KB_GND	32	VCCF
33	KBDATA	34	KBCLK
35	Ground	36	Ground
37	VUSB1	38	VUSB2
39	USB1-	40	USB2-
41	USB1+	42	USB2+
43	USGND	44	USGND



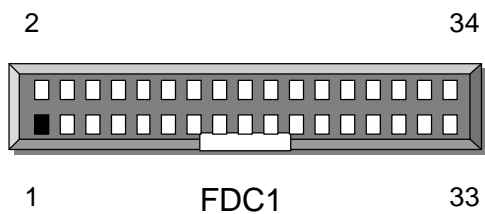
✦ **LCD1 : Panel LCD Connector ( Header )**

<b>PIN</b>	<b>SIGNAL</b>	<b>PIN</b>	<b>SIGNAL</b>
A1	P0	B1	P15
A2	P1	B2	P31
A3	P2	B3	P16
A4	P24	B4	P17
A5	P3	B5	P18
A6	P4	B6	P32
A7	P25	B7	GND
A8	GND	B8	SCLK
A9	P5	B9	GND
A10	P6	B10	DE
A11	P26	B11	FLM
A12	P7	B12	LP
A13	P8	B13	GND
A14	GND	B14	P19
A15	P27	B15	P20
A16	P9	B16	GND
A17	P10	B17	P21
A18	P28	B18	P22
A19	P11	B19	P33
A20	GND	B20	P23
A21	P12	B21	P34
A22	P29	B22	ENBKL
A23	P13	B23	P35
A24	P30	B24	ENAVEE
A25	P14	B25	VCC
A26	ENAVEE	B26	VCC



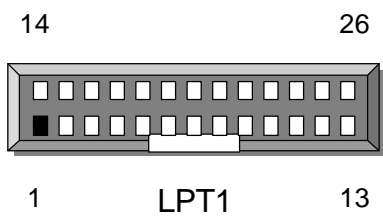
✦ **FDC1 : Floppy Interface Connector ( Header )**

Pin No.	Description	Pin No.	Description
1	Ground	2	Density Select
3	Ground	4	NC
5	Ground	6	DS1
7	Ground	8	Index #
9	Ground	10	Motor Enable A #
11	Ground	12	Drive Select B #
13	Ground	14	Drive Select A #
15	Ground	16	Motor Enable B #
17	Ground	18	Direction #
19	Ground	20	Step #
21	Ground	22	Write Data #
23	Ground	24	Write Gate #
25	Ground	26	Track 0 #
27	Ground	28	Write Protect #
29	NC	30	Read Data #
31	Ground	32	Head Side Select #
33	NC	34	Disk Change #



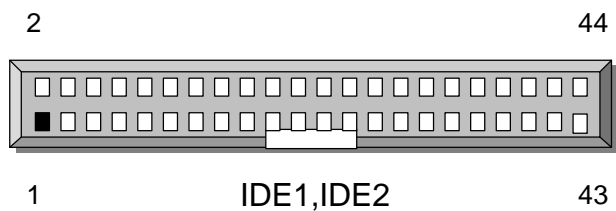
✦ **LPT1 : Parallel Port Connector ( Header )**

Pin No.	Description	Pin No.	Description
1	Strobe #	14	Auto Form Feed
2	Data 0	15	Error #
3	Data 1	16	Initialize #
4	Data 2	17	Printer Select IN #
5	Data 3	18	Ground
6	Data 4	19	Ground
7	Data 5	20	Ground
8	Data 6	21	Ground
9	Data 7	22	Ground
10	Acknowledge #	23	Ground
11	Busy	24	Ground
12	Paper Empty	25	Ground
13	Printer Select	26	Ground



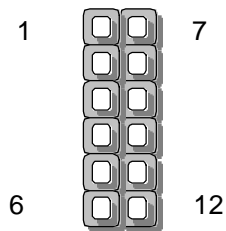
✦ IDE1,IDE2 : IDE Interface Connector ( Header )

Pin No.	Description	Pin No.	Description
1	Reset #	2	Ground
3	Data 7	4	Data 8
5	Data 6	6	Data 9
7	Data 5	8	Data 10
9	Data 4	10	Data 11
11	Data 3	12	Data 12
13	Data 2	14	Data 13
15	Data 1	16	Data 14
17	Data 0	18	Data 15
19	Ground	20	NC
21	DMA REQ #	22	Ground
23	IOW #	24	Ground
25	IOR #	26	Ground
27	IOCHRDY	28	Ground
29	DMA ACK #	30	Ground
31	Interrupt	32	NC
33	SA 1	34	NC
35	SA 0	36	SA 2
37	HDC CS 0#	38	HDC CS 1#
39	HDD Active	40	Ground
41	VCC	42	VCC
43	Ground	44	NC



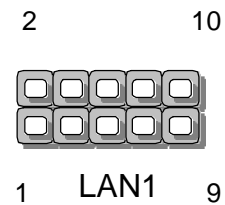
✦ **VGA1 : External VGA Connector ( Header )**

Pin No.	Description	Pin No.	Description
1	R	7	Ground
2	G	8	Ground
3	B	9	Ground
4	H-SYNC	10	Ground
5	V-SYNC	11	Ground
6	DDDA	12	DDCK



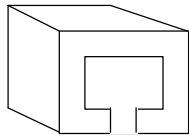
✦ **LAN1 : LAN Connector ( Header )**

Pin No.	Description
1	TX+
2	TX-
3	RX+
4	T45
5	T45
6	RX-
7	T78
8	T78
9	NC
10	NC



## ✦ LAN2 : LAN Connector ( RJ-45 )

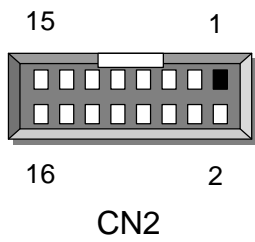
Pin No.	Description
1	TX+
2	TX-
3	RX+
4	T45
5	T45
6	RX-
7	T78
8	T78



LAN2

✦ **CN2 : GAME Port Connector ( Header )**

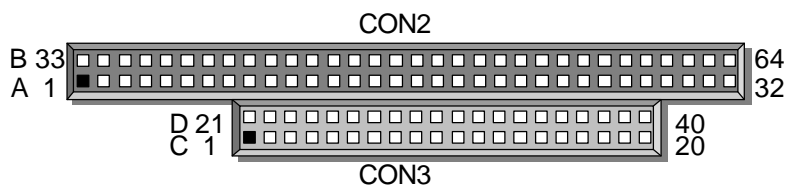
Pin No.	Description	Pin No.	Description
1	VCC	2	VCC
3	SWA	4	SWC
5	OA	6	OC
7	Ground	8	MIDI_OUT
9	Ground	10	OD
11	OB	12	SWD
13	SWB	14	MIDI_IN
15	VCC	16	Ground





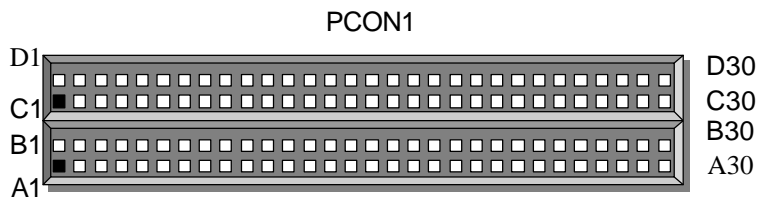
✦ **CON2 , CON3 : PC/104 Connector**

Pin No.	Description			
	CON2		CON3	
	Row A	Row B	Row C	Row D
1	IOCHCK#	Ground	Ground	Ground
2	SD7	RSTDRV	SBHE#	MEMCS16#
3	SD6	+5V	LA23	IOCS16#
4	SD5	IRQ9	LA22	IRQ10
5	SD4	-5V	LA21	IRQ11
6	SD3	DRQ2	LA20	IRQ12
7	SD2	-12V	LA19	IRQ15
8	SD1	0 WS#	LA18	IRQ14
9	SD0	+12V	LA17	DACK0#
10	IOCHRDY	NC	MEMR#	DRQ0
11	AEN	SMEMW#	MEMW#	DACK5#
12	SA19	SMEMR#	SD8	DRQ5
13	SA18	IOW#	SD9	DACK6#
14	SA17	IOR#	SD10	DRQ6
15	SA16	DACK3#	SD11	DACK7#
16	SA15	DRQ3	SD12	DRQ7
17	SA14	DACK1#	SD13	+5V
18	SA13	DRQ1	SD14	MASTER#
19	SA12	REFRESH#	SD15	Ground
20	SA11	SYSCLK	NC	Ground
21	SA10	IRQ7	---	---
22	SA9	IRQ6	---	---
23	SA8	IRQ5	---	---
24	SA7	IRQ4	---	---
25	SA6	IRQ3	---	---
26	SA5	DACK2#	---	---
27	SA4	TC	---	---
28	SA3	BALE	---	---
29	SA2	+5V	---	---
30	SA1	OSC	---	---
31	SA0	Ground	---	---
32	Ground	Ground	---	---



✦ PCON 1 : PC/104-Plus Connector

Pin-N O	Description	Pin- NO	Description	Pin-N O	Description	Pin-N O	Description
A1	KEY2	B1	NC	C1	VCC	D1	AD0
A2	VCC	B2	AD2	C2	AD1	D2	VCC
A3	AD5	B3	GND	C3	AD4	D3	AD3
A4	C/BE-0	B4	AD7	C4	GND	D4	AD6
A5	GND	B5	AD9	C5	AD8	D5	GND
A6	AD11	B6	VCC	C6	AD10	D6	M66EN
A7	AD14	B7	AD13	C7	GND	D7	AD12
A8	VCC3	B8	C/BE-1	C8	AD15	D8	VCC3
A9	SERR-	B9	GND	C9	SBO-	D9	PAR
A10	GND	B10	PERR-	C10	VCC3	D10	SDONE
A11	STOP-	B11	VCC3	C11	LOCK-	D11	GND
A12	VCC3	B12	TRDY-	C12	GND	D12	DEVSEL-
A13	FRAME-	B13	GND	C13	IRDY-	D13	VCC3
A14	GND	B14	AD16	C14	VCC3	D14	C/BE-2
A15	AD18	B15	VCC3	C15	AD17	D15	GND
A16	AD21	B16	AD20	C16	GND	D16	AD19
A17	VCC3	B17	AD23	C17	AD22	D17	VCC3
A18	IDSL0	B18	GND	C18	IDSL1	D18	IDSL2
A19	AD24	B19	C/BE-3	C19	VCC	D19	IDSL3
A20	GND	B20	AD26	C20	AD25	D20	GND
A21	AD29	B21	VCC	C21	AD28	D21	AD27
A22	VCC	B22	AD30	C22	GND	D22	AD31
A23	REQ-0	B23	GND	C23	REQ-1	D23	VCC
A24	GND	B24	REQ-2	C24	VCC	D24	GNT-0
A25	GNT-1	B25	VCC	C25	GNT-2	D25	GND
A26	VCC	B26	PCICLK0	C26	GND	D26	PCICLK1
A27	PCICLK2	B27	VCC	C27	PCICLK3	D27	GND
A28	GND	B28	PIRQ-D	C28	VCC	D28	PCIRST-
A29	+12V	B29	PIRQ-A	C29	PIRQ-B	D29	PIRQ-C
A30	-12V	B30	NC	C30	NC	D30	KEY1



## **CHAPTER 3.**

# **AWARD BIOS SETUP**

Award's ROM BIOS provides a built-in Setup program that allows user to modify the basic system configuration and settings. The modified data will be stored in a battery-backed CMOS RAM so that these data will be retained even when the power is turned off. In general, the information saved in the CMOS RAM stay unchanged unless there is configuration change in the system, such as hard drive replacement or new equipment is installed.

### **3.1 RUNNING AWARD BIOS**

The Setup Utility is stored in the BIOS ROM. When the power of the computer system is turned on, a screen message appears to give you an opportunity to call up the Setup Utility; while the BIOS will enter the Power On Self Test (POST) routines. The POST routines perform various diagnostic checks while initializing the board hardware. If the routines encounter an error during the tests, the error will be reported in either of the two different ways, hear a series of short beeps or see an error message on the screen display. There are two kinds of error: fatal or non-fatal. The system can usually continue to boot up sequence with the non-fatal errors. Non-fatal error messages usually appear on the screen along with the following instructions:

“ Press <F1> to RESUME ”

Write down the message and press the F1 key to continue the bootup sequence. After the POST routines are completed, the following message appears:

“ Press DEL to enter SETUP ”


#### **Entering Setup**

Turn on the power of the computer system and press <Del> immediately. If you don't have the chance to respond, reset the system by simultaneously typing the <Ctrl>, <Alt> and <Delete> keys, or by pushing the ' Reset ' button on the system cabinet. You can also restart by turning the system OFF then ON.

### 3.2 CMOS SETUP UTILITY

To access the AWARD BIOS SETUP program, press the <DEL> key. The screen display will appear as :

#### Main Program Screen

ROM PCI / ISA BIOS (2A59IL7M) CMOS SETUP UTILITY AWARD SOFTWARE, INC.	
<b>STANDARD CMOS SETUP</b> BIOS FEATURES SETUP CHIPSET FEATURES SETUP POWER MANAGEMENT SETUP PNP / PCI CONFIGURATION LOAD BIOS DEFAULTS LOAD SETUP DEFAULTS	INTEGRATED PERIPHERALS SUPERVISOR PASSWORD USER PASSWORD IDE HDD AUTO DETECTION HDD LOW LEVEL FORMAT SAVE & EXIT SETUP EXIT WITHOUT SAVING
ESC : Quit F10 : Save & Exit Setup	 : Select Item (Shift) F2 : Change Color
Time, Date, Hard Disk Type....	

This screen provides access to the utility's various functions.

Listed below are explanation of the keys displayed at the bottom of the screen:

<ESC> : Exit the utility.

<  > : Use arrow keys  to move cursor to your desired selection.

<F10> : Saves all changes made to Setup and exits program.

<Shift> <F2> : Changes background and foreground colors.

### 3.3 STANDARD CMOS SETUP

When you select the "STANDARD CMOS SETUP" on the main program, the screen display will appear as :

#### Standard CMOS Setup Screen

ROM PCI / ISA BIOS(2A59IL7M)									
STANDARD CMOS SETUP									
AWARD SOFTWARE, INC.									
Date (mm : dd : yy) : Mon, Aug 24 1998									
Time (hh : mm : ss) : 14 : 59 : 24									
HARD DISKS	TYPE	SIZE	CYLS	HEAD	PRECOMP	LANDZ	SECTOR	MODE	
Primary Master	: Auto	0	0	0	0	0	0	AUTO	
Primary Slave	: Auto	0	0	0	0	0	0	AUTO	
Drive A : 1.44M, 3.5 in.									
Drive B : None									
Floppy 3 Mode Support : Drive A									
Video : EGA/VGA									
Halt On : All Error									
					Base Memory : 640K				
					Extended Memory : 64512K				
					<u>Other Memory : 384K</u>				
					Total Memory : 65536K				
ESC : Quit			: Select Item			PU / PD / + / - : Modify			
F1 : Help			(Shift) F2 : Change Color						

The Standard CMOS Setup utility is used to configure the following components such as date, time, hard disk drive, floppy drive, display and memory. Once a field is highlighted, on-line help information is displayed in the left bottom of the Menu screen.

**Set Date** : Month, Date, Year.

**Set Time** : Hour, Minute and Second. Use 24-hour clock format (for p.m. time, add 12 to the hour number, you would enter 4:30 p.m. as 16:30). When you select the "STANDARD CMOS SETUP" on the main program, the screen display will appear as:

**Hard Disks** : There are four hard disks listed: "Primary Master", "Primary Slave", "Secondary Master" and "Secondary Slave". For each IDE channel, the first device is

the “Master” and the second device is “Slave”. Hard disk types from 1 to 45 are the standard ones. To select or change the configuration, move the cursor to the desired position and press <Page Up> or <Page Down> to change the option : (1) Press “Auto” for IDE HDD auto detection, (2) Press “User” for user definable, and Press “None” for not installed (e.g. SCSI). There are six categories of information that you must enter for a HDD: “CYLS.” for (number of cylinders), “HEADS” for (number of heads), “PRECOMP” for (write pre-compensation), “LANDZ” for (landing zone), “SECTOR” for (number of sectors) and ”MODE“ for (Normal, LBA, LARGE and AUTO). The hard disk vendor’s or system manufacturer’s documentation should provide you with the drive specifications. For an IDE hard drive, you can set “TYPE” to “Auto” or use the “IDE HDD AUTO DETECTION” utility in the main program screen to enter the drive specifications.

Here is a brief explanation of drive specifications:

- ✦ Type : The BIOS contains a table of pre-defined drive types. Each defined drive type has specified number of cylinders, number of heads, write compensation factor, landing zone, and number of sectors. Drives whose specifications do not accommodate any pre-defined type are classified as type USER.
- ✦ Size : Disk drive capacity (approximate). Note that this size is usually slightly greater than the size of a formatted disk given by a diskchecking program.
- ✦ Cyls : Number of cylinders.
- ✦ Head : Number of heads.
- ✦ Precomp : Write precompensation cylinder
- ✦ Landz : Landing zone.
- ✦ Sector : Number of sectors.
- ✦ Mode : Auto, Normal, Large, or LBA.
  - Auto: The BIOS automatically determines the optimal mode.
  - Normal: Maximum number of cylinders, heads, and sectors supported are 1024, 16, and 63.
  - Large: For drives that do not support LBA and have more than 1024 cylinders.
  - LBA (Logical Block Addressing): During drive accesses, the IDE controller transforms that data address described by sector, head, and cylinder number into a physical block address, significantly improving data transfer rates. For drives with greater 1024 cylinders.

The AWARD BIOS supports three HDD modes: NORMAL, LBA and LARGE.

**NORMAL mode** : This is a Generic Access mode in which neither the BIOS nor the IDE controller will make any transformation during the accession. The maximum HDD size is supported by the NORMAL mode that is 528 Megabytes.

**LBA mode** : This is a Logical Block Addressing mode which is a HDD accessing method to overcome the 528 Megabytes restriction. The number of cylinders, heads and sectors that are shown in setup may not be the physical number contained in the HDD.

During the HDD accessing, the IDE controller will transform the logical address that is described by the cylinder, head and sector numbers into its own physical address as contained inside the HDD. The maximum HDD size that is supported by the LBA mode is 8.4 Gigabytes.

**LARGE mode** : Some IDE HDD contains more than 1024 cylinders without the LBA support. This access mode tricks DOS (or other OS) with the number of cylinders that is less than 1024 by dividing it by 2. At the same time, the number of heads is multiplied by 2. A reverse transformation process will be made inside INT13H in order to access to the right HDD address. The maximum HDD size that is supported by the LARGE mode is 1 Gigabytes.

- Note :
1. To support LBA or LARGE mode, there are softwares located in the AWARD HD Service Routine“INT13H”. It may fail to access a HDD with LBA or LARGE modes selected if you are running under an Operating System that replaces the whole INT13H service routine.
  2. Entering incorrect drive specifications will result in a hard disk drive that will function improperly or no function at all.

**Drive A and Drive B** : Select the correct specifications for the diskette drive(s) installed in the computer.

None	No diskette drive installed
360K, 5.25 in	5-1/4 inch PC-type standard drive; 360 kilobyte capacity
1.2M, 5.25 in	5-1/4 inch AT-type high-density drive; 1.2 megabyte capacity
720K, 3.5in	3 1-2 inch double-sided drive; 720 kilobyte capacity
1.44M, 3.5 in	3 1-2 inch double-sided drive; 1.44 megabyte capacity
2.88M, 3.5 in	3 1-2 inch double-sided drive; 2.88 megabyte capacity

- Note :
1. Not Installed could be used as an option for diskless workstations..
  2. Highlight the listing after each drive name and select the appropriate entry.

**Floppy 3 Mode Support** : When Enabled, the BIOS supports a type of 3.5-inch diskette drive that can read 720-KB, 1.2-MB, and 1.44-MB diskettes.

**Video** : Select the type of primary video subsystem in your computer. The BIOS usually detects the correct video type automatically. The BIOS supports a secondary video subsystem, but you do not select it in Setup.

EGA/VGA	Enhanced Graphics Adapter/Video Graphics Array. For EGA, VGA, SEGA, SVGA or PGA monitor adapters.
CGA 40	Color Graphics Adapter, power up in 40 column mode
CGA 80	Color Graphics Adapter, power up in 80 column mode
MONO	Monochrome adapter, includes high resolution monochrome adapters

**Halt On** : During the power-on-self-test (POST), the computer stops if the BIOS detects a hardware error. You can tell the BIOS to ignore certain errors POST and continue the boot-up process. These are the selections:





No errors	Whenever the BIOS detects a non-fatal error the system will not be stopped and you will be prompted
All errors	The system boot will be stopped for any error that may be detected.
All, But Keyboard	The system boot will not stop for a keyboard error ; it will stop for all Other errors.
All, But Diskette	The system boot will not stop for a disk error ; it will stop for all other Errors.
All, But Disk/Key	The system boot will not stop for a keyboard or disk error ; it will stop for all other errors.



### 3.4 BIOS Features Setup

When you select the “BIOS FEATURES SETUP” on the main program, the screen display will appears as:

#### BIOS Features Setup Screen

ROM PCI / ISA BIOS (2A59IL7M)	
BIOS FEATURES SETUP	
AWARD SOFTWARE, INC	
Virus Warning	: Disabled
CPU Internal Cache	: Enabled
External Cache	: Enabled
Quick Power On Self Test	: Enabled
Boot Sequence	: A,C,SCSI
Swap Floppy Drive	: Disabled
Boot Up Floppy Seek	: Disabled
Boot Up NumLock Status	: On
Boot Up System Speed	: High
Gate A20 Option	: Fast
Typematic Rate Setting	: Disabled
Typematic Rate (Chars Sec.)	: 6
Typematic Delay (M/Sec)	: 250
Security Option	: Setup
PCI/VGA Palette Snoop	: Disabled
Assign IRQ For DRAM	: Disabled
OS Select For DRAM	: Non-OS/2 >64MB
Video BIOS Shadow	: Enabled
C8000 -CBFFF Shadow	: Disabled
CC000 -CFFFF Shadow	: Disabled
D0000 -D3FFF Shadow	: Disabled
D4000 -D7FFF Shadow	: Disabled
D8000 -DBFFF Shadow	: Disabled
DC000 -DFFFF Shadow	: Disabled
Cyrix 6X86/MII CPUID	: Enabled
ESC	: Quit     : Select Item
F1	: Help PU /PD /+/- : Modify
F5	: Old Values (Shift) F2 : Color
F6	: Load BIOS Defaults
F7	: Load Setup Defaults

The following explains the options for each of the features as listed in the above menu:

**Virus Warning** : The default setting of the Virus Warning is “Disabled”. When it is enabled, any attempt to write the boot sector and partition table will halt the system and cause a warning message to appear. If this happens, you can use an anti-virus utility

on a virus free, bootable floppy diskette to reboot, to clean and to investigate your system.

**CPU Internal Cache** : The default setting is "Enabled". This setting enables the CPU internal cache .

**External Cache** : The default setting is "Enabled". This setting enables the external cache.

**Quick Power On Self Test** : The default setting is "Enabled". This speeds up the Power On Self Test (POST) by skipping some items that are normally checked during the full POST. If your system is functioning normally, you can choose this feature to speed the booting process.

**Boot Sequence** : The default setting is "C:,A:"; the other options are "CDROM, C, A" and "A, C" and "C, CDROM, A". This setting determines where the computer looks first for an operating system, the hard disk, or the floppy disk, ...or other. The BIOS will load the operating system from the disk drives in the sequence as selected here.

**Swap Floppy Drive** : The default setting is "Disabled". This setting gives you an option to swap A and B floppy disks. Normally, the floppy drive A is the one at the end of the cable and drive B is at the other end. If you set this option to "Enabled", the Drive A will function as Drive B, and vice-versa under the DOS.

**Boot Up Floppy Seek** : The defaults setting is "Disabled". When enabled, the BIOS will check whether there is a floppy disk drive installed.

**Boot Up Numlock Status** : The default setting is "On". If set "Off", the cursor controls will function on the numeric keypad.

**Boot Up System Speed** : Select High to boot at the default CPU speed; select Low to boot at the speed of the AT bus. Some add-in peripherals or old software (such as old games) may require a slow CPU speed. The default setting is "High".

**Gate A20 Option** : The default setting is "Fast". This is the optimal setting for the CPU card. The other option is "Normal".

**Typematic Rate Setting** : The default setting is "Disabled". If enabled, you can set the typematic rate and typematic delay.

**Typematic Rate (Chars/Sec)** : This setting controls the speed at which the system registers the repeated keystrokes. The choices range from 6 to 30 Chars/Sec. The default setting is “6” Chars/Sec.

**Typematic Delay (Msec)** : This setting controls the time between the display of the first and second characters. There are four delay choices: 250ms, 500ms, 750ms and 1000ms. The default setting is “250” ms.

**Security Option** : This setting controls the password in the main screen. The options are “Setup” and “System”. Select “Setup” and it will protect the Setup Utility settings from being tampered with. Select “System” if you want to use password feature every time the system boots up. The default setting is “Setup”. You can create your password by using the “SUPERVISOR/USER PASSWORD” utility on the main program screen.

**PCI/VGA Palette Snoop** : The default setting is “Disabled”. Set to “Enable” if any ISA adapter card installed requires VGA palette snooping.

**OS Select For DRAM > 64MB** : The default setting is “Non-OS2”. Set to “OS2” if the system memory size is greater than 64MB and the operating system is OS/2.

**Video BIOS Shadow** : The default setting is “Enabled” which will copy the VGA display card BIOS into system DRAM to improve performance.

**C8000-CBFFF Shadow to DC000-DFFFF Shadow** : The default setting for the shadow feature is “Disabled”. When enabled, the ROM with the specific address is copied into system DRAM. It will also reduce the size of memory available to the system. After you have made your selection in the BIOS FEATURES SETUP, press the <ESC> key to go back to the main program screen.

### 3.5 CHIPSET FEATURES SETUP

When you select the “CHIPSET FEATURES SETUP” on the main program, the screen display will appears as:

#### Chipset Features Setup Screen

ROM PCI / ISA BIOS (2A59IL7M)			
CHIPSET FEATURES SETUP			
AWARD SOFTWARE, INC.			
AUTO Configuration	: Enabled	Auto Detect DIMM/PCI Clk	Enabled
DRAM Timing	: 70 ns	Spread Spectrum Modulated	Disabled
DRAM Leadoff Timing	: 10 / 6 / 4	CPU Warning Temperature	: Disabled
DRAM Read Burst (EDO/FP)	: x333/x444	Current CPU Temperature	: 32°C / 89°F
DRAM Write Burst Timing	: x333	Current CPUFAN Speed	: 3934 RPM
Fast EDO Lead Off	: Disabled	Current SYSFAN Speed	: 4210 RPM
Refresh RAS# Assertion	: 5 Clks	Current VCORE (V)	: 1.90 V
Fast RAS To CAS Delay	: 3	Current VCC3 (V)	: 3.28 V
DRAM Page Idle Timer	: 2 Clks	Current +12V (V)	: 11.16 V
DRAM Enhanced Paging	: Enabled	Current VCC (V)	: 5.00 V
Fast MA to RAS# Delay	: 2 Clks		
SDRAM (CAS Lat/RAS-to-CAS)	: 3 / 3		
SDRAM Speculative Read	: Disabled		
System BIOS Cacheable	: Enabled		
Video BIOS Cacheable	: Enabled		
8 Bit I/O Recovery Time	: 1	ESC : Quit	⏏ ⏏ ⏏ ⏏ : Select Item
16 Bit I/O Recovery Time	: 2	F1 : Help	PU /PD /+/- : Modify
Memory Hole At 15M-16M	: Disabled	F5 : Old Values	(Shift) F2 : Color
PCI 2.1 Compliance	: Disabled	F6 : Load BIOS Defaults	
		F7 : Load Setup Defaults	

**Auto Configuration** : The default setting is “Enabled” which will optimize DRAM timing automatically depending on whether the DRAM used is either 70ns or 60ns. The other option is “Disabled” which allows you to change DRAM timing manually.

**DRAM Timing** : The value in this field depends on the performance parameters of the installed memory chips (DRAM). Do not change the value from the factory settings unless you install new memory that has a different performance rating than the original DRAMs.

**DRAM Leadoff Timing** : Select the combination of CPU clocks the DRAM on your board requires before each read from or write to the memory. Changing the value from the setting determined by the board designer for the installed DRAM may cause memory errors.

**DRAM Read Burst (EDO/FP)** : Sets the timing for reads from EDO (Extended Data Output) or FPM (Fast Page Mode) memory. The lower the timing numbers, the faster the system addresses memory. Selecting timing numbers lower than the installed DRAM is able to support can result in memory errors.

**DRAM Write Burst Timing** : Your system designer should select the timing that the system uses when reading from and writing to DRAM. Do not reset from the factory default value.

**Fast EDO Lead Off** : Select Enabled only for EDO DRAMs in either a synchronous cache or a cacheless system. It causes a 1-HCLK pull-in for all read leadoff latencies for EDO DRAMs (i.e., page hits, page misses, and row misses). Select Disabled if any of the DRAM rows are populated with FPM DRAMs.

**Refresh RAS# Assertion** : Select the number of clock cycles in which RAS# is asserted for refresh cycles.

**Fast RAS to CAS Delay** : This field lets you insert a timing delay between the CAS and RAS strobe signals, used when DRAM is written to, read from, or refreshed. Fast gives faster performance; and Slow gives more stable performance. This field applies only when synchronous DRAM is installed in the system.

**DRAM Page Idle Timer** : Select the amount of time in HCLKs that the DRAM controller waits to close a DRAM page after the CPU becomes idle.

**DRAM Enhanced Paging** : When Enable, the chipset keeps the page open until a page/row miss. When Disabled, the chipset uses additional information to keep the DRAM page open when the host may be "right back".

**Fast MA to RAS# Delay Clk** : The values in this field are set by the system board designer, depending on the DRAM installed. Do not change the values in this field unless you change specifications of the installed DRAM or installed CPU.

**SDRAM (CAS Lat/RAS-to-CAS)** : You can select a combination of CAS latency and RAS-CAS delay in HCLKs of 2/2 or 3/3. The system board designer should set the values in this field, depending on the DRAM installed. Do not change the values in this field unless you change specification of the installed DRAM or the installed CPU.

**SDRAM Speculative Read** : The chipset can “speculate” on a DRAM read address, thus reducing read latencies. The CPU issues a read request containing the data memory address. The DRAM controller receives the request. When this field is Enable, the controller issues the read command slightly before it has finished decoding the data address.

**System BIOS Cacheable** : Selecting Enabled allows caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

**Video BIOS Cacheable** : Selecting Enabled allows caching of the video BIOS ROM at C0000h to C7FFFh, resulting in better video performance. However, if any program writes to this memory area, a system error may result.

**8/16 Bit I/O Recovery Time** : The I/O recovery mechanism adds bus clock cycles between PCI-originated I/O cycles to the ISA bus. This delay takes place because the PCI bus is so much faster than the ISA bus. These two fields let you add recovery time (in bus clock cycles) for 16-bit and 8-bit I/O.

**Memory Hole At 15M-16M** : You can reserve this area of system memory for ISA adapter ROM. When this area is reserved, it cannot be cached. The user information of peripherals that need to use this area of system memory usually discusses their memory requirements.

**PCI 2.1 Compliance** : The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select Enable to support compliance with PCI specification version 2.1.

**Auto Detect DIMM/PCI Clk** : The default setting is “Enabled”. To reduce the occurrence of electromagnetic interference (EMI), the BIOS detects the presence or absence of components of DIMM and PCI slots and turns off system dock generator pulses to empty slots.

**CPU Warning Temperature** : The default setting is “66°C/151°F”. Select the combination of lower and upper limits for the CPU temperature. If the CPU temperature extends beyond either limit, any warning mechanism programmed into your system will be activated.

**Current CPU Temperature** : The default setting is “32°C/89°F”. This field displays the current CPU temperature, if your computer contains a monitoring system.

**Current CPUFAN Speed** : The default setting is “3934 RPM”. This field displays the current speed of the CPU fan, if your computer contains a monitoring system.

**Current SYSFAN Speed** : The default setting is “4210 RPM”. This field displays the current speed of the SYS fan, if your computer contains a monitoring system.





**Current VCORE (V), Current VCC3 (V), Current +V12 (V) and Current VCC (V)**: This field detects the voltage input of the board, if your computer contains a monitoring system.

After you have made your selections in the CHIPSET FEATURES SETUP, press the <ESC> key to go back to the main program screen.

### 3.6 POWER MANAGEMENT SETUP

The “Power Management Setup” controls the CPU card’s “Green” features. When you select the “POWER MANAGEMENT SETUP” on the main program, the screen display will appear as:

#### Power Management Setup Screen

ROM PCI / ISA BIOS (2A59IL7M) POWER MANAGEMENT SETUP AWARD SOFTWARE, INC.	
Power Management : Disabled PM Control by APM : Yes Video Off Method : V /H SYNC+ Blank Video Off After : Standby MODEM Use IRQ : 3 Doze Mode : Disabled Standby Mode : Disabled HDD Power Down : Disabled Throttle Duty Cycle : 62.5% ZZ Active in Suspend : Disabled PCI / VGA Act-monitor : Disabled  PowerOn by Ring : Disabled IRQ 8 Break Suspend : Disabled	** Reload Global Timer Events ** IRQ [3 - 7, 9 - 15], NMI : Enabled Primary IDE 0 : Disabled Primary IDE 1 : Disabled Secondary IDE 0 : Disable Secondary IDE 1 : Disable Floppy Disk : Disabled Serial Port : Enabled Parallel Port : Disabled
ESC : Quit     : Select Item F1 : Help      PU /PD/+/- : Modify F5 : Old Values ( Shift ) F2 : Color F6 : Load BIOS Defaults F7 : Load Setup Defaults	

**Power Management :** This setting controls the System Doze Mode, Standby Mode and Suspend Mode Timer features. There are four options namely --

**User Define :** Allows you to customize all power saving timer features.

**Optimize :** This is the recommended setting for general use.

**Test/Demo :** This is for test/demonstration purpose.

**Disable :** Disable the power management features.



**PM Control by APM** : The default setting is “No”. If it is set to “Yes”, the system BIOS will wait for APM’s prompt before it enters any PM mode.

Note : If your system power management is controlled by APM and there is a task running, the APM will not prompt the BIOS to enter any power saving mode after time out.

**Video Off Method** : This setting controls the Video off method in power saving mode. The default setting is “V/H SYNC+Blank”. This setting disables V/H SYNC signals and blanks the screen into the power saving mode. Other options are “Blank Screen” and “DPMS”.

**Video Off After** : As the system moves from lesser to greater power-saving modes, select the mode in which you want the monitor to blank. The default setting is “Standby”.

**MODEM Use IRQ** : Name the interrupt request (IRQ) line assigned to the modem (if any) on your system. Activity of the selected IRQ always awakens the system. The default setting is “NA”.

**Doze Mode** : Options are from “1 Min.” to “1 Hour” and “Disable”. The system speed will change from turbo to slow and the video signal will be suspended, if no Power Management events occur for a specified length of time. Full power function will return when a Wake-Up event is detected.

**Standby Mode** : Options are from “1 Min” to “1 Hour” and “Disable”. The system speed will change from turbo to slow and the video signal will be suspended, if no Power Management events occur for a specified length of time. Full power function will return when a Wake-Up event is detected.

**Suspend Mode** : Option are from “1 Min” to “1 Hour” and “Disable”. The CPU clock will be stopped and the video signal will be suspended, if no Power Management events occur for a specified length of time. Full power function will return when a Wake-Up event is detected.

**HDD Power Down** : Options are from “1 Min.” to “15 Min.” and “Disable”. The IDE hard drive will spin down if it is not accessed within a specified length of time.

**Throttle Duty Cycle** : When the system enters Doze mode, the CPU clock runs only part of the time. You may select the percent of time that the clock runs.

**ZZ Active in Suspend** : When Enable, the ZZ signal is active during Suspend mode.

**PCI/VGA Act-Monitor** : When Enabled, any video activity restarts the global timer for Standby mode. The default setting is "Disabled".

**IRQ 8 Break Suspend** : You can turn On or Off monitoring of IRQ8 (the Real Time Clock) so it does not awaken the system from Suspend mode.

**Reload Global Timer Events** : When Enabled, an event occurring on each device listed below restarts the global time for Standby mode.

**IRQ [3-7, 9-15], NMI**

**Primary IDE 0**

**Primary IDE 1**

**Secondary IDE 0**

**Secondary IDE 1**

**Floppy Disk**

**Serial Port**

**Parallel Port**





After you have made your selection in the POWER MANAGEMENT SETUP, press the <ESC> key to go back to the main program screen.

### 3.7 PNP/PCI CONFIGURATION

Both the ISA and PCI buses on the CPU card use system IRQs & DMAs. You must set up the IRQ and DMA assignments correctly through the PnP/PCI Configuration Setup utility, otherwise the SBC will not work properly.

When you select the “PnP /PCI CONFIGURATION” on the main program, the screen display will appears as:

#### PnP/PCI Configuration Setup Screen

ROM PCI / ISA BIOS (2A59IL7M)	
PNP/PCI CONFIGURATION	
AWARD SOFTWARE, INC.	
PNP OS Installed : No	PCI IDE IRQ Map To : PCI-AUTO
Resources Controlled By : Manual	Primary IDE INT# : A
Reset Configuration Data : Disabled	Secondary IDE INT# : B
IRQ-3 assigned to : Legacy ISA	Assign IRQ For USB : Disabled
IRQ-4 assigned to : Legacy ISA	Used MEM base addr : N/A
IRQ-5 assigned to : PCI/ISA PnP	
IRQ-7 assigned to : PCI/ISA PnP	
IRQ-9 assigned to : PCI/ISA PnP	
IRQ-10 assigned to : Legacy ISA	
IRQ-11 assigned to : Legacy ISA	
IRQ-12 assigned to : PCI/ISA PnP	
IRQ-14 assigned to : PCI/ISA PnP	
IRQ-15 assigned to : PCI/ISA PnP	
DMA-0 assigned to : PCI/ISA PnP	
DMA-1 assigned to : PCI/ISA PnP	ESC : Quit     : Select Item
DMA-3 assigned to : PCI/ISA PnP	F1 : Help PU /PD/+/- : Modify
DMA-5 assigned to : PCI/ISA PnP	F5 : Old Values ( Shift ) F2 : Color
DMA-6 assigned to : PCI/ISA PnP	F6 : Load BIOS Defaults
DMA-7 assigned to : PCI/ISA PnP	F7 : Load Setup Defaults

**PNP OS Installed** : Select Yes if the system operating environment is Plug-and-Play aware (e.g., Windows 95).

**Resources Controlled By :** The Plug and Play BIOS can automatically configure all the boot and Plug and Play-compatible devices. If you select Auto, all the interrupt request (IRQ) and DMA assignment fields disappear, as the BIOS automatically assigns them.

**Reset Configuration Data :** Normally, you leave this field Disabled . Select Enabled to reset Extended System Configuration Data (ESCD) when you exit Setup if you have installed a new add-on and the system reconfiguration has caused such a serious conflict that the operating system cannot boot.

**IRQ n Assigned to :** When resources are controlled manual, assign each system interrupt as one of the following types, depending on the type of device using the interrupt:

Legacy ISA Devices compliant with the original PC AT bus specification, requiring a specific interrupt (such as IRQ4 for serial port 1).

PCI/ISA PnP Devices compliant with the Plug and Play standard, whether designed for PCI or ISA bus architecture.

**DMA n Assigned to :** When resources are controlled manual, assign each system DMA channel as one of the following types, depending on the type of device using the interrupt:

Legacy ISA Devices compliant with the original PC AT bus specification, requiring a specific DMA channel.

PCI/ISA PnP Devices compliant with the Plug and Play standard, whether designed for PCI or ISA bus architecture.

**PCI IDE IRQ Map To :** This field lets you select PCI IDE IRQ mapping or PC AT (ISA) interrupts. If your system does not have one or two PCI IDE connectors on the system board, select values according to the type of IDE interface(s) installed in your system (PCI or ISA). Standard ISA interrupt for IDE channels are IRQ14 for primary and IRQ15 for secondary.

**Assign IRQ For USB :** To use USB feature, set the IRQ to "Enabled". The default setting is "Disabled" .

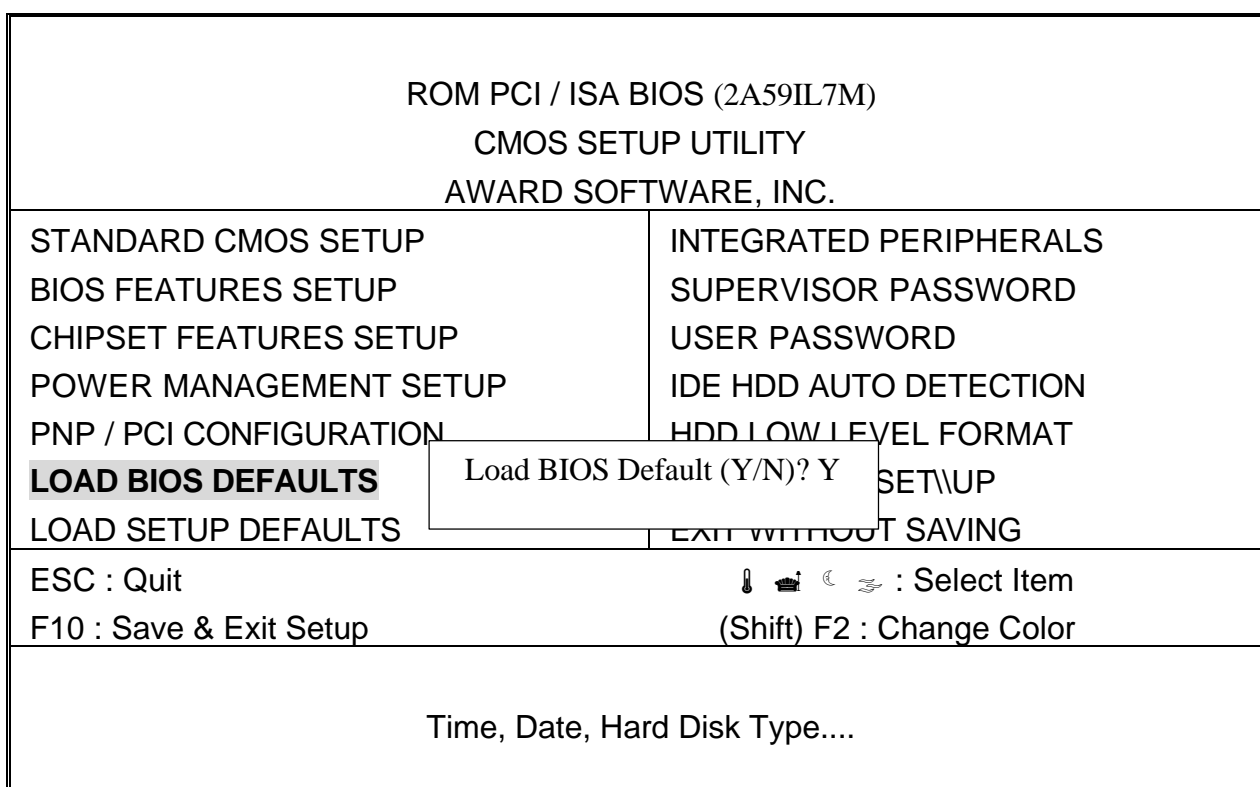
**Used MEM base addr :** Select a base address for the memory area used by any peripheral that requires high memory.



### 3.8 LOAD BIOS DEFAULTS

The BIOS defaults have been set by the manufacturer which represent settings provided with the minimum requirements for your system to operate. “ **Load BIOS Defaults** ” loads the troubleshooting default values permanently recorded in the BIOS ROM. These settings are non-optimal and turn off all high performance features.

The Standard CMOS Setup screen is not affected. To use this feature, highlight it on the main screen and press <Enter>. A line will appear asking if you want to load the BIOS default values. Press the <Y> key and the <Enter>. The default settings will load. Press <N> if you do not want to proceed.







### 3.9 LOAD SETUP DEFAULTS

“LOAD SETUP DEFAULTS” loads the optimal settings which are stored in BIOS ROM. The defaults loaded affect only the BIOS Features Setup, Chipset Features Setup, Power Management Setup, PnP/PCI configuration setup and Integrated Peripherals Setup. There is no effect on the Standard CMOS Setup. To use this feature, highlight the entry on the main screen and press <Enter>. A line will appear on the screen asking if you want to load the Setup default values. Press the <Y> key and then press the <Enter> key if you want to load the Setup defaults. Press <N> if you do not want to proceed.

### 3.10 INTEGRATED PERIPHERALS

When you select the “INTEGRATED PERIPHERALS” on the main program, the screen display will appear as:

#### Integrated Peripheral Setup Screen

ROM PCI / ISA BIOS (2A59IL7M)			
INTEGRATED PERIPHERALS			
AWARD SOFTWARE, INC.			
IDE HDD Block Mode	: Enabled	Onboard Parallel Port	: 378/IRQ7
IDE Primary Master PIO	: Auto	Parallel Port Mode	: SPP
IDE Primary Slave PIO	: Auto		
IDE Primary Master UMDA	: Auto	Onboard Serial Port 3	: 3E8H
IDE Primary Slave UMDA	: Auto	Serial Port 3 Use IRQ	: IRQ10
IDE Secondary Master PIO	: Auto	Onboard Serial Port 4	: 2E8H
IDE Secondary Slave PIO	: Auto	Serial Port 4 Use IRQ	: IRQ11
IDE Secondary Master UDMA	: Auto		
IDE Secondary Slave UDMA	: Auto		
On-Chip Primary PCI IDE	: Enabled		
On-Chip Secondary PCI IDE	: Enabled		
USB Keyboard Support	: Disabled		
KBC input clock	: 8 MHz		
Onboard FDC Controller	: Enabled	ESC : Quit	    : Select Item
Onboard Serial Port 1	: 3F8 / IRQ4	F1 : Help	PU /PD/+/- : Modify
Onboard Serial Port 2	: 2F8 / IRQ3	F5 : Old Values ( Shift )	F2 : Color
UART Mode Select	: Normal	F6 : Load BIOS Defaults	
		F7 : Load Setup Defaults	

**IDE HDD Block Mode** : Block mode is also called block transfer, multiple commands, or multiple sector read/write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support.

**IDE Primary/Secondary Master/Slave PIO** : There are four IDE PIO (Programmed Input/Output) fields let you set a PIO mode (0-4) for each of the four IDE devices that the onboard IDE interface supports. Modes 0 through 4 provide successively increased performance. In Auto mode, the system automatically determines the best mode for each device.

**IDE Primary/Secondary Master/Slave UDMA** : Ultra DMA/33 implementation is possible only if your IDE hard drive supports it and the operating environment includes a DMA driver (Window 95 OSR2 or a third-party IDE bus master driver). If your hard drive and your system software both support Ultra DMA/33, select Auto to enable BIOS support.

**On-Chip Primary/Secondary PCI IDE** : The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select Enabled to activate each channel separately.

**USB Keyboard Support** : Select Enabled if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard.

**KBC input clock** : The system designer must select the correct frequency for the keyboard controller input clock. Do not change this value from default value.

**Onboard FDC Controller** : Select Enabled if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install an add-in FDC or the system has no floppy drive, select Disabled in this field.

**Onboard Serial Port 1 / 2:** Select an address and corresponding interrupt for the first and second serial ports.

**Onboard Parallel Port** : Select a logical LPT port name and matching address for the physical parallel (printer) port.

**Parallel Port Mode:** Select an operating mode for the onboard parallel (printer) port. Select Normal, unless your hardware and software require one of the other modes offered in this field.



### 3.11 SUPERVISOR/USER PASSWORD

The “SUPERVISOR/USER PASSWORD” utility sets the password. The SBC is shipped with the password disabled. If you want to change the password, you must first enter the current password, then at the prompt -- enter your new password. The password is case sensitive, and can be up to 8 alphanumeric characters. Press <Enter> after you have finished typing in the password. At the next prompt, confirm the new password by re-typing it and pressing <Enter> again. When you are done, the screen automatically reverts to the main screen. Remember that when you use this feature, the “Security Option” line in BIOS FEATURES SETUP will determine when entering the password will be required.

**To disable the password**, press the <Enter> key instead of entering a new password when the “Enter Password” in the dialog box appears. A message will appear confirming that the password is disabled.

If you have set both supervisor and user password, only the supervisor password allows you to enter the BIOS SETUP PROGRAM.

Note : If you forget your password, the only way to solve this problem is to discharge the CMOS memory by turning power off and placing a shunt on the S1 (open pad) for 5 seconds, then removing the shunt.

### **3.12 IDE HDD AUTO DETECTION**

If your system has an IDE hard drive, you can use this utility to detect its parameters and enter them into the Standard CMOS Setup automatically.

If the auto-detected parameters displayed do not match the ones that should be used for your hard drive, do not accept them. Press the <N> key to reject the values and enter the correct ones manually on the Standard CMOS Setup screen.

Note :If you are setting up a new hard disk drive (nothing on it) that supports LBA mode, more than one line will appear in the parameter box, choose the line that lists LBA for an LBA drive.

Do not choose Large or Normal if the hard disk drive is already fully formatted when you install it, choose the mode "HDD Low Level Format" to format it.

### **3.13 HDD LOW LEVEL FORMAT**

Select and highlight this option press the <Enter> key to enable you to perform the low level format of hard disk drive.

### **3.14 SAVE & EXIT SETUP**

Select this option and press the <Enter> key to save the new setting information in the CMOS memory and continue with the booting process.

### **3.15 EXIT WITHOUT SAVING**

Select this option and press the <Enter > key to exit the Setup Utility without recording any new values or changing old ones.

## **CHAPTER 4.**

### ***DRIVERS***

### **SUPPORT**

#### **4.1 DRIVERS OF CD-ROM LIST**

The Table list below shows the drivers supported for EM-586EBX.

<b>Diskette Name</b>	<b>Driver Directory Name</b>
Genisys	Win 95/98 Nt 40
Cnt69k	Win31,Win95,Win98 Nt40
Tx- Drv	Winp2x4.exe
ess1938s	Win 95 Win 98 Nt40
Rtk8139a	Win 95/98, Linux, DOS, Nt40, OS/2

## APPENDIX A

### HOW TO USE WATCH-DOG TIMER

To use the watch-dog timer :

**Step 1.** Enable and retrigger the Watch-Dog timer: **443H**

**Step 2.** Disable: **43H**

#### **EX.1: For DOS**

Execute the **DEBUG.EXE** file under DOS, Then key-in **i443**. The system will reboot automatically according to the time-out you set.

For example, if you want to Set **4 seconds** for the time-out, you should set **JP4: 4-8 ON** and **J3: 1-2 ON** to enable watch-dog timer.

```
C:\DOS> DEBUG
-i443
```

#### **EX.2: For assemble Language**

```
Enable :
      :
      :
MOV  DX, 443H
IN AL, DX
      :
      :

Disable :
      :
      :
IN AL, 43H
      :
      :
```

## **APPENDIX B**

### **TECHNICAL REFERENCE**

#### **I/O PORT ADDRESS MAP**

<b>Address</b>	<b>Function</b>
000 - 01F	DMA Controller #1
020 - 03F	Interrupt Controller #1
040 - 05F	Timer Chip
043	Disable Watch-Dog Times Operation (Read)
060 - 06F	Keyboard Controller
070 - 07F	Read Time Clock/NMI Mask
080 - 09F	DMA Page Register
0A0 - 0BF	Interrupt Controller #2
0C0 - 0DF	DMA Controller #2
0F0 - 0F1	Clear/Reset Math Coprocessor
1F0 - 1F7	Hard Disk Controller
200 - 210	Game Port
278 - 27F	Parallel Port #2
2E8 - 2EF	Serial Port #4 (COM 4)
2F8 - 2FF	Serial Port #2 (COM 2)
300 - 31F	prototype Card/Streaming Tape Adapter
360 - 36F	PC Network
378 - 3FF	Parallel Port #1
380 - 38F	SDLC #2
3A0 - 3AF	SDLC #1
3B0 - 3BF	MDA Video Card (Including LPT0)
3C0 - 3CF	EGA Card
3D0 - 3DF	CGA Card
3E8 - 3EF	Serial Port #3 (COM 3)
3F0 - 3F7	Floppy Disk Controller
3F8 - 3FF	Serial Port #1 (COM 1)
443	Enable Watch-dog Timer Operation (read)

## MEMORY ADDRESS MAP

Address Range (Hex)	Description
000000H - 09FFFFH	640 KB of Conventional RAM
0A0000H - 0BFFFFH	128 KB of Video RAM
0C0000H - 0EFFFFH	256 KB of I/O Expansion ROM
0F0000H - 0FFFFFFH	64 KB of System BIOS ROM
0100000H - 7FFFFFFFH	1 MB ~ 128MB of User RAM

## DMA CHANNELS

CHANNEL	Function
DMA 0	Reserved
DMA 1	Reserved
DMA 2	Floppy Disk Controller
DMA 3	ECP Parallel Port
DMA 4	Cascade for DMA #1
DMA 5	Reserved
DMA 6	Reserved
DMA 7	Reserved

## INTERRUPT CONTROLLER

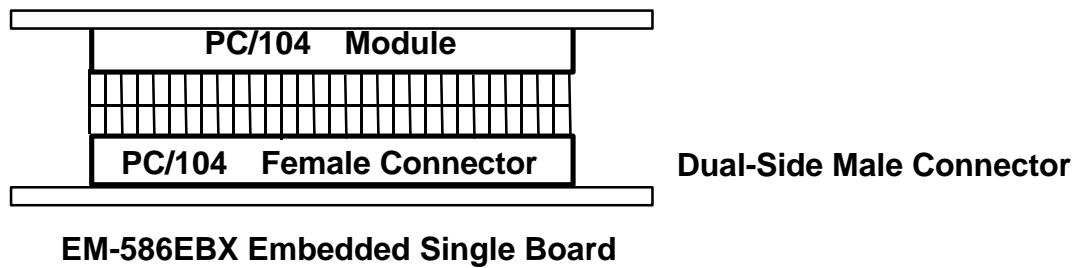
IRQ	Function
IRQ 0	System timer output
IRQ 1	Keyboard
IRQ 2	Cascade for INTC #2
IRQ 3	Serial port #2
IRQ 4	Serial port #1
IRQ 5	Parallel port #2
IRQ 6	Floppy disk controller
IRQ 7	Parallel port #1
IRQ 8	Real time clock
IRQ 9	Software redirected to INT 0AH (IRQ 2)
IRQ 10	Reserved
IRQ 11	Reserved
IRQ 12	PS/2 Mouse
IRQ 13	Math Coprocessor (CPU Internal)
IRQ 14	Primary Hard disk
IRQ 15	Secondary Hard Disk
NMI	Parity Check Error

**APPENDIX C****PC/104 MODULE INSTALLATION**

There are two steps to install the PC/104 module on EM-586EBX Embedded Single Board Computer.

**Step 1.** Plug the Dual Side Male Connector into the PC/104 female connector.

**Step 2.** Plug the PC/104 module's female connector into the EM-586EBX male connector.



***GLOSSARY***

**8-Bit Bus** – Data is transmitted to expansion slots and other components on the bus only along 8 parallel data line.

**10Base-T** – It is a 10Mbps IEEE 802.3/Ethernet standard that uses unshielded twisted pair cable specification. 10Base-T supports network configuration using the CSMA/CD access method over a twisted pair transmission system up to 100 meters in length without the use of repeater.

**16-Bit Bus or ISA Bus** – Data is transmitted along either 8 or 16 data lines, depending on what kind of adapter card is used in an expansion slot. ISA is the abbreviation of Industry Standard Architecture.

**100Base-TX** – It is a 100Mbps IEEE 802.3/Ethernet standard that uses UTP cable. Also called Fast Ethernet, it uses RJ-45 connectors and EIA/TIA T568B pinning. Maximum cable length from hub to node is 100 meters without a repeater.

**Adapter** – It is also called an expansion board, expansion card, or adapter card. It is a small circuit board that is installed in the expansion slots on the motherboard. You can install a particular adapter that connects a new device such as internal modem, sound card, and scanner.

**AGP (Accelerated Graphic Port)** – is a 32-bit, 66MHz external frequency data bus that transmit a maximum of 528MB/s of data (4 times the speed of PCI transmission); this design improves the speed of large amount in video transaction.

**BIOS (Basic Input /Output System)** – This is a chip on the motherboard that contains the instructions for starting up, or booting, the computer, and more.

**Bus** – Data that travels in a computer along the circuits on the motherboard are called buses. Although three main buses (data bus, address bus, and control bus) manage the computer's operation, often these are collectively called the bus. The bus carries instructions back and forth between the CPU and other devices in the system. ISA, EISA, VL-Bus, PCI and SCSI are examples of PC buses.



**bps** – Bits per second. Also often preceded by K (kilo/thousands), **Kbps** – Kilo bytes per second, and M (mega/million), **Mbps** – Mega bytes per second.

**Bus Mastering** – A method of transferring data through a bus in which the device takes over the bus and directly controls the transfer of data to the computer's memory. Bus mastering is a method of Direct Memory Access (**DMA**) transfer.

**Cache** – Cache RAM is an extra holding area for program instructions that need to be frequently used by the CPU or swapped in and out of RAM. Your CPU can usually access those instructions from the cache more quickly than it could from a hard disk or even RAM, so a cache helps the system work more efficiently. Most systems sold today offer either 256K or 512K cache.

**CPU (Central Processing Unit)** – executes all commands and controls the flow of data, providing the “ brain ” that enables the PC to calculate and perform the operations like sorting information more quickly than a human could. The CPU makes perhaps the greatest contribution to a PC's speed and power. Note: Any additional information is subject to change without prior revision from the supplier.

**Table 1 -- CPU Speeds**

Processor type	Speed ratings (MHz)
486DX2	66, 80
486DX4	75, 100, 120
Pentium	90, 100, 120, 133, 166, 200
Pentium MMX	166, 200, 233
Pentium Pro	166, 180, 200
Pentium II	233, 266, 300, 333

**EIDE (Enhanced IDE)** – It is a hard drive controller that enables your system to be able to handle fast hard disk drives at a speed of 10Mbps.

**EISA or MCA Bus** – Data is transmitted along 32 data lines to adapter cards designed specifically to work with the 32-bit buses. MCA expansion slots cannot accept 8-bit or 16-bit adapter cards. EISA stands for Extended Industry Standard Architecture, while MCA stands for MicroChannel Architecture. MCA is architecture used in IBM Microcomputer.

**Expansion slots** – Expansion slots are plug-in connectors that allow you to insert additional circuit boards that attach to the rest of the PC through special circuitry called the **bus**. By inserting the right circuit board -- usually called an **adapter** or an **expansion card** – you can increase the resolution and the number of colors used by the display, or you can transform your PC into a machine for recording and playing music.

**Fast SCSI** – The common nomenclature associated with SCSI-2, the second generation of SCSI offering mandatory parity checking improvements over SCSI-1.

**IDE (Integrated Drive Electronics)** – It was developed from ST-506 type hard drive interface, utilizes BIOS INT 13h hard drive secondary software and supports two hard drives (Master and Slave). Do not need extra software to drive since it is directly initiated in the BIOS. Data transfer rate is 4.1 Mbps. Take note that this interface cannot support other drive like CD-ROM drive.

**IEEE (Institute of Electrical and Electronic Engineers)** – It is an international professional society that issues its own standard, and is a member of ANSI and ISO. Popular known standards is:

- IEEE802.3** – is a physical layer standard for 10Base-T, 100Base-T, Ethernet, and StarLAN.
- IEEE802.5** – is a physical layer standard for Token Ring.
- IEEE802.11** – is a physical layer standard for Wireless LAN/WAN compatibility.
- IEEE802.12** – is a physical layer standard for 100VG AnyLAN.

**LAN (Local Area Network)** – A data communications network spanning a limited area. It provides communications between three or more computers and peripherals, in most cases using a high-speed media as it' s backbone.

**Keyboard** – This is a component that comes in direct contact for you with your PC. The mechanism of keyboard converts a key cap's movement into a signal sent to the computer. The most common key mechanism are “ **capacitate** ” and “ **hard contact** ”. Capacitate keyboard has a spring that causes the plastic and the metal plunger to move nearer to two pads that have large plates (plated in tin, nickel, and copper). These pads are connected to the keyboard's printed circuit board. Hard contact keyboard causes the key cap to collapse a foam rubber dome that presses against a sheet of plastic on the bottom of which is metallic area connected to the rest of the keyboard's circuit board.

**LDCM (LANDesk Client Manager)** – With the help of LDCM, PCs that are either stand-alone or on a network can not escape the control of a system administrator. Alerts will be sent to the user if an abnormal condition is encountered in a PC. It allows the administrator to give each PC a thorough check-up. Additionally, this feature is available to multiple OS' s on the market today. LDCM Key Features include the following : ■ Health Monitoring , ■ Real-Time Alerting , ■ Remote Accessibility , ■ Extensive Instrumentation. This is a product from Intel.

**Mouse** – The keyboard is a barrier to learn how to use a computer. Xerox Corporation first developed the concept of a pointing device, something a computer user could move with his or her hand, causing a corresponding move on screen. Because of its size and tail like cable, the device was named for the mouse. Apple Computer made the mouse a standard feature of its Macintosh computers, and with the popularity of Windows, a mouse is becoming standard equipment on all PCs, as well. The “ **Trackball** ” have survived more awkward methods of navigating with the keyboard. “ **Digitizing tablets** ” are popular with architects and engineers who must translate precise movements of a pen into lines on the screen. “ Touch screens “ , on which you press either your finger and a special light pen to control the software, are too tiring to use for any length of time.

**MMX** – CPU' s with MMX technology are optimized to run multimedia application, and therefore, offer faster multimedia playback than standard CPUs. However, when manufacturers introduce any new hardware technology, the software makers need to catch up. At this time of compilation, most applications can' t yet take advantage of MMX capabilities.

**Parallel port** – Parallel ports (labeled **LPT1**, **LPT2**, and so on) are usually for plugging in printers. It is also often called a **Centronics port** – has been almost synonymous with **printer port**. Although a serial port can also be used to send data from a PC to some models of printers, the parallel port is faster. A serial port sends data one bit at a time over a single one-way wire; a parallel port can send several bits of data across eight parallel wires simultaneously. Take note that a serial connection sends a single bit, a parallel port send an entire byte. A parallel connection has one drawback.

**PCI Bus (Peripheral Component Interconnect)** – It is a connection slot in a motherboard that supports 32-bit bus transfer rates. The now standard PCI Local Bus carries data along at least 32 lines, that is, at least 32 bits at a time. Local bus computer designs add special buses so the CPU can communicate directly with key components like the monitor, resulting in much better performance. You should look for PCI local bus capabilities in any system you buy, especially PCI local bus video (which helps the monitor display more quickly).

**POST (Power-On Self-Test)** – is the first thing your PC does when you turn it on, and it's your first warning of trouble with any of the components. When the POST detects an error from the display, memory, keyboard, or other basic components, it produces an error warning in the form of a message on your display and—in case your display is part of the problem—in the form of a series of beeps.

**RAM (Random Access Memory)** – consists of a bank of chips that act as “ working memory ”, holding program instructions and data only while your computer's on. Unless the instructions and data are saved to a disk, RAM forgets them when you turn your computer off. RAM is measured in megabytes (M). Most computers today come with 32M of RAM, though some sell with only 16M installed. There are a few different flavors and speeds of RAM, as well. One of the most prominent today is Extended Data Output ( EDO ) RAM, but an even faster type of RAM that has just hit the market is called **SyncDRAM**.

**Serial port** – Serial ports are also sometimes called **COM** (short for COMmunications) ports, and are labeled **COM1**, **COM2**, and so on. It is simple in concept: one line to send data, another line to receive data, and a few other lines to regulate how data is sent over the other two lines – from commonplace modems and printers to plotters and burglar alarms. The most common use for serial port is with a **mouse** or **modem**. The reason for this is that a serial port is not a very efficient way to transfer data, so little data

that speed is not crucial, and perfect for modems because. With current technology, phone lines cannot transport more than one signal at a time anyway. The **serial port** is often referred to as an **RS-232 port**.

**SCSI (Small Computer System Interface)** – An intelligent bus for transmitting data commands between a variety of devices. There are many implementations of SCSI, including Fast SCSI, Wide SCSI, Fast Wide SCSI, Fast-20, and Fast-40.

**SCSI-2** – The second generation of SCSI; includes many improvements to SCSI-1, including Fast SCSI, Wide SCSI, and mandatory parity checking.

**SCSI-3** – The third generation of SCSI; introduces Fast-20 and Fast-40 as improvements to the parallel bus. The standard also includes a number of specifications for high-speed serial bus architectures such as SSA, Fiber Channel, and IEEE 1394. Also known as Ultra SCSI.

**Ultra SCSI** – Also known as SCSI-3, is a third generation SCSI standard that introduced parallel bus speed improvements (FAST-20 and FAST-40), and the miniaturized 68-pin micro connector.

**USB (Universal Serial Bus)** – USB consolidates serial, parallel, keyboard, mouse, and game ports into one asynchronous and isochronous communications port with bandwidth for data transfer speeds up to 12 Mbps without termination. By daisy-chaining USB hubs, up to 127 I/O devices can be connected to one USB port on the PC. USB is completely plug-and-play meaning peripherals can be correctly detected and configured automatically as soon as they are connected.

**UTP (Unshielded Twisted Pair)** – Twisted pair cable with neither individual nor overall shielding. **Twisted Pair** are two wires twisted together to reduce susceptibility to RF crosswalk.

**VGA (Video Graphics Array)** – A video adapter that supports 640x480 pixels color resolution. The Windows OS provides medium text & graphics standard.

**VL-Bus** – It is also known as Local Bus; this is an I/O interface that is directly connected and depended of the system CPU. The VL-Bus is an abbreviation of VESA Local Bus.

# Terms and Conditions

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Date:1997.10.20

## Warranty Policy

1. All products are warranted against defects in materials and workmanship on a period of two years from the date of purchase by the customer.
2. The buyer will bear the return freight charges for goods that are returned for repair within the warranty period whereas manufacturer will bear the other way after repair.
3. The buyer will pay for repair (for the replaced materials plus service time) and transportation charges (both ways) for items after the expiration of the warranty period.
4. If the RMA Service Request Form does not meet the stated requirement as listed on "RMA Service " , RMA goods will be returned at the customer expense.
5. The following conditions are excluded from this warranty :
  - A. Improper or inadequate maintenance by the customer.
  - B. Unauthorized modification or misuse.
  - C. Operation outside of the environmental specifications for the product.

## RMA Service

### 1. **Request a RMA# :**

Complete and fax to Supplier the "RMA Request Form" to obtain a RMA number.

### 2. **Shipping:**

- A. The customer is requested to fill up the problem code as listed . If none of the code is selected, please write the symptom description on the remark.
- B. Ship the defective units with freight prepaid.
- C. Mark the RMA # clearly on the box.
- D. Shipping damage as a result of inadequate packing is the customer's responsibility.
- E. Use the original packing materials whenever possible .

### 3. **All RMA# are valid for 30 days only:**

When RMA goods are received after valid RMA# period , the goods will be rejected.

