

All jumpers

Snobol SNB-M022 Jumper Tables

Jumper	Default	Function	Pins	Options
GJ1	Open	Unknown KBC Config	2	
GJ2	2-3	Unknown KBC Config	3	
GJ3	2-3	Unknown Rom strap (74F74)	3	
GJ4	2-3	Unknown Rom strap (74F74)	3	
JP1	2-3	CMOS Ext/Int Bat	3	
JP2	Closed	Int Bat Disconnect	2	Open = Int Bat Disconnect
JP3	2-3	Unknown 493F Config	3	
JP4	1-2	Unknown Pull up/down strap	3	2-3 Unknown Pulldown
JP5	1-2	TA0	3	Intel (1-2), Cyrix (2-3), AMD (1-2)
JP6	1-2	TA1	3	Intel (1-2), Cyrix (1-2), AMD (2-3)
JP7	1-2,3-4	Unknown	4	
JP8	2-3	Unknown	3	
JP9	Open	Unknown	2	
JP10	Open	Unknown	2	
JP11	Closed	Unknown	2	
JP12	Closed	Unknown	2	
JP13	1-2	VLB Delay	3	1-2 Default, 2-3 Delay VLB 1-T for 50Mhz
JP14	1-2	VLB Delay	3	1-2 Default, 2-3 Delay VLB 1-T for 50Mhz
JP15	Open	Cache strap	3	
JP16	2-3	Cache strap	3	
JP17	Open	Cache strap	2	
JP18	Closed	Cache strap	2	
JP19	Open	Cache strap	2	
JP20	1-2	Cache strap	3	
JP21	Closed	FSB Clock	2	
JP22	Closed	FSB Clock	2	
JP23	Closed	FSB Clock	2	
JP24	Open	Unknown	2	
JP25	Open	P24 OD Multi	3	
JP26	Closed	Unknown	2	
JP27	Closed	Unknown	2	

Specific Strappings

CPU Strapping	JP5	JP6
Intel	1-2	1-2
Cyrix	1-2	2-3
AMD	2-3	1-2

Cache Strapping	JP18	JP17	JP19	JP20	JP16	JP15	TAG	RANGE
128K	OFF	OFF	OFF	2-3	1-2	Open	8KB	32MB
256K	ON	OFF	OFF	1-2	2-3	Open	32KB	64MB
512K	ON	ON	OFF	2-3	2-3	1-2	32KB	64MB
1M	ON	ON	ON	1-2	12-3	2-3	128KB	64MB

FSB Clock Strapping	JP23	JP22	JP21
20Mhz	Open	Open	Open
25Mhz	Closed	Open	Open
33Mhz	Closed	Closed	Closed
40Mhz	Closed	Closed	Open
50Mhz	Open	Open	Closed

P24 OD Multi Strapping	JP25
2x	2-3
2.5x	1-2
3x	Open