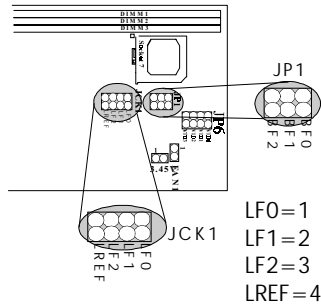


M5VMA

CPU Clock Selection (JCK1)



JCK1(4)

	CLK
ON	AS AGP
OFF	AS CPU

(a) CPU Bus Clock and Multiplier Selection

- * JCK1 (1) OFF & JCK1 (2) ON & JCK1 (3) ON :Bus Clock = 66MHz
- * JCK1 (1) OFF & JCK1 (2) OFF & JCK1 (3) ON:BusClock=75MHz
- * JCK1 (1) OFF & JCK1(2) ON & JCK1(3) OFF :Bus Clock = 83MHz
- * JCK1 (1) ON & JCK1 (2) OFF & JCK1(3)OFF : Bus Clock = 95MHz
- * JCK1 (1) OFF & JCK1 (2) OFF & JCK1(3)OFF :Bus Clock = 100MHz
- * JP1 (BF0) OFF & JP1 (BF1) OFF & JP1 (BF2) OFF :Multiplier = 1.5
- * JP1 (BF0) ON & JP1 (BF1) OFF & JP1 (BF2) OFF : Multiplier = 2
- * JP1 (BF0) ON & JP1 (BF1) ON & JP1 (BF2) OFF : Multiplier = 2.5
- * JP1 (BF0) OFF & JP1 (BF1) ON & JP1 (BF2) OFF : Multiplier = 3
- * JP1 (BF0) OFF & JP1 (BF1) OFF & JP1 (BF2) OFF : Multiplier = 3.5
- * JP1 (BF0) ON & JP 1 (BF1) OFF & JP1 (BF2) ON : Multiplier = 4
- * JP1 (BF0) ON & JP1 (BF1) ON & JP1 (BF2) ON : Multiplier = 4.5
- * JP1 (BF0) OFF & JP1 (BF1) ON & JP1 (BF2) ON : Multiplier = 5

(b) INTEL CPU

CPU Speed	Bus Clock & Multiplier	JCK1 (1)	JCK1 (2)	JCK1 (3)	JP1 (BF0)	JP1 (BF1)	JP1 (BF2)
133MH	66MHz x 2	OFF	ON	ON	ON	OFF	OFF
166MH	66MHz x 2.5	OFF	ON	ON	ON	ON	OFF
200MH	66MHz x 3	OFF	ON	ON	OFF	ON	OFF
233MH	66MHz x 3.5	OFF	ON	ON	OFF	OFF	OFF

(c) AMD-K5™ CPU

CPU Speed	JCK1(1)	JCK1(2)	JCK1(3)	JP1(BF0)	JP1(BF1)	JP1(BF2)
PR-133	OFF	ON	ON	ON	OFF	OFF
PR-166	OFF	ON	ON	ON	ON	OFF
PR-200	OFF	ON	ON	OFF	ON	OFF

(d) AMD-K6™ CPU

CPU Speed	Bus Clock & Multiplier	JCK1 (1)	JCK1 (2)	JCK1 (3)	JP1 (BF0)	JP1 (BF1)	JP1 (BF2)
166MHz	66MHz x 2.5	OFF	ON	ON	ON	ON	OFF
200MHz	66MHz x 3	OFF	ON	ON	OFF	ON	OFF
233MHz	66MHz x 3.5	OFF	ON	ON	OFF	OFF	OFF
266MHz	66MHz x 4	OFF	ON	ON	ON	OFF	ON
300MHz	66MHz x 4.5	OFF	ON	ON	ON	ON	ON

(e) AMD-K6™ -2 CPU

CPU Speed	Bus Clock & Multiplier	JCK1 (1)	JCK1 (2)	JCK1 (3)	JP1 (BF0)	JP1 (BF1)	JP1 (BF2)
250MHz	100 x 2.5	OFF	OFF	OFF	ON	ON	OFF
300MHz	100 x 3	OFF	OFF	OFF	OFF	ON	OFF
333MHz	66 x 5	OFF	ON	ON	OFF	ON	ON
333MHz	95 x 3.5	ON	OFF	OFF	OFF	OFF	OFF
350MHz	100 x 3.5	OFF	OFF	OFF	OFF	OFF	OFF

(f) Cyrix 6x86™ / 6x86L™ CPU

CPU Speed	Bus Clock & Multiplier	JCK1 (1)	JCK1 (2)	JCK1 (3)	JP1 (BF0)	JP1 (BF1)	JP1 (BF2)
PR-166+ 133MHz	66MHz x 2	OFF	ON	ON	ON	OFF	OFF
PR-200+ 150MHz	75MHz x 2	OFF	OFF	ON	ON	OFF	OFF

(g) Cyrix 6x86MII™ CPU

CPU Speed	Bus Clock & Multiplier	JCK1 (1)	JCK1 (2)	JCK1 (3)	JP1 (BF0)	JP1 (BF1)	JP1 (BF2)
PR-166+ 133MHz	66MHz x 2	OFF	ON	ON	ON	OFF	OFF
PR-200+ 150MHz	75MHz x 2	OFF	OFF	ON	ON	OFF	OFF
PR-200+ 166MHz	66MHz x 2.5	OFF	ON	ON	ON	ON	OFF
PR-233+ 166MHz	83MHz x 2	OFF	ON	OFF	ON	OFF	OFF
PR-233+ 188MHz	75MHz x 2.5	OFF	OFF	ON	ON	ON	OFF
PR-233+ 200MHz	66MHz x 3	OFF	ON	ON	OFF	ON	OFF
PR-266+ 208MHz	83MHz x 2.5	OFF	ON	OFF	ON	ON	OFF
PR-300+ 225MHz	75MHz x 3	OFF	OFF	ON	OFF	ON	OFF
PR-300+ 233MHz	66MHz x 3.5	OFF	ON	ON	OFF	OFF	OFF
PR-333+ 249MHz	83MHz x 3	OFF	ON	OFF	OFF	ON	OFF
PR-333+ 263MHz	75MHz x 3.5	OFF	OFF	ON	OFF	OFF	OFF

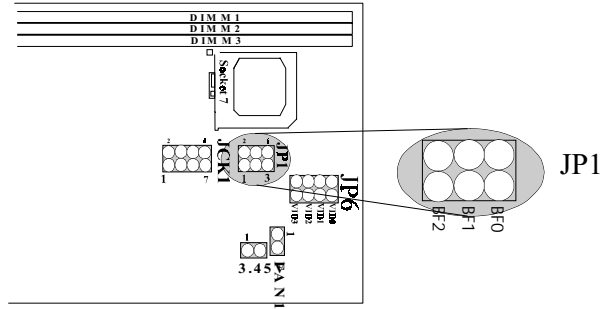
(h) idt-Win™ Chip CPU

CPU Speed	Bus Clock & Multiplier	JCK1 (1)	JCK1 (2)	JCK1 (3)	JP1 (BF0)	JP1 (BF1)	JP1 (BF2)
200MHz	66MHz x 3	OFF	ON	ON	OFF	ON	OFF

(i) idt-Win™ Chip CPU WINCHIP2

CPU Speed	Bus Clock & Multiplier	JCK1 (1)	JCK1 (2)	JCK1 (3)	JP1 (BF0)	JP1 (BF1)	JP1 (BF2)
200MHz	66MHz x 3	OFF	ON	ON	OFF	ON	OFF

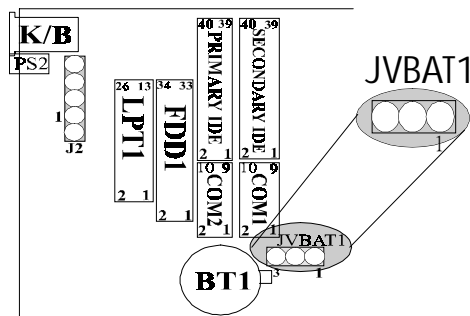
CPU RATIO Selection (JP1)



JP1\VOL1	1.5	2	2.5	3	3.5	4	4.5	5
BF0	OFF	ON	ON	OFF	OFF	ON	ON	OFF
BF1	OFF	OFF	ON	ON	OFF	OFF	ON	ON
BF2	OFF	OFF	OFF	OFF	OFF	ON	ON	ON

Jumper Settings

A jumper is two or more pins which may be covered by a plastic jumper cap, allowing you to select different system options.



CMOS Function Selection (JVBT1)

JVBT1	Assignment
<p>1-2 Closed</p>	Normal Operation
<p>2-3 Closed</p>	Clear CMOS Data
<p>Open</p>	Onboard Battery Disabled

Note : Please follow the procedure as below to clear BIOS Password if your password is lost or forgotten.

