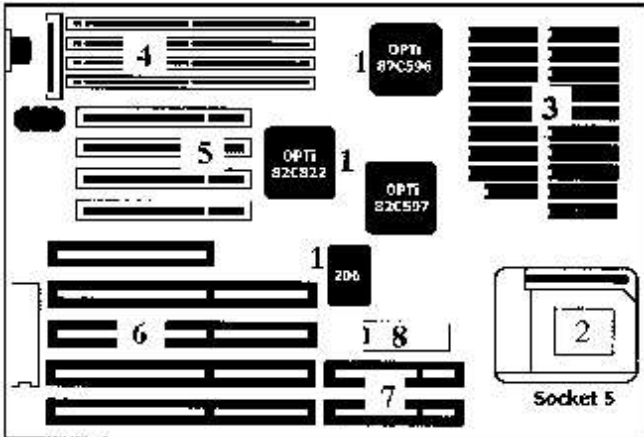


↑ Mainboard HOT-543

Unterkapitel: [Spezifikation](#) | [Konfiguration](#) |



Explanation of the components

1: chipsat, 2: processor socket, 3: secondary cache, 4: memory socket, 5: PCI slots, 6: ISA slots, 7: VESA (VL) slots, 8: system bios (Flash EPROM)




↑ HOT-543 configuration tables

Subchapter: [CPU table](#) | [system clock](#) | [clock multiplier](#) | [Cache memory configuration](#) | [AT bus clock setting](#) | [PCI clock source](#) | [Vesa Local Bus Configuration](#) |

↑ CPU table

Processor settings			
processor	CPU Class	System rate	Multiplier
Intel Pentium P54C	133 MHz	66 MHz	2 x
Intel Pentium P54C	120 MHz	60 MHz	2 x
Intel Pentium P54C	100 MHz	66 MHz	1,5 x
Intel Pentium P54C	100 MHz	50 MHz	2 x
Intel Pentium P54C	90 MHz	60 MHz	1,5 x
Intel Pentium P54C	75 MHz	50 MHz	1,5 x

↑ System rate

system clock frequency	Jumper JP13
50 MHz	
60 MHz	
66 MHz	



View: CPU socket below these jumpers

↑ Rate Multiplier

Rate Multiplier	Jumper JP2
1,5 x	
2 x	

↑ Cache memory configuration

cache memory capacity	JP4	JP5	JP6	JP7	JP8	JP9
64 kB						
128 kB						
256 kB						
512 kB						

View: CPU socket below these jumpers

cache memory capacity	Data SRAM bank 0 U21 to U28	Data SRAM bank 1 U34 to U41	Tag SDRAM U16, U20	Dirty Bit U17 (Optional)
64 kB	8k x 8	file	8k x 8 on U20	16k x 1
128 kB	8k x 8	8k x 8	8k x 8 on U20	16k x 1
256 kB	32k x 8	file	8k x 8 on U20	16k x 1
512 kB	32k x 8	32k x 8	8k x 8 on U20, U16	16k x 1

↑ AT bus clock setting

AT-Bus Clock	JP19 JP18	System speed = 60 MHz PCI speed = 30 MHz	System speed = 66 MHz PCI speed = 33 MHz
PCI Pace / 5		6.0 MHz	6.6 MHz
PCI Pace / 4		7.5 MHz	8.3 MHz
PCI Pace / 3		10.0 MHz	11.0 MHz
PCI Pace / 2		15.0 MHz	16.5 MHz





↑ Source of the PCI clock

This determines whether the PCI clock (LCLK) is generated by a quartz oscillator fitted in the U77 socket, or whether it is derived internally from the system clock.

PCI Clock (LCLK)	JP27/28	U77	JP16
PCI Pace / 5		Quartz is equipped	
PCI Pace / 4		Quartz is not loaded	

↑ Vesa Local Bus Konfiguration

Recommended: JP24 and JP25 closed

VL bus configuration	JP24	JP25
PCI clock >=33MHz	x	
PCI speed <33 MHz	x	
High Speed Write 0 queues		x
High Speed Write 1 queues		x

