

Motherboard: FIC 486SC-P Rev D3
Description: Partial Jumper Settings

Note:

This document is intended to capture the jumper settings for a specific configuration of this board. This is a result of trial and error and other information I was able to pull together from pictures of this board available online as I have not been able to locate the manual or other references on jumper settings for this board. The below description worked for me. Your mileage may vary.

Configuration

CPU: Intel 486 DX 33 Mhz
Bios Post String: 20-0100-001121-00101111-042591-SYMP-F
Bios Core Version: 042591
Cache Chips: W24257AK-15 (256k bits or 32kbx8) x8 chips total
Tag Chip: AE88128AK-15 (128k bits or 16kbx8)



Jumper Settings:

JP3: 2-3

JP32: Closed
JP26: Open
JP27: Open
JP28: Closed

JP12: Closed
JP13: 1-2
JP14: Closed

JP33: 2-3

JP22: 1-2
JP23: 1-2
JP24: 1-2
JP25: 1-2

JP34: 1-2

JP38: Open
JP41: Open

JP35: 2-3
JP36: 2-3
JP37: 2-3
JP39: 2-3
JP40: 2-3
JP42: 2-3

Note:

The above configuration results in a total cache of 128kb (not 256kb). All eight cache chips (plus tag chip) have to be present. I am not sure if this board only supports a maximum 128kb of cache or if there are other settings which will enable the whole 256kb. After much experimentation I was not able to get the full amount recognized.