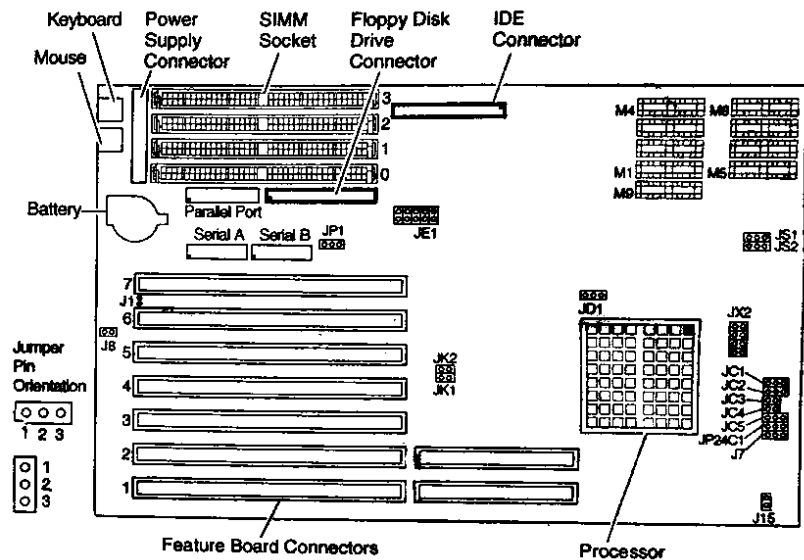


Six-Slot ELI-II System Configuration

Function	Jumper:Pins	Setting
Clear CMOS	J1: Short temporarily to clear CMOS	
CPU with internal Write/Back Cache Other CPUs	J7:2-3 J7:1-2	<input type="checkbox"/>
Video Select: VGA/Mono Video Select: EGA/CGA	J8:Off J8:On	<input type="checkbox"/>
On board Buzzer Enabled	J15: On	<input type="checkbox"/>
Select CPU 486SX Select CPU 486DX/DX2/SX2	JC1:2-3 On JC2:2-3 On JC3:Off JC4:Off JC1:1-2 On JC2:1-2 On JC3:On JC4:On	<input type="checkbox"/>
DX4 with internal 2.5X Clock <i>DX-80-120 AND P24</i> DX4 with internal 3X Clock <i>DX-75-100</i>	JP24C1:2-3 JP24C1:Off	<input type="checkbox"/>
Cache Size 0 KB Cache size 128 KB Cache size 256 KB	JS1:Off JS2:Off JS1:1-2 On JS2:1-2 On JS1:2-3 On JS2:2-3 On	<input type="checkbox"/>
Local Bus IDE Select Enabled Local Bus IDE Select Disabled	JDI:1-2 JDI:2-3	<input type="checkbox"/>
CPU External Clock Select 25MHz CPU External Clock Select 33MHz	JK1:On JK2:Off JK1:Off JK2:On	<input type="checkbox"/>
Parallel Port Enhanced Mode Support: ECP DMA1 ECP DMA3 ECP Disabled	JE1:1-2, 3-4, 5-6 JE1:1-2, 7-8, 8-10 JE1: All Off	<input checked="" type="checkbox"/> DEFAULT

Function	Jumper:Pins	Setting
LPT Interrupt Select IRQ5 LPT Interrupt Select IRQ7	JP1:1-2 JP1:2-3	<input type="checkbox"/>
SL Enhanced CPUs P24CT/P24D/P24T	JX2: All Off JC5:1-2 JX2: All On JC5:2-3	<input checked="" type="checkbox"/>



VIA / Unisys 486-ELI6-II

J1 = To clear Cmos short temporarily

J8 = Vid select VGA/Mono = off
Ega/Cga = on

JP1 = LPT IRQ, irq5 = 1-2, irq7 = 2-3

Jf1, Jf2 = SMC type. 651 = 2 - 3
type 665 = 1 - 2

Je1 = Para port ECP dma select
Dma 1 = 1-2, 3-4, 5-6
Dma 3 = 1-2, 7-8, 9-10
Disabled = All off

Jd1 = Local bus IDE enable select
Enable = 1-2, disable = 2-3

Jk1, Jk2 = CPU clock rate
25mhz : Jk1 = on, Jk2 = off
33mhz : Jk1 = off, Jk2 = on

CPU type select:

486SX type:

Jc1=2-3, Jc2=2-3, Jc3=off, Jc4=off

486DX/DX2/SX2 type:

Jc1=1-2, Jc2=1-2, Jc3=on, Jc4=on

SL enhanced CPU:

Jx2=all off, Jc5=1-2

P24ct/P24d/P24t type CPU:

Jx2=all on, Jc5=2-3

DX2 @ 2X clock: Jp24c = 1-2

DX4 @ 2.5X clock: Jp24c = 2-3

DX4 @ 3X clock: Jp24c = Off

Jc6, Jc7 = Intel/AMD : all on

J7 = CPU L1 cache type:

WB = 2-3, all others = 1-2

CPU voltage: 5v = s1,s2,s3,s4 = off

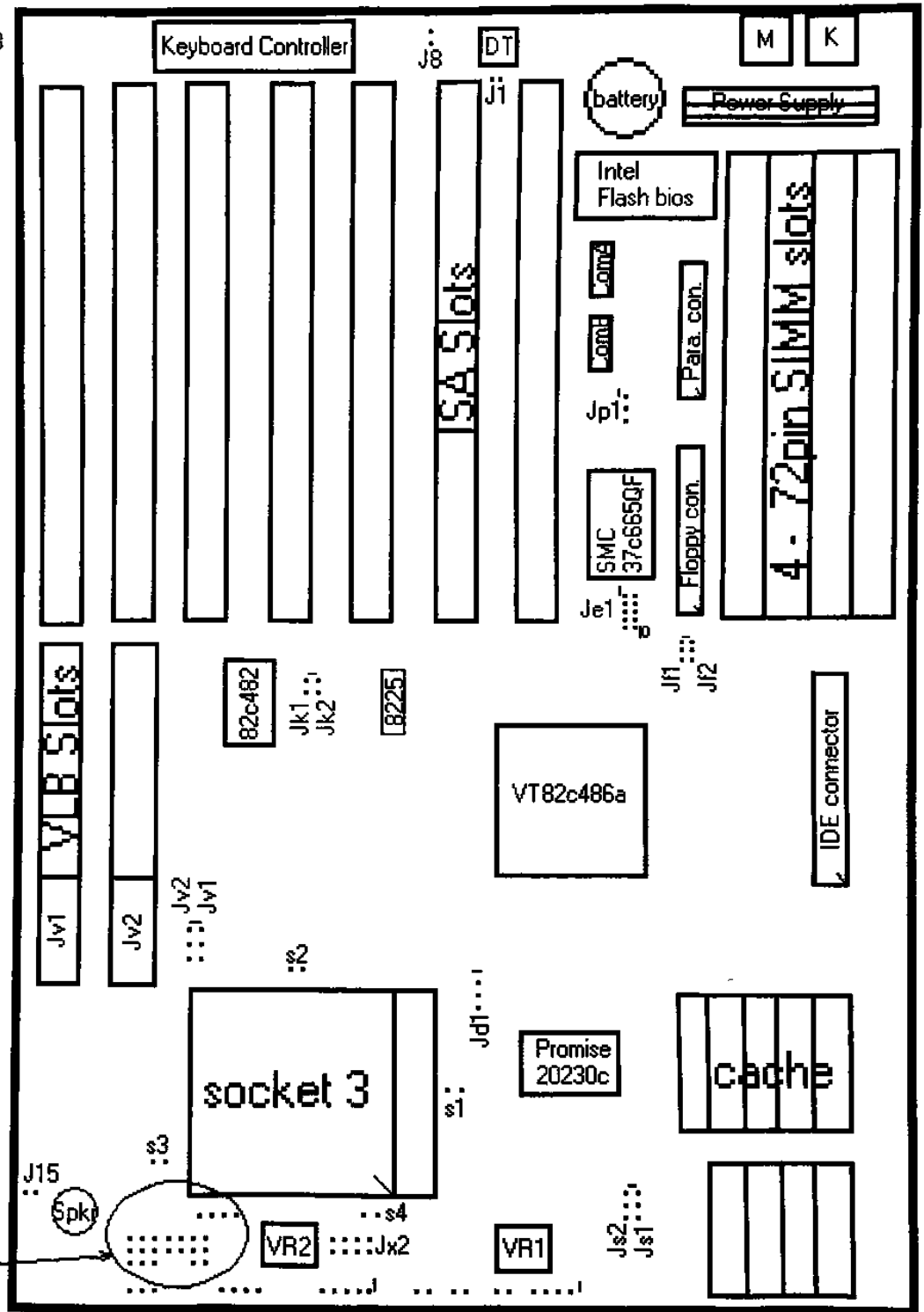
3.45v = s1,s2,s3,s4 = on

Jv1 = Vlb wait state: 1-2 = 1ws, 2-3 = 0

Jv2 = Vlb clk state:

>33mhz = 1-2, 33mhz or less = 2-3

default : Jv1=1-2, Jv2=2-3



J15 = Onboard speaker : enable = on

Js1, Js2 = Cache size select:
0kb : all off
128kb : Js1 = 1-2, Js2 = 1-2
256kb : Js1 = 2-3, Js2 = 2-3

