# **BX20** brief installation guide.

Click Banner above for Cornish Net Home page, then on the overclocking button for more info on the BX 20 and other hardware topics.

# 1. Features

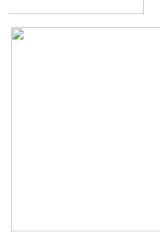
Employs switching type regulator to stabilize CPU operation

- Supports 66, 75\*,83\*, and 100 MHz CPU external clock speeds
- Supports Pentium<sup>®</sup> II 350~450MHz processor cartridge (Based on 100 MHz) and Pentium<sup>®</sup> II 233~333MHz processor cartridge (Based on 66 MHz)
- Supports Pentium<sup>®</sup> III 450~500MHz processor cartridge (Based on 100 MHz)
- Intel 440BX chipset
- AGP V1.0 compliant
- Supports up to 512MB MAX.(8, 16, 32, 64, 128M SDRAM )
- Supports Ultra DMA/33 IDE protocol
- Supports Advanced Configuration and Power Interface (ACPI) specification

Note: \* 75MHz and 83MHz bus speeds supported but not guaranteed due to the PCI and chipset spees.

# 2. Block Diagram

3. Connector Define & Jumper Setting



# PN1

POWER LED : Power LED header

KeyLock : Keylock header

HDD.LED : HDD LED header

POW ON. : Power On/Off header

SMI : Hardware suspend header

PN2

RESET : Hardware Reset header

SPEAKER : Speaker header

# SP-LED : Suspend LED header

#### ■ I/O port connectors

IDE1 : IDE channel 1

IDE2 : IDE channel 2

FDC : Floppy Disk connector

LPT : Parallel port connector

COM1 : Serial port COM1 connector

COM2 : Serial port COM2 connector

USB : Universal Serial Bus connector

KB : Keyboard connector

IR1 : Infrared remote connectors

Mouse : PS/2 mouse connectors

FAN1 : CPU fan power connectors

WOL1: Wake On LAN

#### BIOS Setting

CCOMS1 1-2 close Operation CMOS data

2-3 close Clear CMOS data

Note: 1. When you install RAM module, we strongly suggest that you install it in the order of DIMM1~DIMM4. If you installed RAM module disregarded the order, you may encounter some problems. One of the problems is that you can't boot up the system. Another possible problem is that the BIOS can't detect the memory you installed.

2. According this motherboard layout design, DIMM3 and DIMM4 share two banks. Therefore, please don't install RAM modules that will occupy two banks into both DIMM3 and DIMM4 at the same time. There are two main types of RAM modules on the market now, one is single sided and the other is double sided. The single sided RAM module doesn't mean it only occupies one bank, and double sided RAM module also doesn't mean it will occupy two banks. When you want to install RAM modules into both DIMM3 and DIMM4 at the same time, please make sure how many banks of RAM modules will occupy. For this issue, you can refer to the specifications of your RAM module from the manufacturer.

# 4. CPU Settings

<u>SPEED</u>	<b>DS1</b>	<b>DS2</b>	<u>DS3</u>	DS4	DS5	<b>DS6</b>	<b>DS7</b>	<b>DS8</b>
333MHz @ 66MHz	ON	ON	OFF	OFF	OFF	OFF	ON	OFF
366MHz @ 66MHz	ON	OFF	OFF	OFF	OFF	OFF	ON	OFF
400MHz @ 66MHz	OFF	ON	ON	ON	OFF	OFF	ON	OFF
433MHz @ 66MHz	OFF	OFF	ON	ON	OFF	OFF	ON	OFF
466MHz @ 66MHz	OFF	ON	OFF	ON	OFF	OFF	ON	OFF
500MHz @ 66MHz	OFF	OFF	OFF	ON	OFF	OFF	ON	OFF
533MHz @ 66MHz	OFF	ON	ON	OFF	OFF	OFF	ON	OFF
350MHz @100MHz	ON	OFF	OFF	ON	OFF	OFF	OFF	OFF
400MHz @100MHz	ON	ON	ON	OFF	OFF	OFF	OFF	OFF
450MHz @100MHz	ON	OFF	ON	OFF	OFF	OFF	OFF	OFF
500MHz @100MHz	ON	ON	OFF	OFF	OFF	OFF	OFF	OFF
550MHz @100MHz	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF
600MHz @100MHz	OFF	ON	ON	ON	OFF	OFF	OFF	OFF
650MHz @100MHz	OFF	OFF	ON	ON	OFF	OFF	OFF	OFF
700MHz @100MHz	OFF	ON	OFF	ON	OFF	OFF	OFF	OFF
750MHz @100MHz	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF
800MHz @100MHz	OFF	ON	ON	OFF	OFF	OFF	OFF	OFF

### BUS SPEED:

BUS SPEED	<u>DS5</u>	<u>DS6</u>	<u>DS7</u>	<u>DS8</u>
50MHz	ON	ON	ON	OFF
66MHz	OFF	OFF	ON	OFF
75MHz*	OFF	ON	ON	OFF
83MHz*	ON	OFF	ON	OFF
100MHz	OFF	OFF	OFF	OFF
103MHz*	ON	ON	OFF	OFF
112MHz*	OFF	ON	OFF	OFF

MULTIPLIER:				
<b>MULTIPLIER</b>	<u>DS1</u>	<u>DS2</u>	<u>DS3</u>	<u>DS4</u>
2.0x	ON	ON	ON	ON
2.5x	ON	OFF	ON	ON
3.0x	ON	ON	OFF	ON
3.5x	ON	OFF	OFF	ON
4.0x	ON	ON	ON	OFF
4.5x	ON	OFF	ON	OFF
5.0x	ON	ON	OFF	OFF
5.5x	ON	OFF	OFF	OFF
6.0x	OFF	ON	ON	ON
6.5x	OFF	OFF	ON	ON
7.0x	OFF	ON	OFF	ON
7.5x	OFF	OFF	OFF	ON
8.0x	OFF	ON	ON	OFF

# **SITE TRAFFIC** analysis