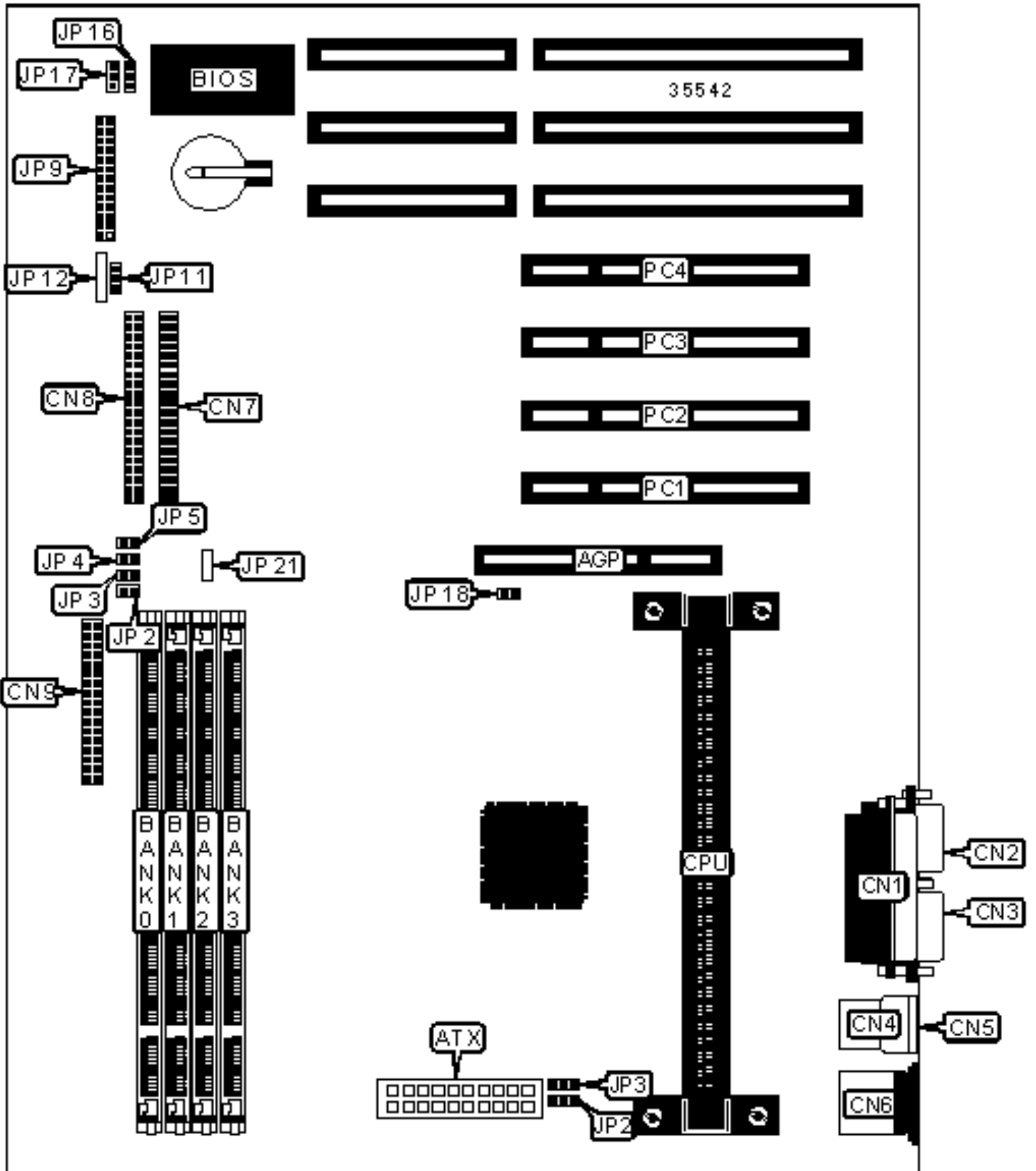


ZIDA TECHNOLOGIES, LTD.

6MLX (VER. 1.10)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	IR connector	J15
ATX power connector	ATX	CPU fan power	JP2
Parallel port	CN1	CPU fan power	JP3
Serial port 1	CN2	Power LED & keylock	JP9/pins 1 –5
Serial port 2	CN3	Green PC connector	JP9/pins 7 & 8
USB connector 1	CN4	Speaker	JP9/pins 9 – 13
USB connector 2	CN5	IDE interface LED	JP9/pins 14 & 15
PS/2 mouse port	CN6	Turbo switch	JP9/pins 17 & 18
IDE interface 1	CN7	Reset switch	JP9/pins 22 & 23
IDE interface 2	CN8	Turbo LED	JP9/pins 25 & 26
Floppy drive interface	CN9	32-bit PCI slots	PC1 – PC4

Note: The location of J15 is unidentified.

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	JP11	Pins 1 & 2 closed
CMOS memory clear	JP11	Pins 2 & 3 closed
» Factory configured - do not alter	JP16	Pins 1 & 2 closed
» Factory configured - do not alter	JP17	Pins 1 & 2 closed
» Factory configured - do not alter	JP18	Pins 1 & 2 closed
» Factory configured - do not alter	JP21	Unidentified

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
------	--------	--------	--------	--------

8MB	(1) 1M x 64	None	None	None
16MB	(1) 2M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
24MB	(1) 2M x 64	(1) 1M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	None
32MB	(1) 4M x 64	None	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
40MB	(1) 4M x 64	(1) 1M x 64	None	None
48MB	(1) 4M x 64	(1) 2M x 64	None	None

DIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2	Bank 3
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None	None	None
64MB	(1) 4M x 64	(1) 4M x 64	None	None
72MB	(1) 8M x 64	(1) 1M x 64	None	None
80MB	(1) 8M x 64	(1) 2M x 64	None	None
96MB	(1) 8M x 64	(1) 4M x 64	None	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
128MB	(1) 16M x 64	None	None	None
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
136MB	(1) 16M x 64	(1) 1M x 64	None	None
144MB	(1) 16M x 64	(1) 2M x 64	None	None

152MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 4M x 64	None	None
176MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 16M x 64	(1) 8M x 64	None	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
224MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 32M x 64	None	None	None
256MB	(1) 16M x 64	(1) 16M x 64	None	None
256MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
280MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
304MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
320MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
352MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
448MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None	None
512MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
640MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	None
1024MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board accepts EDO & SDRAM memory. Maximum SDRAM is 512MB. Maximum EDO is 1GB.

CACHE CONFIGURATION

Note: 512KB cache is located on the Pentium II CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP2	JP3	JP4	JP5
233MHz	66MHz	3.5x	Open	Open	Closed	Closed
266MHz	66MHz	4x	Closed	Closed	Open	Closed
300MHz	66MHz	4.5x	Open	Closed	Open	Closed
333MHz	66MHz	5x	Closed	Open	Open	Closed