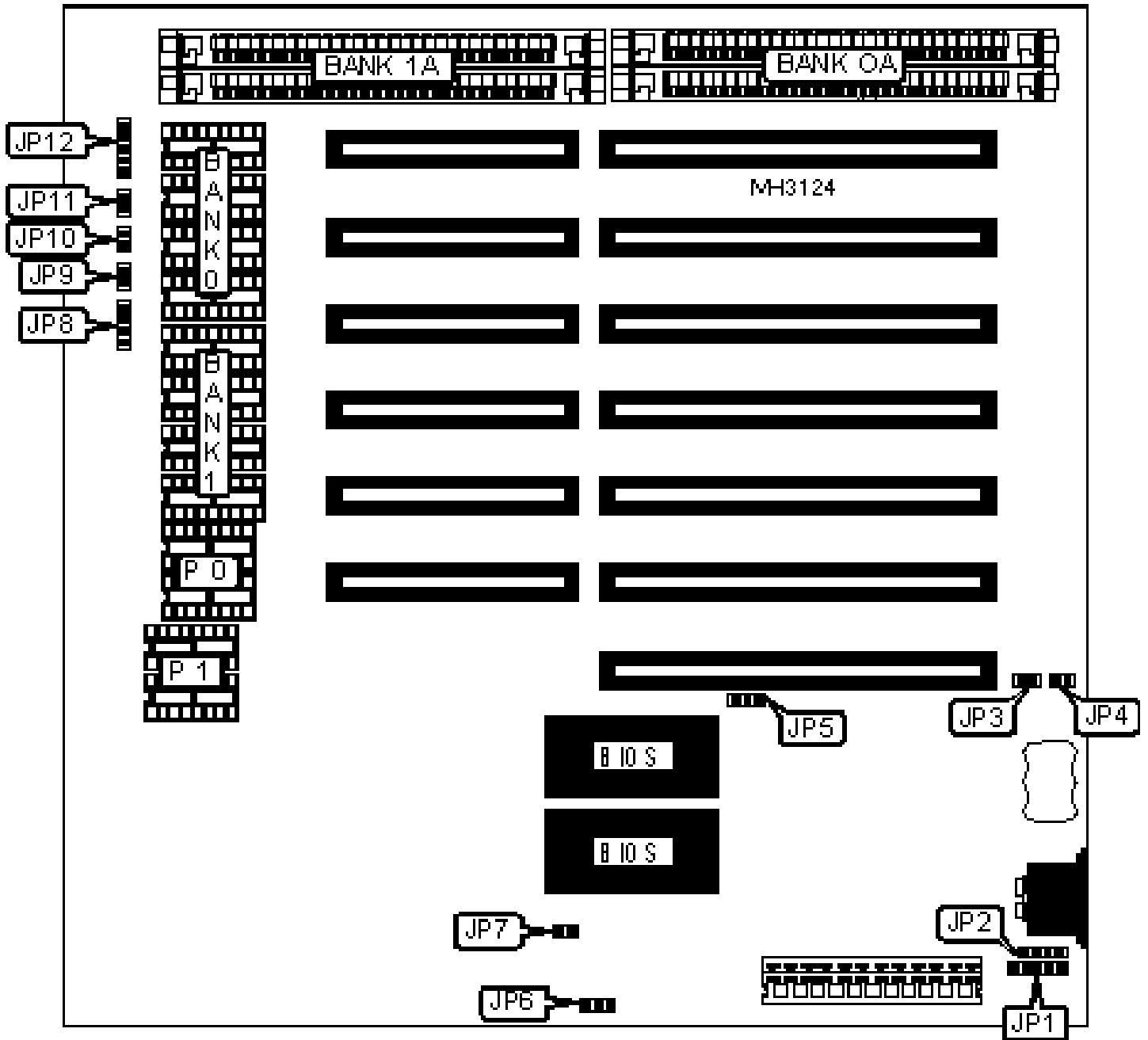


UNIDENTIFIED

HT12HS

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Auxiliary keyboard connector	JP1	Turbo switch	JP10
External battery	JP2	Reset switch	JP11
Speaker	JP8	Power LED & keylock	JP12
Turbo LED	JP9		

USER CONFIGURABLE SETTINGS

Function	Jumper	Position
Monitor type select color	JP3	Closed
Monitor type select monochrome	JP3	Open
CMOS memory normal operation	JP4	Closed
CMOS memory clear	JP4	Open
BIOS type select 27256	JP5	pins 2 & 3 closed
BIOS type select 27128	JP5	pins 1 & 2 closed
NPU speed select 4.77MHz	JP6	pins 1 & 2 closed
NPU speed select 10.6MHz	JP6	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	P 0	Bank 1	P 1	Bank 0A	Bank 1A
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164	NONE	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	(4) 441000	(2) 411000	(4) 441000	(2) 411000	NONE	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9

CPU SPEED CONFIGURATION

System clock	Bus clock	DRAM speed	JP7
12MHz	8MHz asynch	100ns	Open
12MHz	6MHz synch	100ns	Closed
16MHz	8MHz synch	80ns	Closed
20MHz	8MHz asynch	60/70ns	Open
20MHz	10MHz synch	60/70ns	Closed

MISCELLANEOUS TECHNICAL NOTE

Note: The location of pin 1 is unidentified.