U.S. INTEGRATED TECHNOLOGIES, INC.

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Configuration
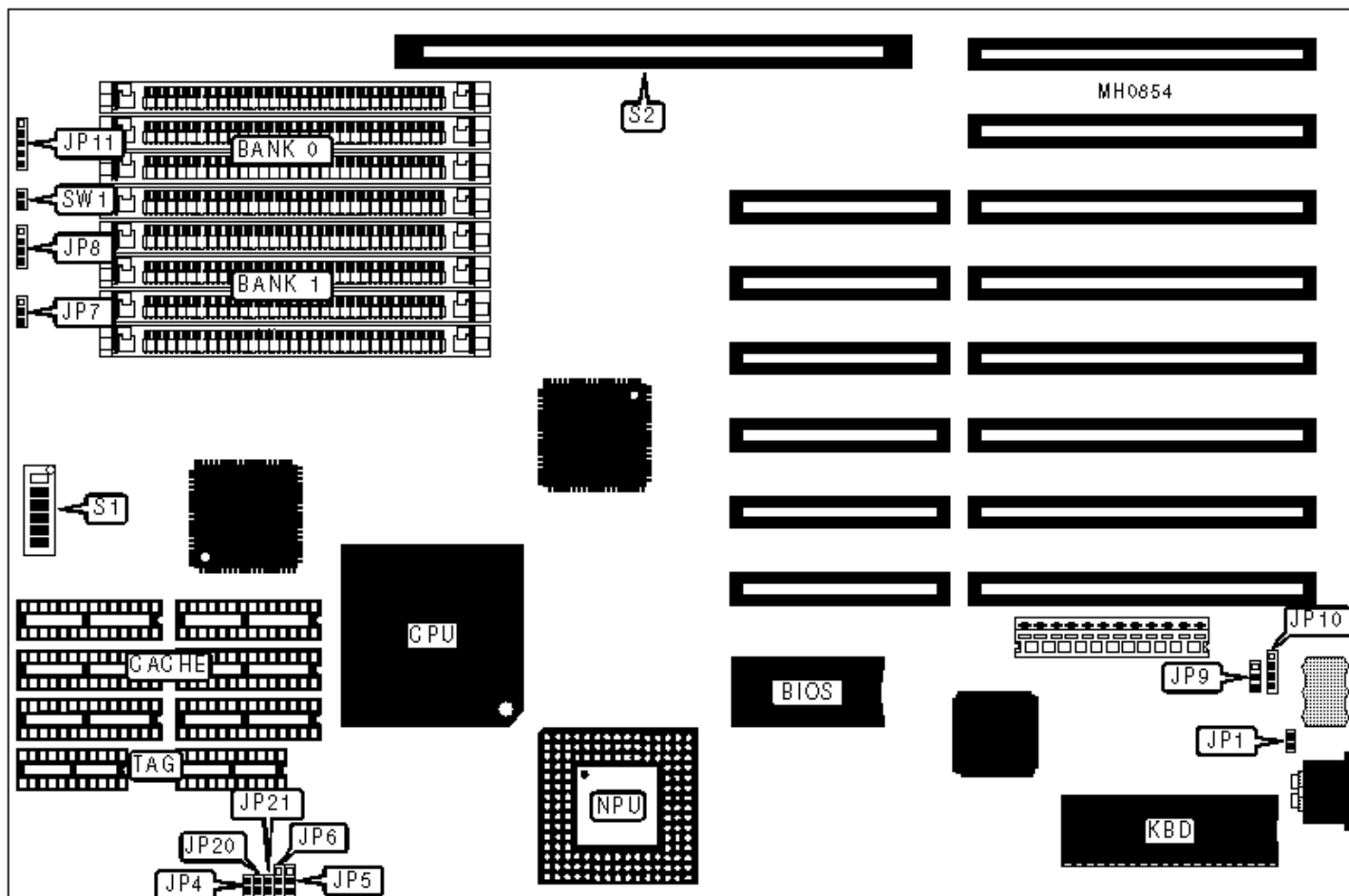
CONNECTIONS

Purpose	Location	Purpose	Location
Speaker	JP8	Reset switch	SW1
External battery	JP10	32-bit external memory card	S2
Power LED & keylock	JP11		

USER CONFIGURABLE SETTINGS

	Function	Jumper	Position
»	Monitor type select color	JP1	Closed
	Monitor type select monochrome	JP1	Open
»	Speaker type select internal	JP7	pins 1 & 2 closed
	Speaker type select external	JP7	pins 2 & 3 closed
»	Battery type select internal	JP9	pins 1 & 2 closed
	Battery type select external	JP9	pins 2 & 3 closed
»	Factory configured - do not alter	JP31	Closed
»	Factory configured - do not alter	JP32	Open
»	Factory configured - do not alter	JP33	Closed
»	Factory configured - do not alter	JP34	Closed
»	Factory configured - do not alter	JP35	Closed
»	Factory configured - do not alter	JP36	Closed
»	Factory configured - do not alter	JP37	Closed
»	Factory configured - do not alter	JP39	pins 2 & 3 closed
»	Factory configured - do not alter	JP40	pins 1 & 2 closed
»	Factory configured - do not alter	JP41	pins 2 & 3 closed
»	Factory configured - do not alter	JP45	pins 1 & 2, 3 & 4 closed
»	Factory configured - do not alter	JP46	pins 1 & 2, 3 & 4 closed

»	Factory configured - do not alter	JP48	Open

DRAM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1 M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION

Size	Bank 0	TAG
64KB	(8) 8K x 8	(2) 16K x 4
256KB	(8) 32K x 8	(2) 16K x 4

CACHE JUMPER CONFIGURATION

Size	JP4	JP5	JP6	JP20	JP21
64KB	Open	2 & 3	2 & 3	Open	Open
256KB	Closed	1 & 2	1 & 2	Closed	Closed

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION

Speed	S1/3	S1/4	S1/5	S1/6
25MHz	On	Off	Off	Off
33MHz	On	Off	Open	On
40MHz	Off	On	Off	On