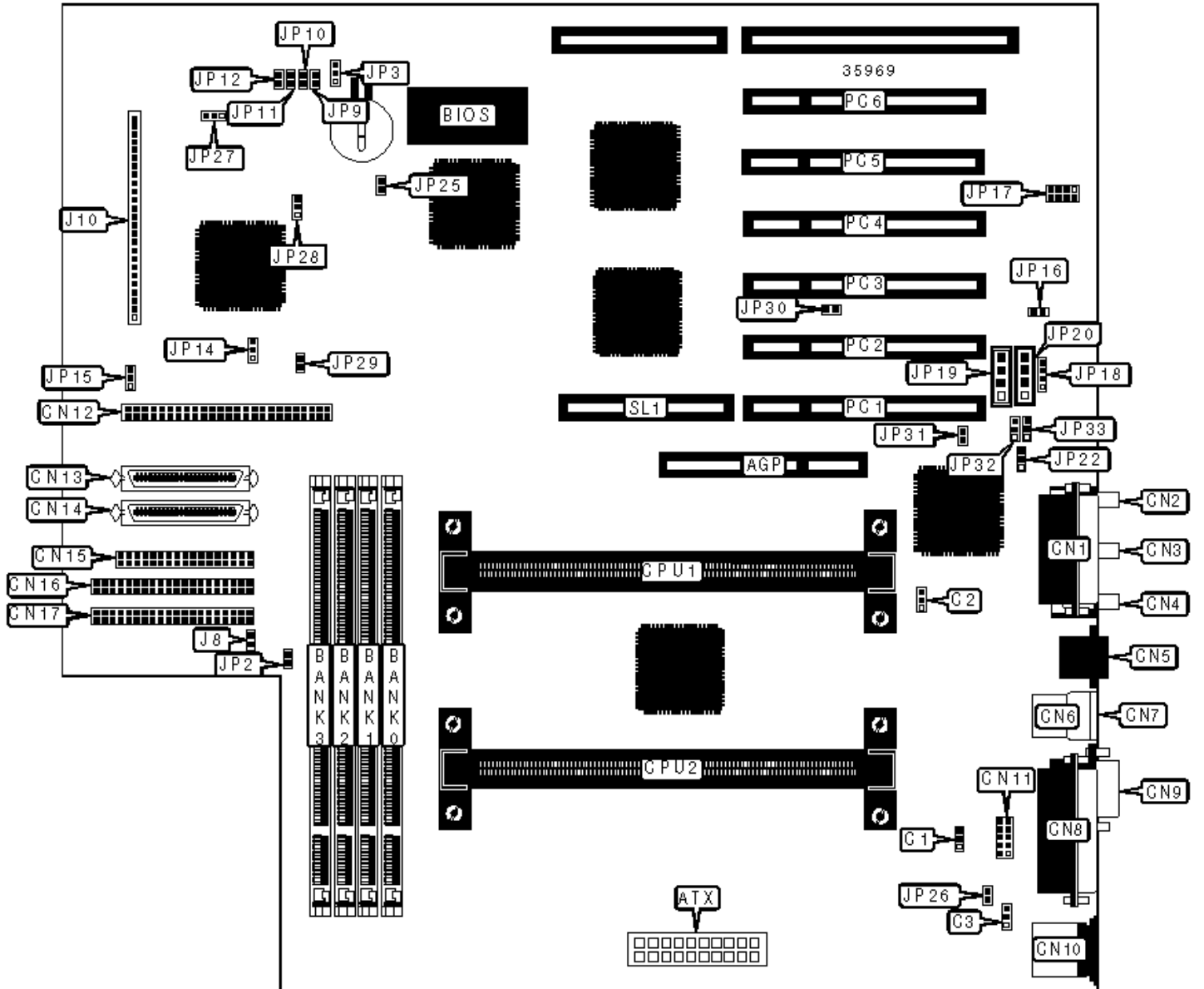


TYAN COMPUTER CORPORATION

S1836DLUAN

Configuration



CONNECTIONS

| Purpose | Location | Purpose | Location |
|----------------------------|----------|------------------------------|------------------|
| AGP slot | AGP | Floppy drive interface | CN15 |
| ATX power connector | ATX | IDE interface 2 | CN16 |
| CPU fan power | C1 | IDE interface 1 | CN17 |
| CPU fan power | C2 | Chassis intrusion connector | J8 |
| Chassis fan power | C3 | Soft off power supply | J10/pins 1 & 2 |
| Game/MIDI port | CN1 | Green PC connector | J10/pins 3 & 4 |
| Microphone in | CN2 | IR connector | J10/pins 6 - 10 |
| Line in | CN3 | IDE interface LED | J10/pins 13 - 16 |
| Line out | CN4 | Power LED | J10/pins 18 & 20 |
| Ethernet 10BaseT connector | CN5 | Reset switch | J10/pins 22 & 23 |
| USB connector 1 | CN6 | Speaker | J10/pins 24 - 27 |
| USB connector 2 | CN7 | Wavetable connector | JP17 |
| Parallel port | CN8 | Modem connector | JP18 |
| Serial port 1 | CN9 | Audio in - CD-ROM(Panasonic) | JP19 |
| PS/2 mouse port | CN10 | Audio in - CD-ROM (Mitsumi) | JP20 |
| Serial port 2 | CN11 | Wake on LAN connector | JP22 |
| SCSI interface | CN12 | Green PC connector | JP25 |
| Wide SCSI interface | CN13 | 32-bit PCI slots | PC1 - PC6 |
| Wide SCSI interface | CN14 | RAID slot | SL1 |

USER CONFIGURABLE SETTINGS

| Function | Label | Position |
|-------------------------------------|-------|-------------------|
| » Factory configured - do not alter | JP2 | Unidentified |
| » CMOS memory normal operation | JP3 | Pins 1 & 2 closed |

| | | | |
|---|-----------------------------------|------|-------------------|
| | CMOS memory clear | JP3 | Pins 2 & 3 closed |
| » | On board sound enabled | JP16 | Open |
| | On board sound disabled | JP16 | Closed |
| | WOL power disabled | JP26 | Open |
| | WOL power enabled | JP26 | Closed |
| » | SCSI tagram enabled | JP27 | Pins 1 & 2 closed |
| | SCSI tagram disabled | JP27 | Pins 2 & 3 closed |
| » | Factory configured - do not alter | JP28 | Pins 1 & 2 closed |
| » | SCSI enabled | JP29 | Open |
| | SCSI disabled | JP29 | Closed |
| » | PCI-PCI bridge enabled | JP30 | Open |
| | PCI-PCI bridge disabled | JP30 | Closed |
| » | Ethernet enabled | JP31 | Open |
| | Ethernet disabled | JP31 | Closed |

DIMM CONFIGURATION

| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
|------|-------------|-------------|-------------|-------------|
| 8MB | (1) 1M x 64 | None | None | None |
| 16MB | (1) 2M x 64 | None | None | None |
| 16MB | (1) 1M x 64 | (1) 1M x 64 | None | None |
| 24MB | (1) 2M x 64 | (1) 1M x 64 | None | None |
| 24MB | (1) 1M x 64 | (1) 1M x 64 | (1) 1M x 64 | None |
| 32MB | (1) 4M x 64 | None | None | None |
| 32MB | (1) 2M x 64 | (1) 2M x 64 | None | None |
| 32MB | (1) 1M x 64 | (1) 1M x 64 | (1) 1M x 64 | (1) 1M x 64 |
| 40MB | (1) 4M x 64 | (1) 1M x 64 | None | None |

| | | | | |
|-------|--------------|--------------|--------------|-------------|
| 48MB | (1) 4M x 64 | (1) 2M x 64 | None | None |
| 48MB | (1) 2M x 64 | (1) 2M x 64 | (1) 2M x 64 | None |
| 64MB | (1) 2M x 64 | (1) 2M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 64MB | (1) 8M x 64 | None | None | None |
| 64MB | (1) 4M x 64 | (1) 4M x 64 | None | None |
| 72MB | (1) 8M x 64 | (1) 1M x 64 | None | None |
| 80MB | (1) 8M x 64 | (1) 2M x 64 | None | None |
| 96MB | (1) 8M x 64 | (1) 4M x 64 | None | None |
| 96MB | (1) 4M x 64 | (1) 4M x 64 | (1) 4M x 64 | None |
| 128MB | (1) 16M x 64 | None | None | None |
| 128MB | (1) 8M x 64 | (1) 8M x 64 | None | None |
| 128MB | (1) 4M x 64 | (1) 4M x 64 | (1) 4M x 64 | (1) 4M x 64 |
| 136MB | (1) 16M x 64 | (1) 1M x 64 | None | None |
| 144MB | (1) 16M x 64 | (1) 2M x 64 | None | None |
| 176MB | (1) 16M x 64 | (1) 2M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 192MB | (1) 16M x 64 | (1) 8M x 64 | None | None |
| 192MB | (1) 8M x 64 | (1) 8M x 64 | (1) 8M x 64 | None |
| 256MB | (1) 32M x 64 | None | None | None |
| 256MB | (1) 16M x 64 | (1) 16M x 64 | None | None |
| 256MB | (1) 8M x 64 | (1) 8M x 64 | (1) 8M x 64 | (1) 8M x 64 |
| 272MB | (1) 16M x 64 | (1) 16M x 64 | (1) 1M x 64 | (1) 1M x 64 |
| 280MB | (1) 32M x 64 | (1) 1M x 64 | (1) 1M x 64 | (1) 1M x 64 |
| 288MB | (1) 16M x 64 | (1) 16M x 64 | (1) 2M x 64 | (1) 2M x 64 |
| 320MB | (1) 16M x 64 | (1) 16M x 64 | (1) 4M x 64 | (1) 4M x 64 |
| 384MB | (1) 16M x 64 | (1) 16M x 64 | (1) 16M x 64 | None |
| 448MB | (1) 32M x 64 | (1) 8M x 64 | (1) 8M x 64 | (1) 8M x 64 |

| | | | | |
|---|--------------|--------------|--------------|--------------|
| 512MB | (1) 32M x 64 | (1) 32M x 64 | None | None |
| 512MB | (1) 16M x 64 | (1) 16M x 64 | (1) 16M x 64 | (1) 16M x 64 |
| 640MB | (1) 32M x 64 | (1) 16M x 64 | (1) 16M x 64 | (1) 16M x 64 |
| 768MB | (1) 32M x 64 | (1) 32M x 64 | (1) 32M x 64 | None |
| 1024MB | (1) 32M x 64 | (1) 32M x 64 | (1) 32M x 64 | (1) 32M x 64 |
| Note: Board accepts EDO & SDRAM memory. | | | | |

CACHE CONFIGURATION

256KB/512KB cache is located on the Pentium II CPU. 128KB cache is located on the Celeron 300A & 333 C

CPU SPEED SELECTION

| CPU speed | Clock speed | Multiplier | JP9 | JP10 | JP11 | JP12 |
|-----------|-------------|------------|--------|--------|--------|--------|
| 233MHz | 66MHz | 3.5x | Closed | Open | Open | Closed |
| 266MHz | 66MHz | 4x | Open | Closed | Closed | Closed |
| 300MHz | 66MHz | 4.5x | Open | Closed | Open | Closed |
| 300MHz | 100MHz | 3x | Closed | Open | Closed | Closed |
| 333MHz | 66MHz | 5x | Open | Open | Closed | Closed |
| 350MHz | 100MHz | 3.5x | Closed | Open | Open | Closed |
| 400MHz | 100MHz | 4x | Open | Closed | Closed | Closed |
| 450MHz | 100MHz | 4.5x | Open | Closed | Open | Closed |
| 500MHz | 100MHz | 5x | Open | Open | Closed | Closed |

SCSI TERMINATION SELECTION

| Setting | | JP14 | JP15 |
|---------|------------------------------|-------------------|-------------------|
| » | Termination enabled | Pins 1 & 2 closed | Pins 1 & 2 closed |
| | Termination set by SCSI chip | Pins 2 & 3 closed | Pins 2 & 3 closed |

LINE OUT SELECTION

| Setting | | JP32 | JP33 |
|---------|-----------------|-------------------|-------------------|
| » | Preamp disabled | Pins 2 & 3 closed | Pins 2 & 3 closed |
| | Preamp enabled | Pins 1 & 2 closed | Pins 1 & 2 closed |