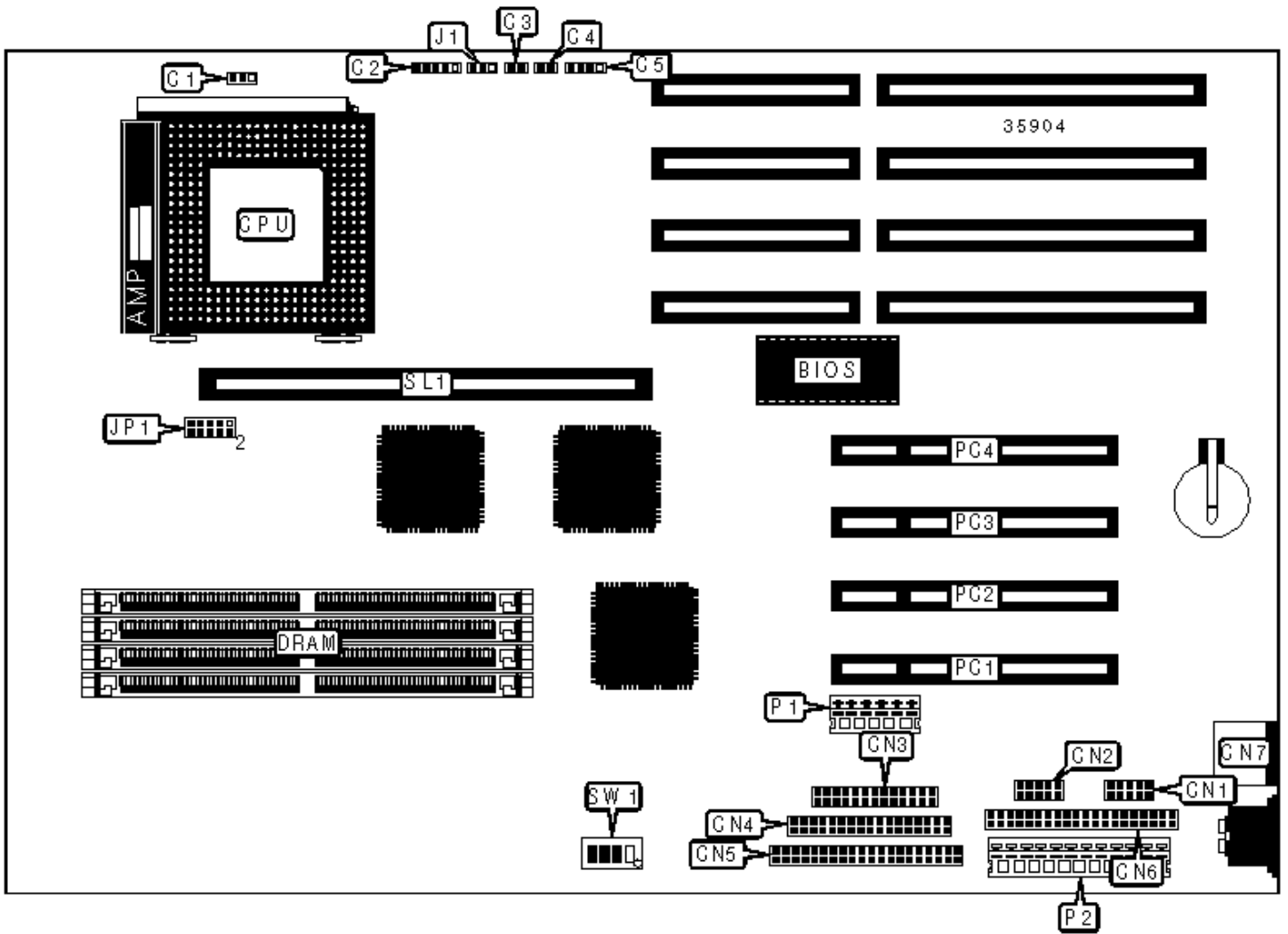


TEXAS MICRO, INC.

P5000HX-M-WMB

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Chassis fan power	C1	Floppy drive interface	CN4
Power LED & keylock	C2	IDE interface 1	CN5
Reset switch	C3	IDE interface 2	CN6
IDE interface LED	C4	PS/2 mouse port	CN7
Speaker	C5	3.3v power	P1
Serial port 1	CN1	5v power	P2
Serial port 2	CN2	32-bit PCI slots	PC1 - PC4
Parallel port	CN3	Cache slot	SL1

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	J1	Unidentified
» Monitor type select color	SW1/1	On
Monitor type select monochrome	SW1/1	Off
» Flash BIOS enabled; crisis recovery mode disabled	SW1/2	Off
Flash BIOS disabled; crisis recovery mode enabled	SW1/2	On
» CMOS memory normal operation	SW1/3	Off
CMOS memory clear	SW1/3	On
» Factory configured - do not alter	SW1/4	Unidentified

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None

16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION	
Size	SL1
256KB	256KB module installed
512KB	512KB module installed

CPU SPEED SELECTION			
CPU speed	Clock speed	Multiplier	JP1
200MHz	66MHz	3x	Pins 1 & 3, 5 & 7, 8 & 10 closed
233MHz	66MHz	3.5x	Pins 1 & 3, 7 & 9, 8 & 10 closed