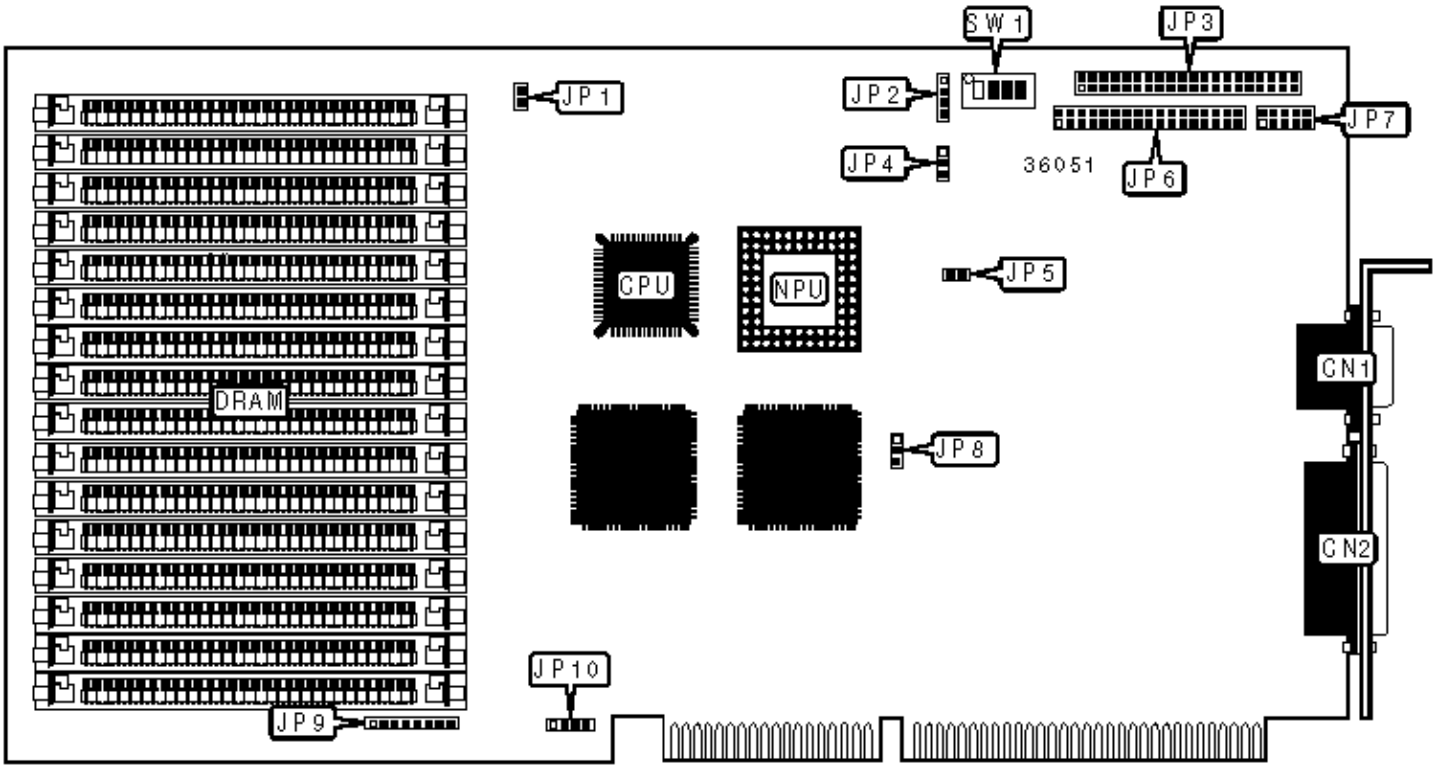


TEXAS MICRO, INC.

D386WB CPU (REV. E)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Serial port 1	CN1	IDE interface	JP3
Parallel port	CN2	Floppy drive interface	JP6
IDE interface LED	JP1	Serial port 2	JP7
External battery	JP2	Keyboard connector	JP9

USER CONFIGURABLE SETTINGS

Function	Label	Position
» On board battery enabled	JP2	Pins 1 & 2 closed
On board battery disabled	JP2	Pins 2 & 3 closed
» Watchdog timer enabled	JP4	Pins 1 & 2 closed
Watchdog timer disabled	JP4	Pins 2 & 3 closed
» Boot from card enabled	JP5	Open
Boot from external EPROM enabled	JP5	Closed
» NPU synchronous with CPU	JP8	Pins 1 & 2 closed
NPU asynchronous with CPU	JP8	Pins 2 & 3 closed
» On board speaker enabled	JP10	Pins 3 & 4 closed
On board speaker disabled	JP10	Pins 1 & 2 closed
» Monitor type select color	SW1/1	On
Monitor type select monochrome	SW1/1	Off
» Factory configured - do not alter	SW1/2	Off
» CMOS memory normal operation	SW1/3	Off
CMOS memory clear	SW1/3	On
» Factory configured - do not alter	SW1/4	Off

SIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	None	None	None
2MB	(4) 256K x 9	(4) 256K x 9	None	None
4MB	(4) 1M x 9	None	None	None
5MB	(4) 256K x 9	(4) 1M x 9	None	None
8MB	(4) 1M x 9	(4) 1M x 9	None	None
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	None
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None	None	None
20MB	(4) 1M x 9	(4) 4M x 9	None	None
20MB	(4) 4M x 9	(4) 1M x 9	None	None
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	None
24MB	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9	None
24MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	None
28MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9
28MB	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
28MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	None	None
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	None
36MB	(4) 4M x 9	(4) 1M x 9	(4) 4M x 9	None
36MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	None
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
40MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9
40MB	(4) 4M x 9	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9

DIMM/SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2	Bank 3
40MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	None
52MB	(4) 4M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
52MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
52MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

Note: The location of banks are unidentified.