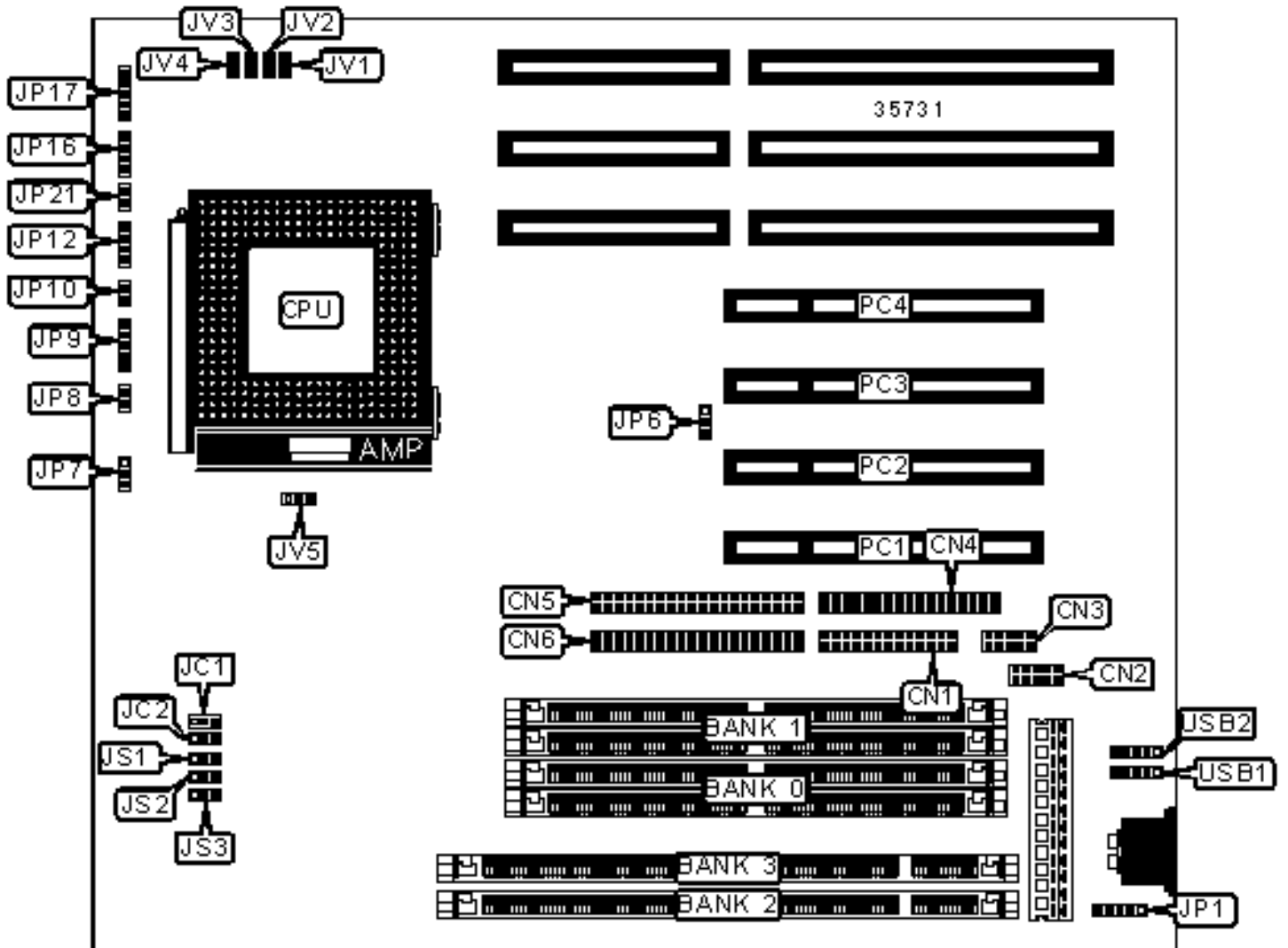


QDI COMPUTER, INC.

P5I430TX-400

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Parallel port	CN1	Power LED & keylock	JP9
Serial port	CN2	Reset switch	JP10
Serial port	CN3	IDE interface LED	JP12
Floppy drive interface	CN4	Speaker	JP16
IDE interface 2	CN5	IR connector	JP17
IDE interface 1	CN6	Turbo LED	JP21
PS/2 mouse interface	JP1	32-bit PCI slots	PC1 – PC4
Chassis fan power	JP7	USB connector 1	USB1
Green PC connector	JP8	USB connector 2	USB2

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	JP6	Open
CMOS memory clear	JP6	Closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36

48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None

SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory.

DIMM CONFIGURATION

Size	Bank 2	Bank 3
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64

40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
Note: Board accepts EDO & SDRAM memory.		

CACHE CONFIGURATION

Note: the location of the cache is unidentified.

CPU SPEED SELECTION (CX 6X86)

CPU speed	Clock speed	Multiplier	JC1	JC2	JS1	JS2	JS3
120MHz	50MHz	2x	1 & 2	2 & 3	1 & 2	1 & 2	1 & 2
133MHz	55MHz	2x	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3
150MHz	60MHz	2x	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3
166MHz	66MHz	2x	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3
200MHz	75MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX 6X86MX)

CPU speed	Clock speed	Multiplier	JC1	JC2	JS1	JS2	JS3
166MHz	60MHz	2.5x	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
200MHz	75MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2	2 & 3
200MHz	66MHz	2.5x	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
233MHz	75MHz	2.5x	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3
233MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5)

CPU speed	Clock speed	Multiplier	JC1	JC2	JS1	JS2	JS3
75MHz	50MHz	1.5x	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
90MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3
100MHz	66MHz	1.5x	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
120MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3
133MHz	66MHz	1.5x	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
166MHz	66MHz	2.5x	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
200MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K6)							
CPU speed	Clock speed	Multiplier	JC1	JC2	JS1	JS2	JS3
166MHz	66MHz	2.5x	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
200MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3
233MHz	66MHz	3.5x	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JC1	JC2	JS1	JS2	JS3
75MHz	50MHz	1.5x	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
90MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3
100MHz	66MHz	1.5x	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
120MHz	60MHz	2x	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3
133MHz	66MHz	2x	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3
150MHz	60MHz	2.5x	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
166MHz	66MHz	2.5x	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3

180MHz	60MHz	3x	2 & 3	1 & 2	1 & 2	2 & 3	2 & 3
200MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL MMX)							
CPU speed	Clock speed	Multiplier	JC1	JC2	JS1	JS2	JS3
166MHz	66MHz	2.5x	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
200MHz	66MHz	3x	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3
233MHz	66MHz	3.5x	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.							

CPU VOLTAGE SELECTION (SINGLE)					
Voltage	JV1	JV2	JV3	JV4	JV5
3.3v	Closed	Open	Closed	Closed	2 & 3
3.5v	Closed	Closed	Closed	Closed	2 & 3
Note: Pins designated should be in the closed position.					

CPU VOLTAGE SELECTION (DUAL)						
Voltage	V core	JV1	JV2	JV3	JV4	JV5
3.3v	2.8v	Open	Open	Open	Closed	1 & 2
3.3v	2.9v	Closed	Open	Open	Closed	1 & 2
3.3v	3.2v	Open	Open	Closed	Closed	1 & 2
Note: Pins designated should be in the closed position.						