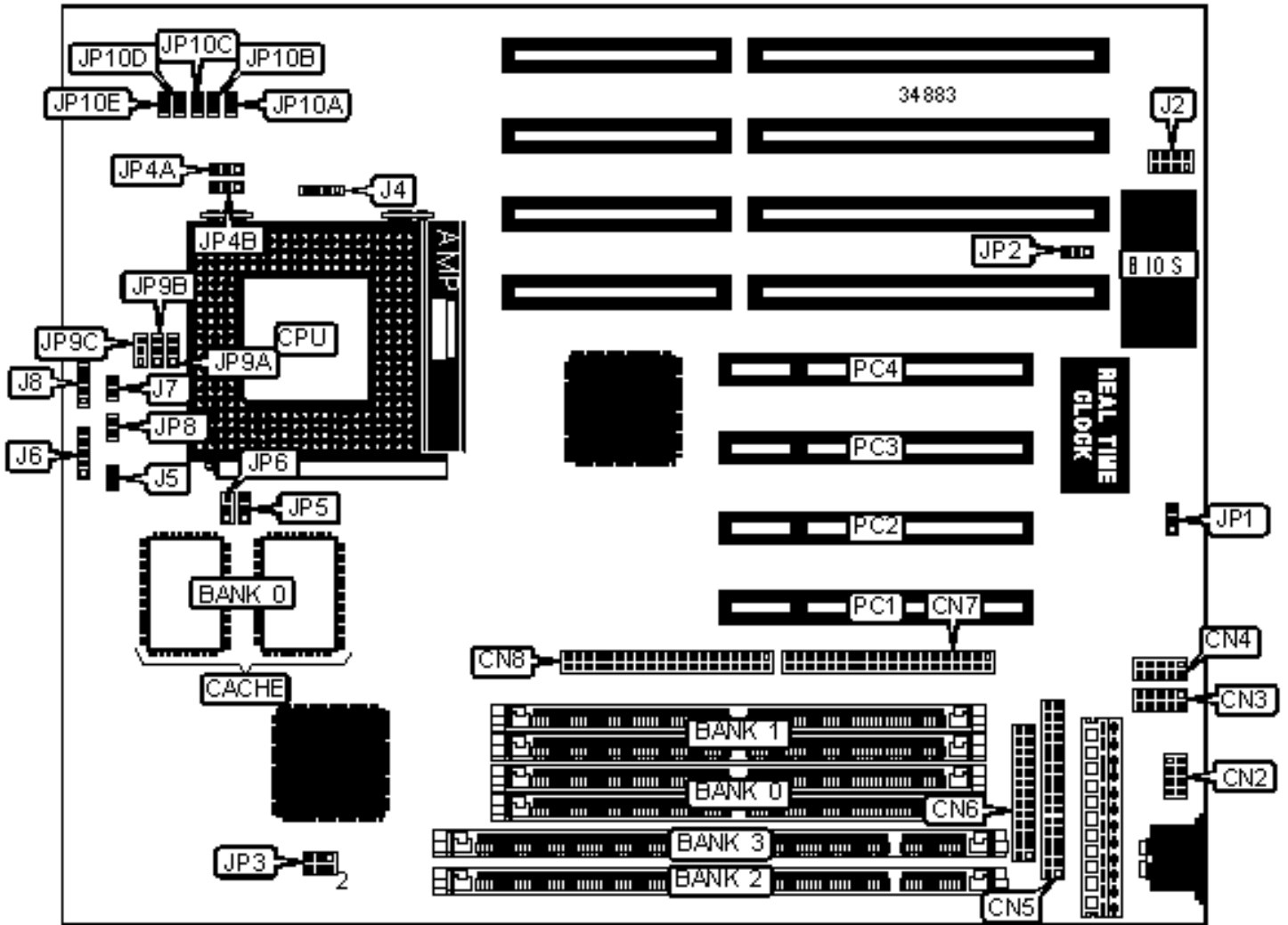


PC WAVE, INC.

M-560

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
PS/2 mouse interface	CN2	Chassis fan power	J4
Serial port 1	CN3	IDE interface LED	J5
Serial port 2	CN4	Power LED & keylock	J6
Floppy drive interface	CN5	Reset switch	J7
Parallel port	CN6	Speaker	J8
IDE interface 2	CN7	Turbo LED	JP8
IDE interface 1	CN8	32-bit PCI slots	PC1 – PC4
USB connector	J2		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	JP1	Pins 1 & 2 closed
CMOS memory clear	JP1	Pins 2 & 3 closed
» Factory configured - do not alter	JP9C	Unidentified

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36

48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory. Do not populate banks 0 & 3 at the same time.

DIMM CONFIGURATION

Size	Bank 2	Bank 3
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None

64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
Note: Board accepts SDRAM memory.		

DIMM/SIMM VOLTAGE CONFIGURATION	
Voltage	JP3
3.3v	Pins 1 & 3, 2 & 4 closed
5v	Pins 3 & 5, 4 & 6 closed

CACHE CONFIGURATION	
Size	Bank 0
512KB	(2) 64K x 32

CPU SPEED SELECTION (CX 6X86)						
CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
120MHz	60MHz	2x	2 & 3	2 & 3	2 & 3	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
150MHz	75MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2
166MHz	83MHz	2x	1 & 2	1 & 2	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (IBM 6X86)						
CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B

120MHz	60MHz	2x	2 & 3	2 & 3	2 & 3	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
150MHz	75MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2
166MHz	83MHz	2x	1 & 2	1 & 2	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (CX 6X86L)						
CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
120MHz	60MHz	2x	2 & 3	2 & 3	2 & 3	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
150MHz	75MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2
166MHz	83MHz	2x	1 & 2	1 & 2	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (IBM 6X86L)						
CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
120MHz	60MHz	2x	2 & 3	2 & 3	2 & 3	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
150MHz	75MHz	2x	1 & 2	2 & 3	2 & 3	1 & 2
166MHz	83MHz	2x	1 & 2	1 & 2	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (CX 6X86MX)						
CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3
200MHz	66MHz	3x	2 & 3	1 & 2	1 & 2	2 & 3

233MHz	75MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (IBM 6X86MX)						
CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3
200MHz	66MHz	3x	2 & 3	1 & 2	1 & 2	2 & 3
233MHz	75MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (AM K5)						
CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
75MHz	50MHz	1.5x	Unidentified	Unidentified	1 & 2	1 & 2
90MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	1 & 2
100MHz	66MHz	1.5x	2 & 3	1 & 2	1 & 2	1 & 2
120MHz	60MHz	2x	2 & 3	2 & 3	2 & 3	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
150MHz	60MHz	2.5x	2 & 3	2 & 3	2 & 3	2 & 3
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (AM K6)						
CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
200MHz	66MHz	3x	2 & 3	1 & 2	1 & 2	2 & 3
233MHz	66MHz	3.5x	2 & 3	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (IDT C6)

CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
200MHz	66MHz	3x	2 & 3	1 & 2	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
75MHz	50MHz	1.5x	Unidentified	Unidentified	1 & 2	1 & 2
90MHz	60MHz	1.5x	2 & 3	2 & 3	1 & 2	1 & 2
100MHz	66MHz	1.5x	2 & 3	1 & 2	1 & 2	1 & 2
120MHz	60MHz	2x	2 & 3	2 & 3	2 & 3	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	2 & 3	1 & 2
150MHz	60MHz	2.5x	2 & 3	2 & 3	2 & 3	2 & 3
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3
180MHz	60MHz	3x	2 & 3	2 & 3	1 & 2	2 & 3
200MHz	66MHz	3x	2 & 3	1 & 2	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL MMX)

CPU speed	Clock speed	Multiplier	JP5	JP6	JP9A	JP9B
166MHz	66MHz	2.5x	2 & 3	1 & 2	2 & 3	2 & 3
200MHz	66MHz	3x	2 & 3	1 & 2	1 & 2	2 & 3
233MHz	66MHz	3.5x	2 & 3	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION

Type	JP4A	JP4B
Single voltage CPU	Pins 1 & 2 closed	Pins 1 & 2 closed
Dual voltage CPU	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU VOLTAGE SELECTION

Voltage	JP10A	JP10B	JP10C	JP10D	JP10E
2.5v	Open	Open	Open	Open	Open
2.8v	Open	Open	Open	Open	Closed
2.9v	Open	Open	Open	Closed	Open
3.2v	Open	Open	Closed	Open	Open
3.3v	Open	Closed	Open	Open	Open
3.5v	Closed	Open	Open	Open	Open