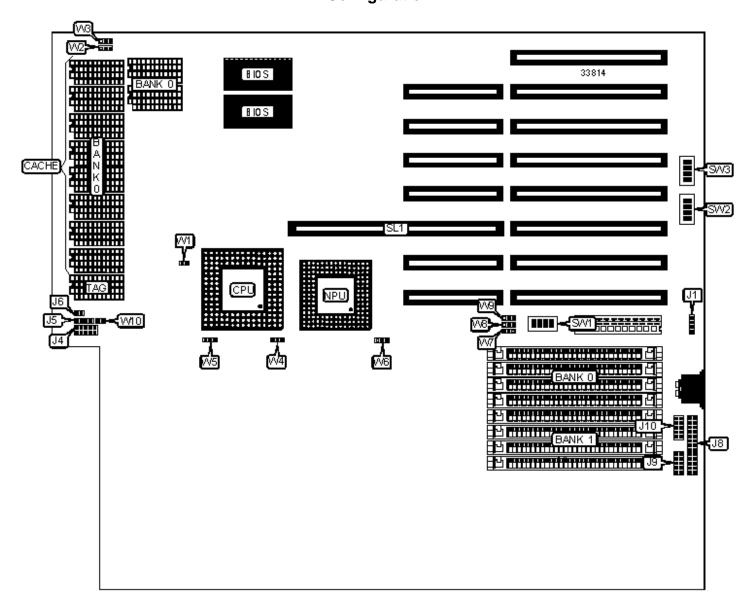
## PARCORP, INC

## 486-33

## Configuration



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	J1	Speaker	J5	
Front panel connector	J4	External memory card	SL1	

	USER CONFIGURABLE SETTINGS					
	Function	Label	Position			
»	Factory configured - do not alter	J6	Unidentified			
»	Factory configured - do not alter	J8	Unidentified			
»	Factory configured - do not alter	J9	Unidentified			
»	Factory configured - do not alter	J10	Unidentified			
»	Factory configured - do not alter	SW2	Unidentified			
»	Factory configured - do not alter	SW3	Unidentified			
»	Factory configured - do not alter	W1	Open			
»	Factory configured - do not alter	W4	Pins 2 & 3 closed			
»	Factory configured - do not alter	W5	Pins 1 & 2 closed			
»	Monitor type select color	W7	Pins 1 & 2 closed			
	Monitor type select monochrome	W7	Pins 2 & 3 closed			
»	Speaker enabled	W10	Closed			
	Speaker disabled	W10	Open			

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	
1MB	(4) 256K x 9	None	None	None	
2MB	(4) 256K x 9	(4) 256K x 9	None	None	
4MB	(4) 1M x 9	None	None	None	

6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	None
8MB	(4) 1M x 9	(4) 1M x 9	None	None
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	None
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
	•			

Note: Banks 2 & 3 are located on the external memory card.

DRAM SWITCH CONFIGURATION					
Size	SW1/1	SW1/2	SW1/3	SW1/4	
1MB	On	Off	On	On	
2MB	Off	Off	On	Off	
4MB	On	On	On	On	
6MB	Off	Off	Off	On	
8MB	Off	On	On	Off	
10MB	Off	Off	Off	Off	
12MB	Off	On	Off	On	
16MB	Off	On	Off	Off	

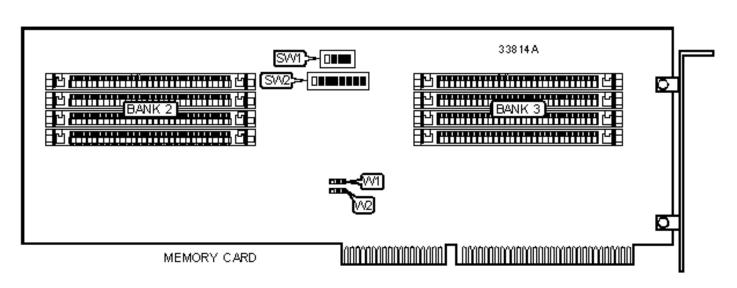
DRAM JUMPER CONFIGURATION						
SIMM in Bank 0 SIMM in Bank 1 W8 W9						
256KB	N/A	N/A	Pins 2 & 3 closed			
1MB	N/A	N/A	Pins 1 & 2 closed			
N/A	256KB	Pins 2 & 3 closed	N/A			
N/A	1MB	Pins 1 & 2 closed	N/A			

## **CACHE CONFIGURATION**

Size	Bank 0	TAG
64KB	(10) 16K x 4	(1) 16K x 1
256KB	(10) 64K x 4	(1) 64K x 1

CACHE JUMPER CONFIGURATION					
Size	W2	W3			
64KB	Pins 1 & 2 closed	Pins 1 & 2 closed			
256KB	Pins 2 & 3 closed	Pins 2 & 3 closed			

CPU SPEED SELECTION			
Speed W6			
8MHz (only)	Pins 1 & 2 closed		
33MHz (only)	Open		
33MHz (switchable)	Pins 2 & 3 closed		



	USER CONFIGURABLE SETTINGS					
Function Label		Position				
»	Factory configured - do not alter	SW1/4	N/A			
»	Factory configured - do not alter	W1	Pins 1 & 2 closed			

»	Factory configured - do not alter	W2	Pins 1 & 2 closed	
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DRAM SWITCH CONFIGURATION					
Memory upgrade	Bank 2	Bank 3	SW1/1	SW1/2	SW1/3
4MB	4MB	None	Off	Off	On
8MB	4MB	4MB	Off	Off	Off

	DRAM SWITCH CONFIGURATION								
DRAI	И on mainb	oa&W2/1	SW2/2	SW2/3	SW2/4	SW2/5	SW2/6	SW2/7	SW2/8
	4MB	On	Off	Off	On	Off	Off	On	On
	8MB	On	On	Off	Off	Off	Off	On	On