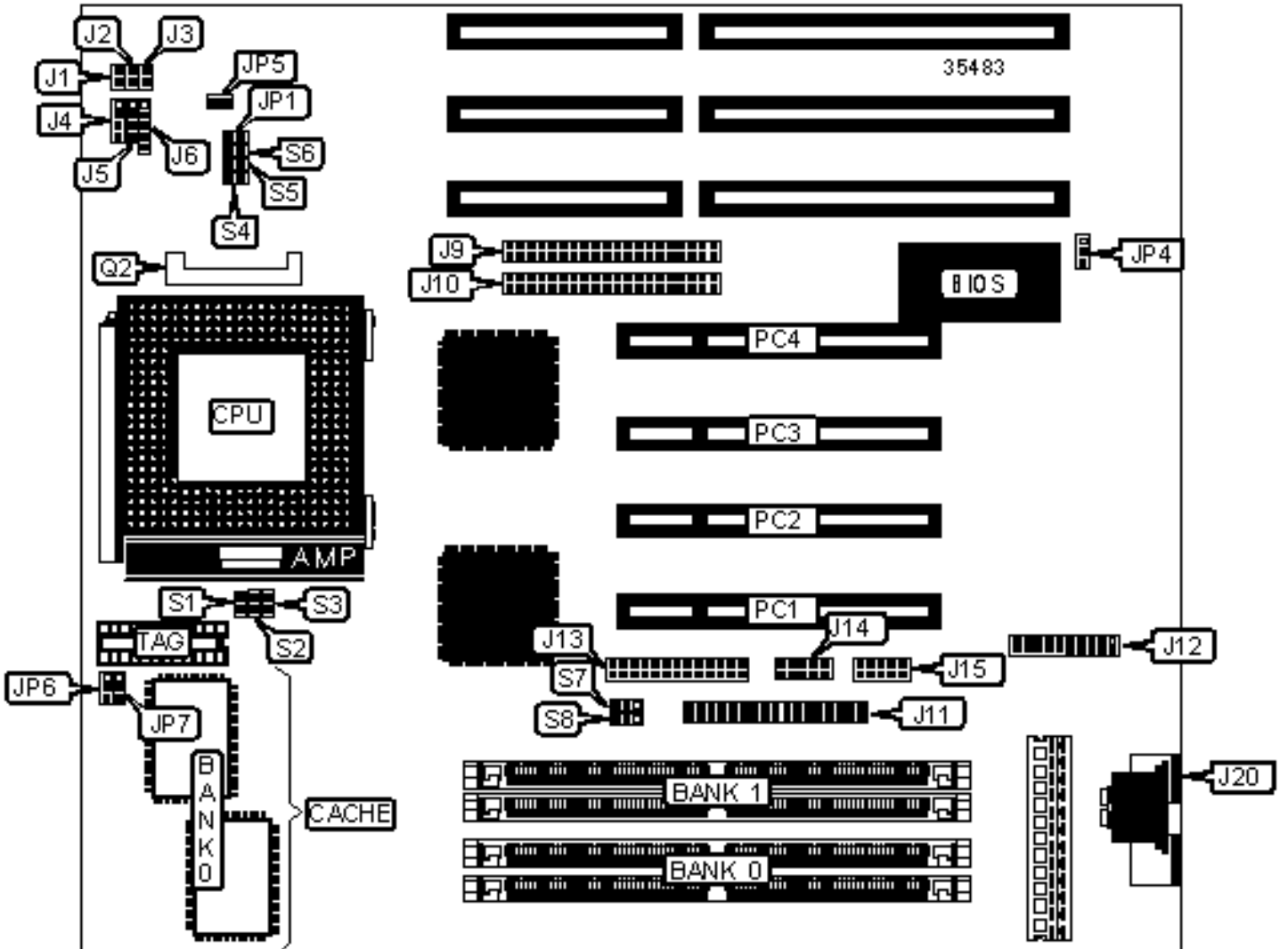


DTK COMPUTER, INC.

PAM-0056I (VER. 3.02)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Reset switch	J1	USB 1 connector	J12/pins 2 - 5
Green PC connector	J2	PS/2 mouse interface	J12/pins 6 - 10
Green PC LED connector	J3	USB 2 connector	J12/pins 12 - 15
Speaker	J4	IR connector	J12/pins 16 - 20
IDE interface LED	J5	Parallel port	J13
Power LED & keylock	J6	Serial port 2	J14
IDE interface 1	J9	Serial port 1	J15
IDE interface 2	J10	PS/2 mouse port	J20
Floppy drive interface	J11	32-bit PCI slots	PC1 - PC4

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Flash BIOS voltage select 5v	JP4	Pins 2 & 3 closed
Flash BIOS voltage select 12v	JP4	Pins 1 & 2 closed
» CMOS memory normal operation	JP5	Open
CMOS memory clear	JP5	Closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1MB x 36	None
16MB	(2) 1MB x 36	(2) 1MB x 36
16MB	(2) 2MB x 36	None
24MB	(2) 2MB x 36	(2) 1MB x 36
32MB	(2) 2MB x 36	(2) 2MB x 36

32MB	(2) 4MB x 36	None
40MB	(2) 4MB x 36	(2) 1MB x 36
48MB	(2) 4MB x 36	(2) 2MB x 36
64MB	(2) 4MB x 36	(2) 4MB x 36
64MB	(2) 8MB x 36	None
72MB	(2) 8MB x 36	(2) 1MB x 36
80MB	(2) 8MB x 36	(2) 2MB x 36
96MB	(2) 8MB x 36	(2) 4MB x 36
128MB	(2) 8MB x 36	(2) 8MB x 36

CACHE CONFIGURATION		
Size	Bank 0	TAG
256K	(2) 32K x 32	16K x 8 or 32K x 8
512K	(2) 64K x 32	16K x 8 or 32K x 8

CPU SPEED SELECTION (INTEL)							
Speed	Clock speed	Multiplier	JP6	JP7	S1	S2	S3
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open
120MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open	Open
133MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed	Open
150MHz	60MHz	2.5x	2 & 3	2 & 3	Closed	Open	Open
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	Closed	Open
200MHz	66MHz	3x	2 & 3	1 & 2	Open	Closed	Open

Note: Pins designated are in the closed position.

CPU SPEED SELECTION (AM K5)

Speed	Clock speed	Multiplier	JP6	JP7	S1	S2	S3
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open
120MHz	60MHz	2x	1 & 2	1 & 2	Closed	Open	Open
133MHz	66MHz	2x	1 & 2	1 & 2	Open	Closed	Open
150MHz	60MHz	2.5x	1 & 2	1 & 2	Open	Closed	Open
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	Closed	Open

Note: Pins designated are in the closed position.

CPU SPEED SELECTION (CX 6X86)

Speed	Clock speed	Multiplier	JP6	JP7	S1	S2	S3
120MHz	50MHz	2x	1 & 2	2 & 3	Closed	Closed	Open
133MHz	55MHz	2x	1 & 2	2 & 3	Open	Open	Open
150MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open	Open
166MHz	66MHz	2x	2 & 3	2 & 3	Open	Closed	Open
200MHz	75MHz	2x	1 & 2	2 & 3	Open	Open	Closed

Note: Pins designated are in the closed position.

CPU VOLTAGE SELECTION (SINGLE)

Voltage	Q2	JP1	S4	S5	S6
3.4V	2 Transistors installed	Closed	Closed	Closed	Closed
3.5V	2 Transistors installed	Open	Closed	Closed	Closed

CPU VOLTAGE SELECTION (DUAL)

V core	V I/O	Q2	JP1	S4	S5	S6
3.4V	2.8V	2 Transistors installed	Closed	Open	Open	Open

DRAM VOLTAGE SELECTION

Voltage	S7	S8
5V	Pins 1 & 2 closed	Pins 1 & 2 closed
3.3V	Pins 2 & 3 closed	Pins 2 & 3 closed