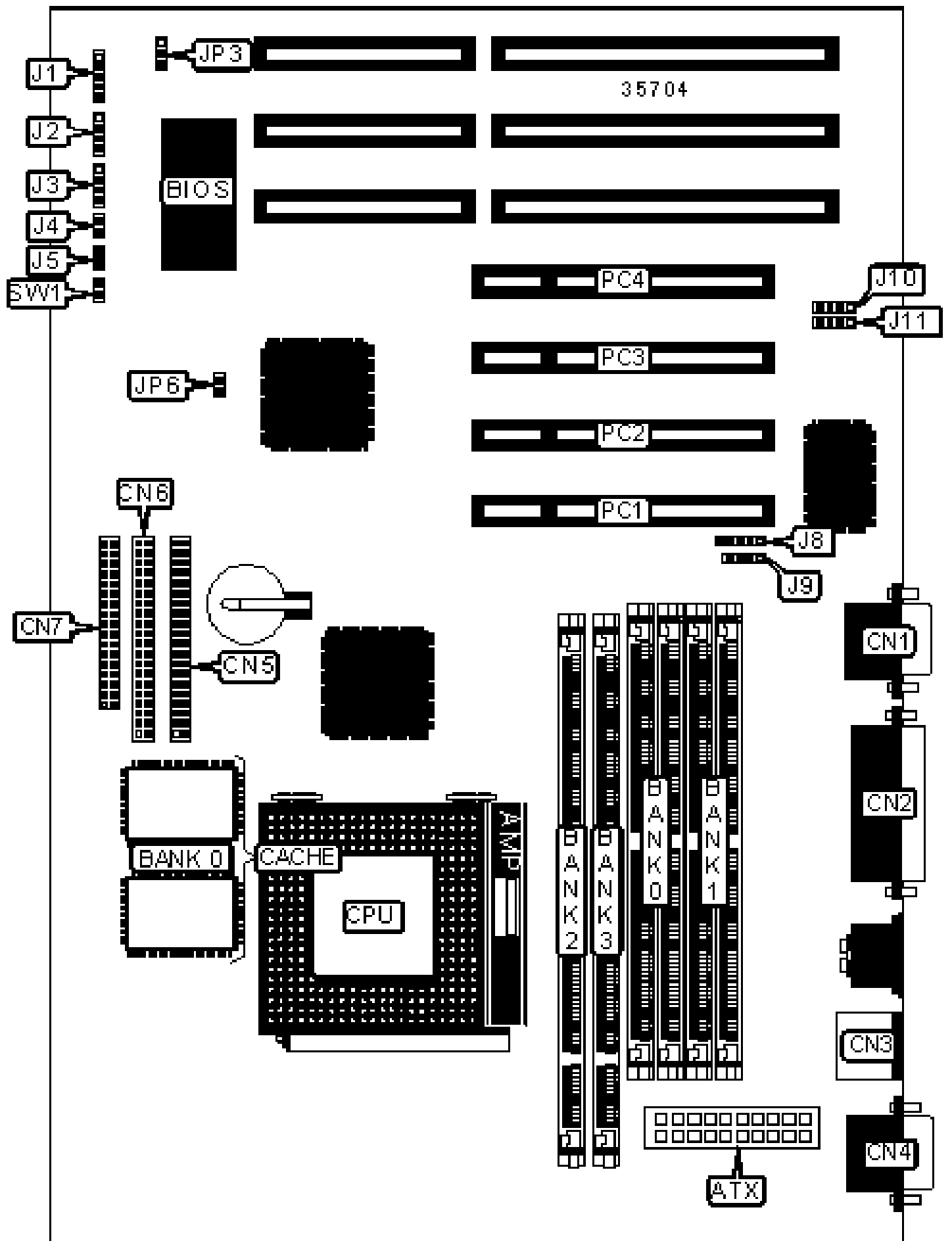


**DATAEXPERT CORPORATION**

**ATX431**

**Configuration**



## CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	IDE interface LED	J3
Serial port 1	CN1	Reset switch	J4
Parallel port	CN2	Turbo LED	J5
PS/2 mouse port	CN3	IR connector	J8
Serial port 2	CN4	CIR connector	J9
IDE interface 1	CN5	USB connector 1	J10
IDE interface 2	CN6	USB connector 2	J11
Floppy drive interface	CN7	32-bit PCI slots	PC1 – PC4
Power LED & keylock	J1	Soft off power supply	SW1
Speaker	J2		

## USER CONFIGURABLE SETTINGS

Function	Label	Position
» Flash BIOS voltage select 5v	JP3	Pins 2 & 3 closed
Flash BIOS voltage select 12v	JP3	Pins 1 & 2 closed
» CMOS memory normal operation	JP6	Closed
CMOS memory clear	JP6	Open

## SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36

32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

### SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory.

### DIMM CONFIGURATION

Size	Bank 2	Bank 3
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64

24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
Note: Board accepts SDRAM memory.		

<b>CACHE CONFIGURATION</b>	
Size	Bank 0
512KB	(2) 64K x 32