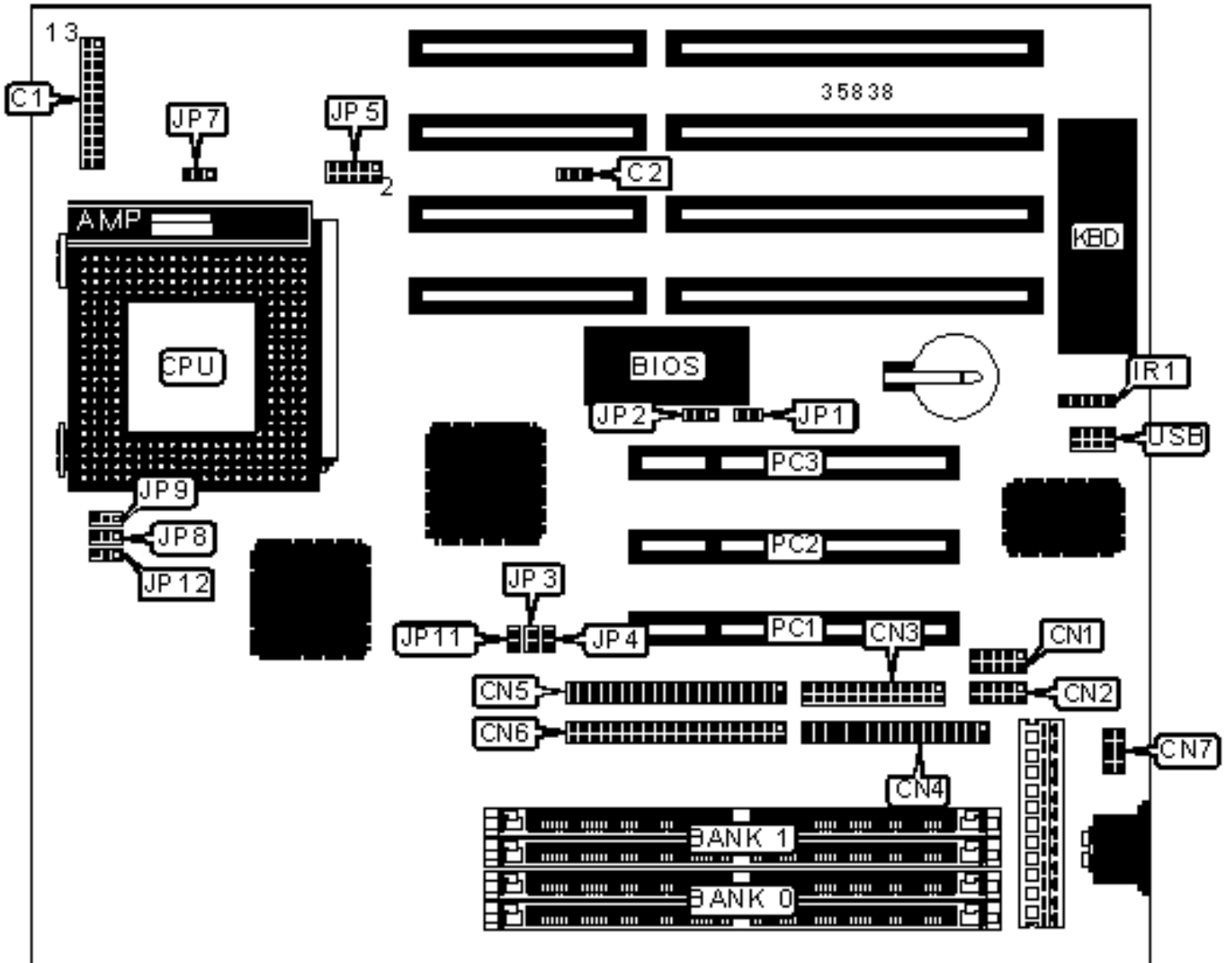


A-TREND TECHNOLOGY CORPORATION

ATC-1030

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Reset switch	C1/pins 1 & 2	Serial port 2	CN2
Green PC connector	C1/pins 4 & 5	Parallel port	CN3
Turbo LED	C1/pins 7 & 8	Floppy drive interface	CN4
IDE interface LED	C1/pins 10 & 11	IDE interface 2	CN5
Soft off power supply	C1/pins 12 & 24	IDE interface 1	CN6
Speaker	C1/pins 13 – 16	PS/2 mouse interface	CN7
Power LED & keylock	C1/pins 18 & 20	IR connector	IR1
Chassis fan power	C2	32-bit PCI slots	PC1 – PC3
Serial port 1	CN1	USB connector	USB

USER CONFIGURABLE SETTINGS

Function	Label	Position
» CMOS memory normal operation	JP1	Open
CMOS memory clear	JP1	Closed
» Boot block disabled	JP2	Pins 2 & 3 closed
Boot block enabled	JP2	Pins 1 & 2 closed
» Factory configured - do not alter	JP12	Unidentified

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36

32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(2) 32K x 32	(1) 16K/32K x 8
512KB	(2) 64K x 32	(1) 16K/32K x 8
Note: The location of the cache & TAG is unidentified.		

CPU SPEED SELECTION (CX 6X86L)							
CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9	JP11
120MHz	50MHz	2x	Open	Open	1 & 2	2 & 3	Closed
133MHz	55MHz	2x	Closed	Closed	1 & 2	2 & 3	Closed
150MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3	Closed
166MHz	66MHz	2x	Open	Closed	1 & 2	2 & 3	Closed
200MHz	75MHz	2x	Closed	Closed	1 & 2	2 & 3	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (CX 6X86MX)

CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9	JP11
150MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3	Closed
166MHz	60MHz	2.5x	Closed	Open	2 & 3	2 & 3	Closed
166MHz	66MHz	2x	Open	Closed	1 & 2	2 & 3	Closed
200MHz	66MHz	2.5x	Open	Closed	2 & 3	2 & 3	Closed
200MHz	75MHz	2x	Closed	Closed	1 & 2	2 & 3	Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5)

CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9	JP11
75MHz	50MHz	1.5x	Open	Open	1 & 2	1 & 2	Closed
90MHz	60MHz	1.5x	Closed	Open	1 & 2	1 & 2	Closed
100MHz	66MHz	1.5x	Open	Closed	1 & 2	1 & 2	Closed
120MHz	60MHz	1.5x	Closed	Open	1 & 2	1 & 2	Closed
133MHz	66MHz	1.5x	Open	Closed	1 & 2	1 & 2	Closed
166MHz	66MHz	2.5x	Open	Closed	2 & 3	2 & 3	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K6)

CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9	JP11
166MHz	66MHz	2.5x	Open	Closed	2 & 3	2 & 3	Closed
200MHz	66MHz	3x	Open	Closed	2 & 3	1 & 2	Closed
233MHz	66MHz	3.5x	Open	Closed	1 & 2	1 & 2	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9	JP11
75MHz	50MHz	1.5x	Open	Open	1 & 2	1 & 2	Closed
90MHz	60MHz	1.5x	Closed	Open	1 & 2	1 & 2	Closed
100MHz	66MHz	1.5x	Open	Closed	1 & 2	1 & 2	Closed
120MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3	Closed
133MHz	66MHz	2x	Open	Closed	1 & 2	2 & 3	Closed
150MHz	60MHz	2.5x	Closed	Open	2 & 3	2 & 3	Closed
166MHz	66MHz	2.5x	Open	Closed	2 & 3	2 & 3	Closed
180MHz	60MHz	3x	Closed	Open	2 & 3	1 & 2	Closed
200MHz	66MHz	3x	Open	Closed	2 & 3	1 & 2	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL MMX)

CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9	JP11
166MHz	66MHz	2.5x	Open	Closed	2 & 3	2 & 3	Closed
200MHz	66MHz	3x	Open	Closed	2 & 3	1 & 2	Closed
233MHz	66MHz	3.5x	Open	Closed	1 & 2	1 & 2	Closed

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION

Type	JP5
AM K5	Pins 1 & 2 closed
AM K6	Pins 7 & 8 closed
AM K6-233	Pins 5 & 6 closed

Cyrix 6x86L	Pins 9 & 10 closed
Cyrix 6x86MX	Pins 7 & 8 closed
Intel MMX	Pins 9 & 10 closed
Intel P54C STD	Pins 3 & 4 closed
Intel P54C VRE	Pins 1 & 2 closed

CPU VOLTAGE SELECTION

Voltage	JP7
Single	Pins 1 & 2 closed
Dual	Pins 2 & 3 closed