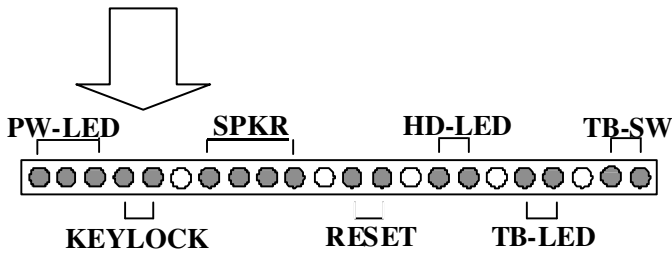
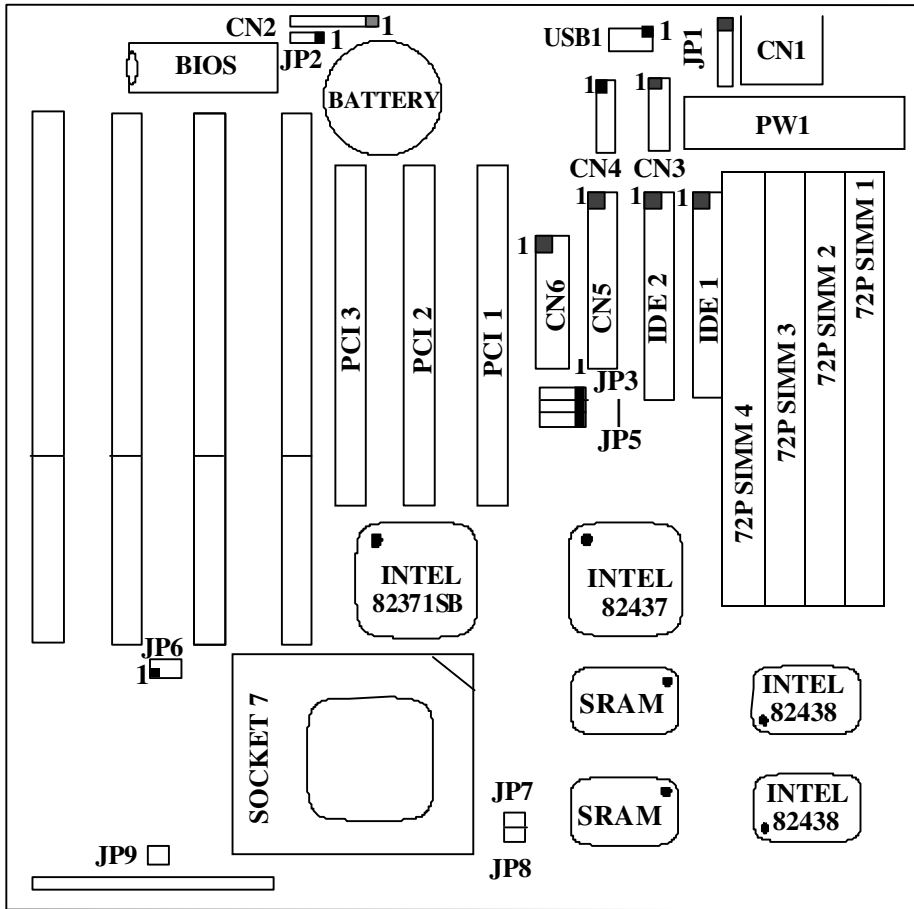


INTEL I430 VX CHIPSET
USER'S MANUAL
(VER: VX2B)

I. LAYOUT REFERENCE



II. JUMPER SETTINGS

1. INTEL/CYRIX/AMD CPU TYPE SELECTOR

INTEL® CPU	CLOCK SPEED	RATIO	JP3	JP4	JP5	JP6	JP7	JP8	JP9
P54C 75 3.3V	(50MHZ)	1.5	1-2	1-2	1-2	3-4	OFF	OFF	OFF
P54C 90 3.3V	(60MHZ)	1.5	2-3	1-2	1-2	3-4	OFF	OFF	OFF
P54C 100 3.3V	(66MHZ)	1.5	1-2	2-3	1-2	3-4	OFF	OFF	OFF
P54C 120 3.3V	(60MHZ)	2	2-3	1-2	1-2	3-4	ON	OFF	OFF
P54C 133 3.3V	(66MHZ)	2	1-2	2-3	1-2	3-4	ON	OFF	OFF
P54C 150 3.3V	(60MHZ)	2.5	2-3	1-2	1-2	3-4	ON	ON	OFF
P54C 166 3.3V	(66MHZ)	2.5	1-2	2-3	1-2	3-4	ON	ON	OFF
P54C 180 3.3V	(60MHZ)	3	2-3	1-2	1-2	3-4	OFF	ON	OFF
P54C 200 3.3V	(66MHZ)	3	1-2	2-3	1-2	3-4	OFF	ON	OFF
P55C 166 2.8/3.3V MMX	(66MHZ)	2.5	1-2	2-3	1-2	5-6	ON	ON	OFF
P55C 200 2.8/3.3V MMX	(66MHZ)	3	1-2	2-3	1-2	5-6	OFF	ON	OFF
P55C 233 2.8/3.3V MMX	(66MHZ)	3.5	1-2	2-3	1-2	5-6	OFF	OFF	OFF

AMD® CPU FOR (PRxxxAB)	CLOCK SPEED	RATIO	JP3	JP4	JP5	JP6	JP7	JP8	JP9
K5 PR75 3.52V	(50MHZ)	1.5	1-2	1-2	1-2	1-2	OFF	OFF	ON
K5 PR90/PR120 3.52V	(60MHZ)	1.5	2-3	1-2	1-2	1-2	OFF	OFF	ON
K5 PR100/PR133 3.52V	(66MHZ)	1.5	1-2	2-3	1-2	1-2	OFF	OFF	ON
K5 PR166 3.52V	(66MHZ)	2.5	1-2	2-3	1-2	1-2	ON	ON	ON
K6 PR166 2.9/3.3V	(66MHZ)	2.5	1-2	2-3	1-2	5-6	ON	ON	OFF
K6 PR200 2.9/3.3V	(66MHZ)	3	1-2	2-3	1-2	5-6	OFF	ON	OFF
K6 PR233 3.3/3.3V*	(66MHZ)	3.5	1-2	2-3	1-2	3-4	OFF	OFF	OFF

CYRIX® CPU	CLOCK SPEED	RATIO	JP3	JP4	JP5	JP6	JP7	JP8	JP9
P120+ 3.52V 6X86	(50MHZ)	2	1-2	1-2	1-2	1-2	ON	OFF	ON
P133+ 3.52V 6X86	(55MHZ)	2	1-2	1-2	2-3	1-2	ON	OFF	ON
P150+ 3.52V 6X86	(60MHZ)	2	2-3	1-2	1-2	1-2	ON	OFF	ON
P166+ 3.52V 6X86	(66MHZ)	2	1-2	2-3	1-2	1-2	ON	OFF	ON
P200+ 3.52V 6X86	(75MHZ)	2	2-3	1-2	2-3	1-2	ON	OFF	ON
P133+ 2.8/3.3V 6X86L	(55MHZ)	2	1-2	1-2	2-3	5-6	ON	OFF	OFF
P150+ 2.8/3.3V 6X86L	(60MHZ)	2	2-3	1-2	1-2	5-6	ON	OFF	OFF
P166+ 2.8/3.3V 6X86L	(66MHZ)	2	1-2	2-3	1-2	5-6	ON	OFF	OFF
P200+ 2.8/3.3V 6X86L	(75MHZ)	2	2-3	1-2	2-3	5-6	ON	OFF	OFF

NOTE : "*" THIS CPU HAD NOT BEEN TESTED WHEN THIS WAS PRINTED
 NOTE : INTEL P55C MMX, AMD K6 MMX AND CYRIX 6X86L ARE "DUAL VOLTAGE CPUs"

EXPLANATION OF JUMPER SETTING

JP3-JP5 CPU & PCI BUS FREQUENCY SELECTOR

	50 MHz	55 MHz	60 MHz	66 MHz	75MHz	83 MHz
JP3	1-2	1-2	2-3	1-2	2-3	1-2
JP4	1-2	1-2	1-2	2-3	1-2	2-3
JP5	1-2	2-3	1-2	1-2	2-3	2-3

JP6 : CPU V CORE VOLTAGE SELECTOR

	3.52V	3.3V	2.8V
JP6	1-2	3-4	5-6

JP7, JP8 : CPU CLOCK SELECTOR

	1.5 X	2.0 X	2.5 X	3.0 X	3.5 X
JP7	OFF	ON	ON	OFF	OFF
JP8	OFF	OFF	ON	ON	OFF

JP9 : SECOND REGULATOR VOLTAGE SELECTOR (OPTIONAL)

	3.52V	3.3V
JP9	ON	OFF

NOTE : INTEL P54C/P55C, AMD K6 AND CYRIX 6X86L SET "OFF"
AMD K5, CYRIX 6X86 SET "ON"

2. JP2 : BATTERY SELECTOR (BLACK JUMPER CAP)

	NORMAL	CLEAR CMOS
JP2	1-2 (DEFAULT)	2-3

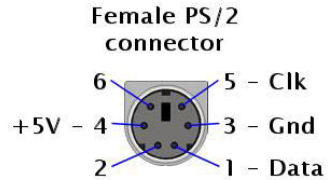
☞ **CUSTOMER NEEDS TO CLEAR CMOS, THEN RECONFIGURE IT IF FORGETS PASSWORD FOR BIOS SETUP.**

3. OTHER JUMPER SETTINGS AND CONNECTORS :

- IDE1** : PRIMARY IDE CONNECTOR.
- IDE2** : SECONDARY IDE CONNECTOR
- CN1** : KEYBOARD
- CN3** : SERIAL PORT 2 CONNECTOR.
- CN4** : SERIAL PORT 1 CONNECTOR.
- CN5** : FLOPPY DISK CONNECTOR .
- CN6** : PARALLEL PORT CONNECTOR.
- PW1** : AT POWER CONNECTOR

4. JP1 : PS/2 MOUSE CONNECTOR.

PIN # 1	MOUSE CLOCK
PIN # 2	MOUSE DATA
PIN # 3	NONE
PIN # 4	GROUND
PIN # 5	+5 V



5. CN2 : IRCON (INFRARED) CONNECTOR

IR CONNECTOR PIN OUT						
PIN 1	PIN 2	PIN 3	PIN 4	PIN 5	PIN 6	PIN 7
RX	GND	TX	+5V	RXH	VCC	GND

NOTE:IRCON USES SAME I/O PORT AS COM2. THERE IS NO ANY HARDWARE JUMPER SETTING FOR IRCON/COM2 ON THIS MAIN BOARD BUT CUSTOMER NEEDS TO SET PROPER BIOS SETTING FOR "HPSIR", "ASKIR" OR "DISABLED"(DEFAULT)

UNDER "INFRA RED (IR) FUNCTION" OF "INTEGRATED PERIPHERAL" ACCORDING TO THE FOLLOWING TABLE:

	IRDA 1.0	ASKIR	FIR	DISABLED
(UNDER INTEGRATED PERIPHERALS) "INFRA RED (IR) FUNCTION"	USE IRDA 1.0 ON IRCON.	USE AMPLITUDE SHIFT KEYED IR ON IRCON.	USE FOR FAST IR 4MB/S	USE COM2 (DEFAULT)

6. USB1 : USB (UNIVERSAL SERIAL BUS) CONNECTOR

USB PIN OUT	
USB1	USB2
PIN1 +5V	PIN2 +5V
PIN3 USBP0-	PIN4 USBP1-
PIN5 USBP0+	PIN6 USBP1+
PIN7 GND	PIN8 GND

NOTE : USB AND IRCON CONNECTORS ARE OPTIONAL.