# MP - 586IVX

# **USER'S MANUAL**

**PCI - ISA SOLUTION** 

### PENTIUMä PCI - ISA BUS MAINBOARD

**REV.2.0 Second Edition** 

#### MP - 586IVX

### Who need 82430 VX?

1. If you want to get the best performance.

(Because more buffers & Quick DRAM Timing is supported.)

2. If you need a USB ports for C&C application.

(Because 2 USB ports will be ready in the end of Q2/96".)

### Why do you need MP - 586IVX 82430VX motherboard?

- 1. Because it is the best performance motherboard. (See next page) We have compared MP 586IVX with PC Magazine EDITOR choice-system.
- Because MP 586IVX has 2 banks Sync. DIMM Module, you may upgrade your memory easily with flexibility.

Others don't, because they only have 1 bank Sync. DIMM Module.

3. Just plug a 256KB Pipeline cache module, and you can get cache size upto 512KB easily. No jumper setting needed.

Others don't, because they don't have an upgrade cache module. Or they have to change jumper setting to 512KB.

4. No risk for Intel P55CT CPU upgrade because MP - 586IVX will auto detect P55CT and support right operation voltage.

Others don't, because they need an EXTRA VRM module or jumper setting for P55CT CPU upgrade.

5. DIP switch setting is supported. You don't have to worry any jumper shunter missed.

Others don't, because they have jumpers on board.

## I. PERFORMANCE

### Gateway P5-166 XL (P/166) / SAG STF 166(P/166):

These two systems' score are according to PC Magazine February 20, 1996. Page 146-148.

### MP - 586IVX (P166):

The system tested with 1.2GB hard disk drive, 4 x CD-ROM, and 16MB of EDO RAM. Using the graphics driver provides from Windows95 in 1024 by 768 resolution with 256 colors, small font, and a refresh rate 72Hz. Also we defragmented hard disk before each test for Windows95 manage virtual memory setting.

Hardware Configuration:

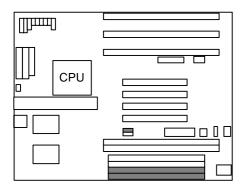
- CPU Intel 166 MHz
- RAM 8MB 2pcs. Total 16 MB (Panasonic EUXSR08XX00E)
- Cache size 256 KB Pipeline Burst SRAM (UMC UM61L3232AF-7) 2 pcs. 512 KB Pipeline Burst SRAM (UMC UM61L3232AF-7) 4 pcs.
- DISPLAY Matrox MGA Millennium Power Desk
- STORAGE Quantum Fireball 1280AT
- O.S. Windows 95

|                             | O.S.      | Cache size | VGA                                 | WINSTONE96 |
|-----------------------------|-----------|------------|-------------------------------------|------------|
| Gateway<br>P5-166 XL(P/166) | Windows95 | 256KB      | Matrox MGA Impression               | 75         |
| SAG<br>STF 166(P/166)       | Windows95 | 512KB      | Image 128#9                         | 76         |
| GA-586VX(P/166)             | Windows95 | 256KB      | Matrox MGA Millennium<br>Power Desk | 79.6       |
| GA-586VX(P/166)             | Windows95 | 512KB      | Matrox MGA Millennium<br>Power Desk | 80.6       |

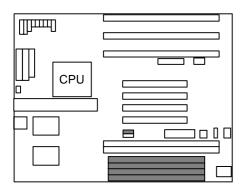
#### II. Quick DRAM/SDRAM Installation Guide:

MP - 586IVX

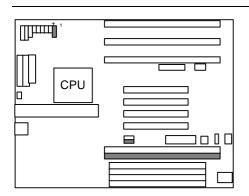
A. EDO DRAM or FAST PAGE DRAM one bank install only.



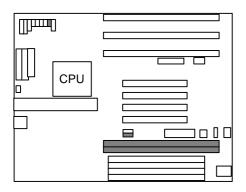
B. EDO DRAM or FAST PAGE DRAM two banks install .



C. SYNC- DRAM one bank install only.



## D. SYNC- DRAM two banks install.



III. Quick Installation Guide:

### MP - 586IVX

| Intel CPU                      | SW1 | SW2 | SW3 | SW4 |
|--------------------------------|-----|-----|-----|-----|
| 1. Pentium 75 MHz              | OFF | OFF | ON  | ON  |
| 2. Pentium 90 MHz              | OFF | OFF | OFF | ON  |
| 3. Pentium 100 MHz             | OFF | OFF | OFF | OFF |
| 4. Pentium 120 MHz             | ON  | OFF | OFF | ON  |
| 5. Pentium 133 MHz             | ON  | OFF | OFF | OFF |
| 6. Pentium 150 MHz/P55C-150MHz | ON  | ON  | OFF | ON  |
| 7. Pentium 166 MHz/P55C-166MHz | ON  | ON  | OFF | OFF |
| 8. Pentium 180 MHz             | OFF | ON  | OFF | ON  |
| 9. Pentium 200 MHz/P55C-200MHz | OFF | ON  | OFF | OFF |
| 10. P54CT-125 MHz              | ON  | ON  | ON  | ON  |
| 11. P54CT-150 MHz              | ON  | ON  | OFF | ON  |
| 12. P54CT-166 MHz              | ON  | ON  | OFF | OFF |
| 13. P54CTB-150 MHz **          | ON  | ON  | OFF | ON  |
| 14. P54CTB-166 MHz **          | ON  | ON  | OFF | OFF |
| 15. P54CTB-180 MHz **          | OFF | ON  | OFF | ON  |
| 16. P54CTB-200 MHz **          | OFF | ON  | OFF | OFF |

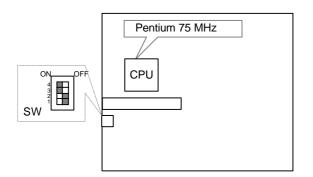
|                              | 1   | 1   |     | 1   |
|------------------------------|-----|-----|-----|-----|
| AMD/Cyrix CPU                | SW1 | SW2 | SW3 | SW4 |
| 17. AMDK5- 75 MHz-P75        | OFF | OFF | ON  | ON  |
| 18. AMDK5- 90 MHz-P90        | OFF | OFF | OFF | ON  |
| 19. AMDK5-100 MHz-P100       | OFF | OFF | OFF | OFF |
| 20. AMDK5- 90 MHz-P120       | OFF | OFF | OFF | ON  |
| 21. AMDK5-100 MHz-P133       | ON  | OFF | OFF | OFF |
| 22. AMDK5- P166              | ON  | ON  | OFF | OFF |
| 23. AMDK5- P200              | OFF | ON  | OFF | OFF |
| 24. Cyrix 6x86-100 MHz-P120+ | ON  | OFF | ON  | ON  |
| 25. Cyrix 6x86-110 MHz-P133+ | ON  | OFF | ON  | OFF |
| 26. Cyrix 6x86-120 MHz-P150+ | ON  | OFF | OFF | ON  |
| 27. Cyrix 6x86-133 MHz-P166+ | ON  | OFF | OFF | OFF |
| 28. Cyrix 6x86L-P150+ 2.8V   | ON  | OFF | OFF | ON  |
| 29. Cyrix 6x86L-P166+ 2.8V   | ON  | OFF | OFF | OFF |

\* Note : If Cyrix 6x86 is being used, please check the CPU Date Code after 605.

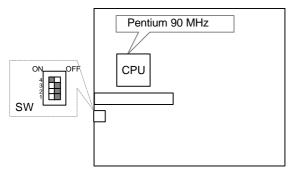
\* \* . Note : To Support Intel CPU P54CTB-150MHz/166MHz/180MHz/200MHz .

GA-586VX Is Available From PCB Ver.3.34.

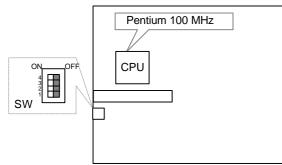
1. Pentium 75 MHz



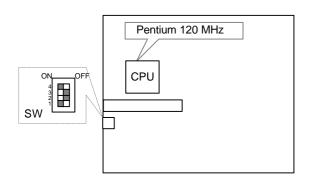
2. Pentium 90 MHz



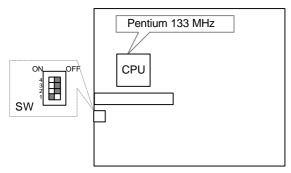
3. Pentium 100 MHz



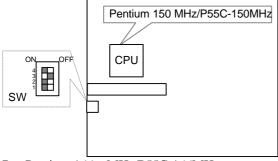
4. Pentium 120 MHz



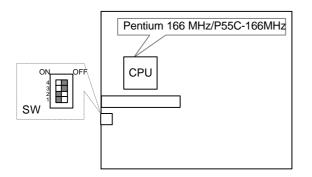
5. Pentium 133 MHz



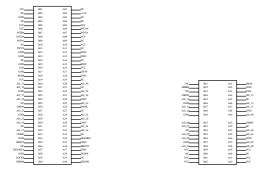
6. Pentium 150 MHz/P55C-150MHz



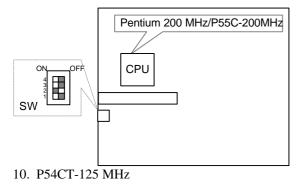
7. Pentium 166 MHz/P55C-166MHz

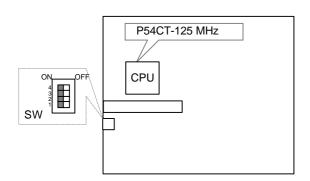


8. Pentium 180 MHz

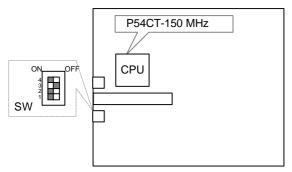


### 9. Pentium 200 MHz/P55C-200MHz

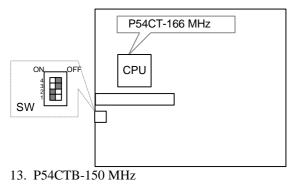


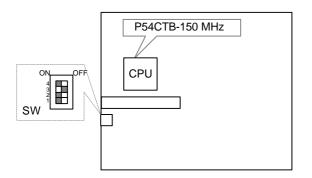


## 11. P54CT-150 MHz

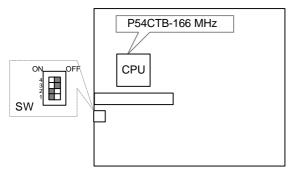


## 12. P54CT-166 MHz

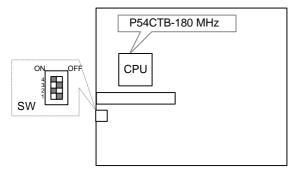




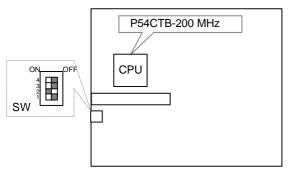
14. P54CTB-166 MHz



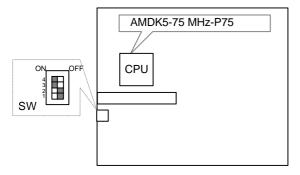
## 15. P54CTB-180 MHz



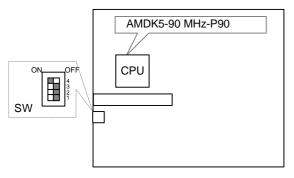
16. P54CTB-200 MHz



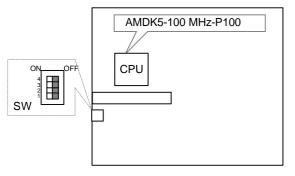
17. AMDK5- 75 MHz-P75



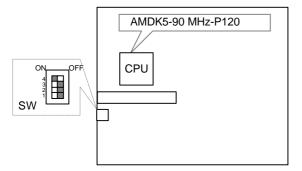
18. AMDK5- 90 MHz-P90



## 19. AMDK5-100 MHz-P100

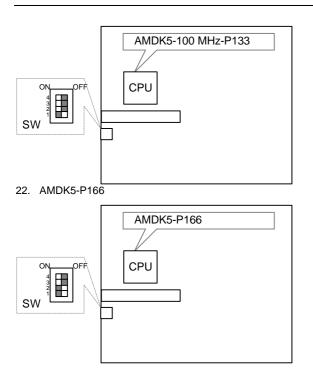


20. AMDK5- 90 MHz-P120

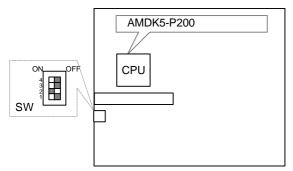


21. AMDK5-100 MHz-P133

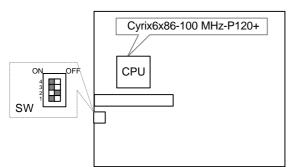




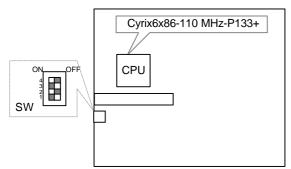
### 23. AMDK5-P200



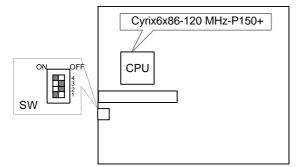
24. Cyrix 6x86-100 MHz-P120+



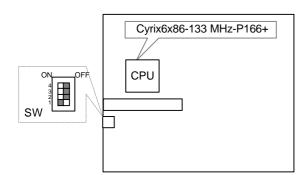
25. Cyrix 6x86-110 MHz-P133+



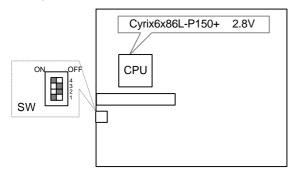
26. Cyrix 6x86-120 MHz-P150+



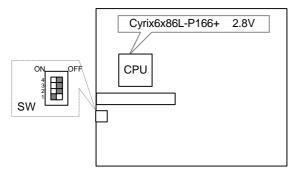
27. Cyrix 6x86-133 MHz-P166+



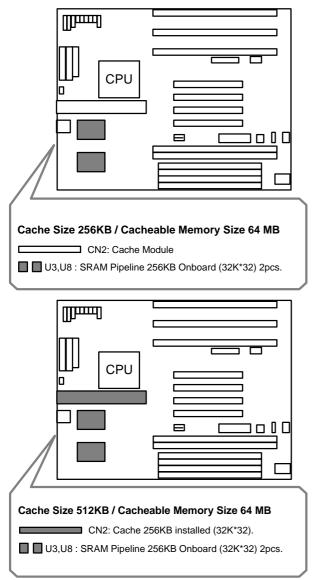
28. Cyrix 6x86L-P150+ 2.8V



29. Cyrix 6x86L-P166+ 2.8V

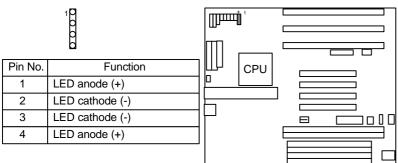


### IV. SRAM Install



### V. Quick Installation Guide of Jumper setting:

### HD: IDE Hard Disk Active LED

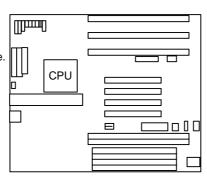


### **GN: Green Function Switch**

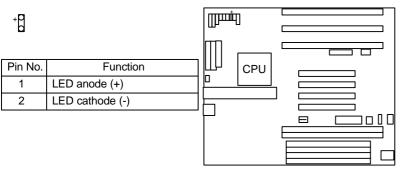


Close: For system entering Green mode.

Open: Normal operation.



#### GD: Green Function LED



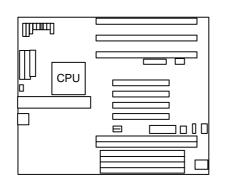
### **RST: Reset Switch**

# 9

Close: For hardware reset system.

# 8

Open: Normal operation.



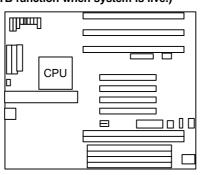
TB: Turbo Switch (Don't switch TB function when system is live.)

# ŧ

Close: For low speed (50MHz).

# 6

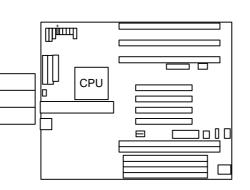
Open: For high speed (Normal).

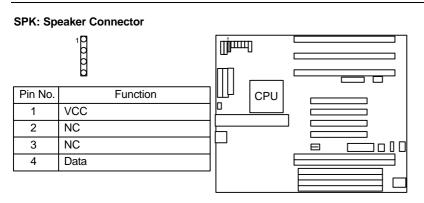


### **TD: Turbo LED Connector**

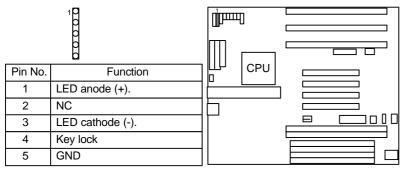
+0

| Pin No. | Function        |
|---------|-----------------|
| 1       | LED anode (+)   |
| 2       | LED cathode (-) |
|         |                 |





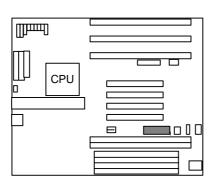
### PWR: Power LED and Key-Lock Connector



# POWER: Power Connector

0000000000000

| Pin No.    | Function           |
|------------|--------------------|
| 1          | Power Good signal. |
| 2,10,11,12 | VCC (+5V)          |
| 3          | +12V               |
| 4          | -12V               |
| 5,6,7,8    | GND                |
| 9          | -5V                |

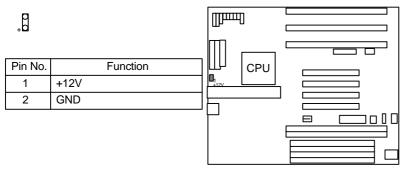


# MP - 586IVX

### J1: Keyboard Connector

| 0<br>0<br>0<br>0<br>0 |            |   |     |  |
|-----------------------|------------|---|-----|--|
| Pin No.               | Function   |   | CPU |  |
| 1                     | Key Clock. |   |     |  |
| 2                     | Key Data   | Б |     |  |
| 3                     | NC         | Γ |     |  |
| 4                     | VCC (+5V)  |   |     |  |
| 5                     | GND        |   |     |  |

# J9: CPU Cooling Fan Power Connector

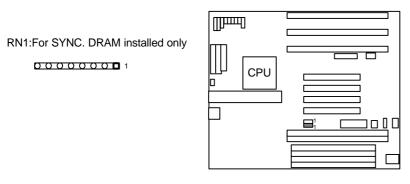


RN1,4:

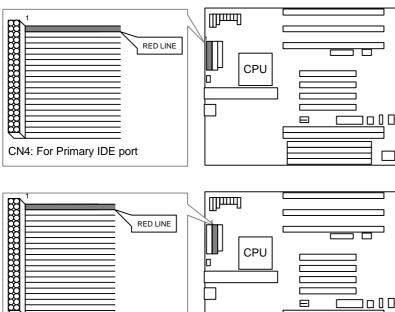
RN4:For EDO, FPM installed only

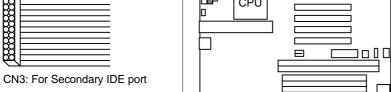
0000000**0**1

Г

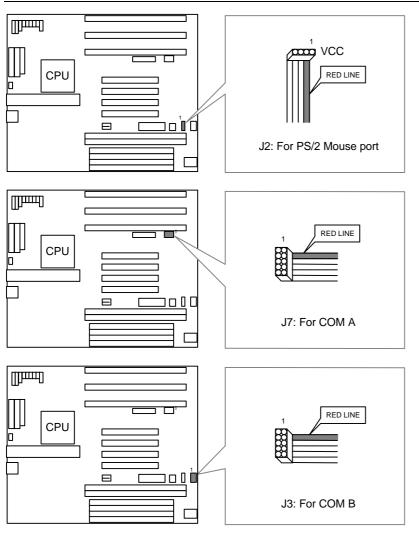


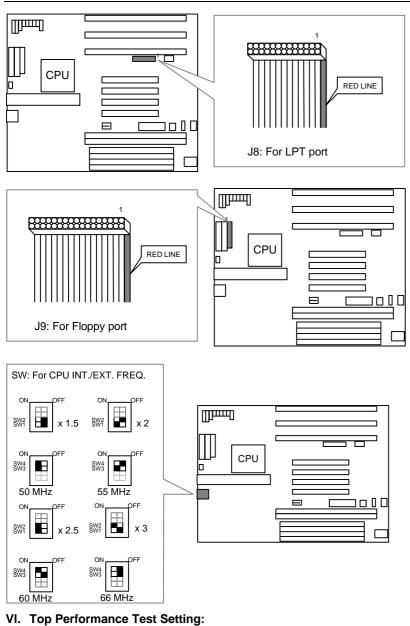
CN3,4,J2,3,7,8,9: I/O Ports Connector





MP - 586IVX





### MP - 586IVX

Users have to modify the value for each item in chipset features as follow: Note:60ns EDO-60ns DRAM is necessary for top performance setting. Chipset features setup

| CHIPSET FEATURES SETUP<br>AWARD SOFTWARE, INC.                                                                                                                                                                                    |                                                                                            |                                                                                                                                                                                                                                                                                                                                                                |  |  |  |  |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|--|--|--|
| Auto Configuration                                                                                                                                                                                                                | : Disabled                                                                                 | Passive Release : Enabled                                                                                                                                                                                                                                                                                                                                      |  |  |  |  |
| DRAM RAS# Precharge Time<br>DRAM R/W Leadoff Timing<br>Fast RAS To CAS Delay<br>DRAM Read Burst (EDO/FPM)<br>DRAM Write Burst Timing<br>Fast MA to RAS# Delay CLK<br>Refresh RAS# Assertion<br>Turn-Around Insertion<br>ISA Clock | : 3<br>: 6<br>: 2<br>: x222/x333<br>: x222<br>: 1<br>: 4 CLKS<br>: Disabled<br>: PCI CLK/4 | Delayed Transaction : Disabled                                                                                                                                                                                                                                                                                                                                 |  |  |  |  |
| Pipeline Cache Timing<br>System BIOS Cacheable<br>Video BIOS Cacheable<br>8 Bit I/O Recovery Time<br>16 Bit I/O Recovery Time<br>Memory Hole At 15M-16M<br>Peer Concurrency                                                       | : Fastest<br>: Enabled<br>: Enabled<br>: 1<br>: 1<br>: Disabled<br>: Enabled               | $\begin{array}{c c} \text{ESC} : \text{Quit} & & & & & & \\ \hline \text{ESC} : \text{Quit} & & & & & \\ \text{F1} & : \text{Help} & & \text{PU/PD/+/-} & : \text{Modify} \\ \text{F5} & : \text{Old Values} & (\text{Shift})\text{F2} & : \text{Color} \\ \text{F6} & : \text{Load BIOS Defaults} \\ \text{F7} & : \text{Load Setup Defaults} \\ \end{array}$ |  |  |  |  |

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OCTOBER 22, 1996 Taipei, Taiwan

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# 1. INTRODUCTION

### 1.1. PREFACE

Welcome to use the **MP-586IVX** motherboard. The motherboard is a Pipeline 256 KB / 512 KB CACHE PENTIUM<sup>TM</sup> Processor based PC / AT compatible system with ISA bus and PCI Local Bus, and has been designed to be the fastest PC / AT system. There are some new features allowing you to operate the system with just the performance you want.

This manual also explains how to install the motherboard for operation, and how to set up your CMOS CONFIGURATION with BIOS SETUP program.

### **1.2. KEY FEATURES**

- □ Pentium<sup>®</sup> based PC / AT compatible mainboard with PCI ISA Bus.
- □ 4 PCI Bus slots, 3 ISA Bus slots.
- Supports Pentium processor running at 75-200 MHz, P54CT (125 / 150 / 166), P55C (150 / 166 / 200), P54CTB (150 / 166 / 180 / 200), AMDK5 (P-75 / P-90 / P-100 / P-120 / P-133 / P-166 / P-200 ), Cyrix 6x86-100 / 110 / 120 / 133(P-120+ / P-133+ / P-150+ / P-166+), Cyrix 6x86L (P-150+ / P-166+ 2.8V).
- □ Supports true 64 bits CACHE and DRAM access mode.
- □ Supports 321 Pins (Socket 7) ZIF white socket on board.
- □ Supports 256 KB / 512 KB Pipeline Burst Sync. 2nd Cache.
- CPU L1 / L2 Write-Back cache operation.
- □ Supports 8 128 MB DRAM memory on board.
- □ Supports 2\*168 pin 64/72 Bit DIMM module.
- □ Supports 2-channel Enhanced PCI IDE ports for 4 IDE Devices.
- □ Supports 2\*COM (16550), 1\*LPT (EPP / ECP), 1\*1.44MB Floppy port.
- □ Supports PS/2 Mouse port.
- □ Supports Green function, Plug & Play function.
- Licensed AWARD BIOS, FLASH EEPROM for BIOS update.
- BENCHMARQ3287 / DALLAS 12887 / ODIN 12C887 RTC on board.
- □ 22cm\*28cm, 4 layers PCB.
- □ Supports USB port. (optional)

### **1.3. PERFORMANCE LIST**

The following performance data list is the testing results of some popular benchmark testing programs. These data are just referred by users, and there is no responsibility for different testing data values gotten by users. (The different Hardware & Software configuration will result in different benchmark testing results.)

- CPU Pentium™ processor 133/166 MHz
- EDO 8MB 2pcs. Total 16 MB (Panasonic EUXSR08XX00E) • DRAM
- CACHE SIZE 256 KB Pipeline Burst SRAM (UMC UM61L3232AF-7) •
- Matrox Millennium DISPLAY •
- STORAGE •
  - Onboard IDE port + Quantum Fireball 1280AT
- MS DOS V6.22 / Windows for workgroup 3.11 • O.S.

≺DOS≻

| Program             | Item               | Unit       | Pentium 133/66 | Pentium 166/66 |
|---------------------|--------------------|------------|----------------|----------------|
| LandMark Speed      | CPU                | MHz        | 771.42         | 964.28         |
|                     | FPU                | MHz        | 2589.62        | 3237.06        |
| V2.0                | VIDEO              | chr/ms     | 16384.00       | 16115          |
|                     | MIPS               | Mips       | 67.6           | 83.6           |
| Power Meter         | Dhrystone          | K-Dstone/s | 119.0          | 147.0          |
|                     | Whetstone          | K-Wstone/s | 25623.4        | 32235.9        |
| V1.81               | Data Transfer Rate | KB/S       | 14073.9        | 14275.0        |
|                     | Mean Seek          | ms         | 10.3           | 10.3           |
|                     | Track-Track Seek   | ms         | 2.6            | 2.6            |
| Norton System Info. | CPU                | Index      | 423.5          | 529.4          |
| V8.0                | Disk               | Index      | 22             | 21.9           |
| Core Test           | DATA Transfer Rate | KB/S       | 12416          | 12448          |
|                     | Sequential Read    | ms         | 6304           | 6288           |
| V.3.02              | Random Read        | ms         | 2384           | 2400           |
|                     | Performance Index  | Index      | 79.23          | 79.38          |
| PC BenchMark        | DOS Mark           | Index      | 1121.33        | 1220.69        |
|                     | CPU Mark16         | Index      | 287.34         | 342.76         |
| V9.0                | Video Score        | Index      | 6277.55        | 6266.21        |
|                     | Disk Score         | Index      | 607.13         | 639.53         |

< WINDOWS > – with Display Driver MGA Millennium power desktop1024 x 768 x 256 colors x 72Hz

- with Triones IDE Bus Master Driver Ver.3.20.

| Program     | Item               | Unit      | Pentium 133/66 | Pentium 166/66 |
|-------------|--------------------|-----------|----------------|----------------|
| Winbench 95 | Disk Winmark95     | KB/s      | 1090           | 1100           |
|             | Graphics Winmark95 | Mpixels/S | 22.7           | 26             |
| Winstone 95 | Winstone95         | Index     | 207.4          | 223.3          |
| Winbench 96 | CPU mark16         |           | 281            | 319            |
|             | CPU mark32         |           | 293            | 331            |
|             | Winbench 96        |           | 16.7           | 18.8           |
| Winstone 96 | Winstone 96        |           | 84.5           | 91.7           |

CPU

Pentium™ processor 133/166 MHz

- DRAM EDO 8MB 2pcs. Total 16 MB (Panasonic EUXSR08XX00E)
- CACHE SIZE 512 KB Pipeline Burst SRAM (UMC UM61L3232AF-7)
- DISPLAY Matrox Millennium
- STORAGE Onboard IDE port + Quantum Fireball 1280AT
- O.S. MS DOS V6.22 / Windows for workgroup 3.11

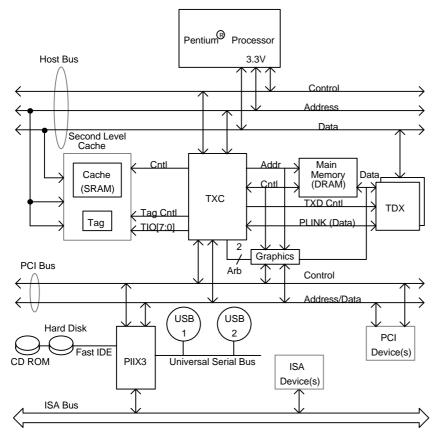
## ≺DOS≻

| Program             | Item               | Unit       | Pentium 133/66 | Pentium 166/66 |
|---------------------|--------------------|------------|----------------|----------------|
| LandMark Speed      | CPU                | MHz        | 771.42         | 964.28         |
|                     | FPU                | MHz        | 2589.65        | 3237.09        |
| V2.0                | VIDEO              | chr/ms     | 16384.00       | 16115          |
|                     | MIPS               | Mips       | 67.7           | 83.6           |
| Power Meter         | Dhrystone          | K-Dstone/s | 119.0          | 147.0          |
|                     | Whetstone          | K-Wstone/s | 25623.4        | 32235.9        |
| V1.81               | Data Transfer Rate | KB/S       | 14073.9        | 14275.0        |
|                     | Mean Seek          | ms         | 10.2           | 10.3           |
|                     | Track-Track Seek   | ms         | 2.6            | 2.6            |
| Norton System Info. | CPU                | Index      | 423.5          | 529.4          |
| V8.0                | Disk               | Index      | 22.0           | 22             |
| Core Test           | DATA Transfer Rate | KB/S       | 12448          | 12448          |
|                     | Sequential Read    | ms         | 6304           | 6304           |
| V.3.02              | Random Read        | ms         | 2384           | 2384           |
|                     | Performance Index  | Index      | 79.43          | 79.43          |
| PC BenchMark        | DOS Mark           | Index      | 1123.68        | 1226.45        |
|                     | CPU Mark16         | Index      | 289.47         | 347.66         |
| V9.0                | Video Score        | Index      | 6261.62        | 6266.78        |
|                     | Disk Score         | Index      | 607            | 641.06         |

#### ✓ WINDOWS>- with Display Driver MGA Millennium power desktop1024 x 768 x 256 colors x 72Hz - with Triones IDE Bus Master Driver Ver.3.20

| Program     | Item               | Unit      | Pentium 133/66 | Pentium 166/66 |
|-------------|--------------------|-----------|----------------|----------------|
| Winbench 95 | Disk Winmark95     | KB/s      | 1150           | 1120           |
|             | Graphics Winmark95 | Mpixels/S | 23.3           | 26.8           |
| Winstone 95 | Winstone95         | Index     | 210.2          | 225.9          |
| Winbench 96 | CPU mark16         |           | 292            | 337            |
|             | CPU mark32         |           | 311            | 351            |
|             | Winbench 96        |           | 16.9           | 18.7           |
| Winstone 96 | Winstone 96        |           | 85.1           | 93             |

### **1.4. BLOCK DIAGRAM**



#### **1.5. INTRODUCE THE PCI - BUS**

Connecting devices to a CPU local bus can dramatically increase the speed of I/O-bound peripherals with only a slight increase in cost over traditional systems. This price / performance point has created a vast market potential for local bus products. The main barrier to this market has been the lack of an accepted standard for local bus peripherals. Many mainboard and chipset manufactures developed their own local bus implementations, but they are incompatible with each other. The VL (Video Electronics Standards Association) local bus and PCI (Peripheral Component Interconnect) bus specification was created to end this confusion.

The PCI - bus standard, under development since Jun. 1992, which is designed to bring workstation-level performance to standard PC platform. The PCI - bus removes many of the bottlenecks that have hampered PC for several years. On the PCI - bus, peripherals operate at the native speed of the computer system, thus enabling data transfer between peripherals and the system at maximum speed. This performance is critical for bandwidth-constrained devices such as video, multimedia, mass storage, and networking adapters.

PCI - bus standard provides end-users with a low-cost, extendible and portable local bus design, which will allow system and peripherals from different manufactures to work together.

#### **1.6. FEATURES**

- □ 32 bits bus transfer mode.
- Bus Master or Slave access.
- Memory burst transfer to 132 MB/sec.
- □ 33 MHz operation speed.
- □ 10 device loading ability.
- CPU independent.

# 2. SPECIFICATION

# 2.1. HARDWARE

| • CPU                             | <ul> <li>Pentium<sup>™</sup> processor 75 - 200 MHz, P55C,</li> <li>P54CT, P54CTB, AMDK5(P-75/P-90/P-100/P-120/P-133/P-166/P-200), Cyrix 6x86(P-120+/P-133+/P-150+/P-166+), Cyrix 6x86L(P-150+/P-166+ 2.8V).</li> </ul> |
|-----------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|                                   | – 321 pins (socket 7) ZIF white socket on board.                                                                                                                                                                        |
|                                   | – 3.52V / 2.8V Dual Power Ready.                                                                                                                                                                                        |
| <ul> <li>COPROCESSOR</li> </ul>   | <ul> <li>Included in Pentium.</li> </ul>                                                                                                                                                                                |
| • SPEED                           | – 50 / 60 / 66 MHz system and 25 / 30 / 33 PCI-Bus speed.                                                                                                                                                               |
|                                   | – 7.5 / 8 MHz AT bus speed.                                                                                                                                                                                             |
|                                   | <ul> <li>Hardware and Software speed switchable<br/>function.</li> </ul>                                                                                                                                                |
| <ul> <li>DRAM MEMORY</li> </ul>   | – 2 banks 72 pins SIMM module socket on board.                                                                                                                                                                          |
|                                   | – 2 banks 168 pins DIMM module socket on board                                                                                                                                                                          |
|                                   | <ul> <li>Use 4 / 8 / 16 / 32 MB 60~70 ns SIMM module<br/>DRAM.</li> </ul>                                                                                                                                               |
|                                   | – 8 ~ 128 MB DRAM size.                                                                                                                                                                                                 |
|                                   | <ul> <li>Supports Fast Page / EDO DRAM access mode.</li> </ul>                                                                                                                                                          |
|                                   | <ul> <li>Supports 2x168 pin 64/72 bit DIMM module.</li> </ul>                                                                                                                                                           |
| CACHE MEMORY                      | <ul> <li>– 16 KB cache memory included in Pentium.</li> </ul>                                                                                                                                                           |
|                                   | <ul> <li>Pipeline Burst Sync. 2nd cache.</li> </ul>                                                                                                                                                                     |
|                                   | <ul> <li>Supports Write Back cache function for both<br/>CPU &amp; on board cache.</li> </ul>                                                                                                                           |
| <ul> <li>SHADOW RAM</li> </ul>    | <ul> <li>Main BIOS shadow function.</li> </ul>                                                                                                                                                                          |
|                                   | <ul> <li>Video BIOS shadow function programmable.</li> </ul>                                                                                                                                                            |
|                                   | <ul> <li>Shadow RAM cacheable function.</li> </ul>                                                                                                                                                                      |
| <ul> <li>I/O BUS SLOTS</li> </ul> | – 4 Master / Slave PCI BUS.                                                                                                                                                                                             |
|                                   | – 3 16-bit ISA BUS.                                                                                                                                                                                                     |
| • IDE PORTS                       | <ul> <li>2-channel Enhanced IDE on board. (Using IRQ14, 15)</li> </ul>                                                                                                                                                  |
|                                   |                                                                                                                                                                                                                         |

| • I/O PORTS                                                                                   | <ul> <li>Supports Mode 3,4 IDE &amp; ATAPI CD - ROM.</li> <li>Supports USB port (optional).</li> <li>Supports 2 16550 COM ports. (Using IRQ4, 3)</li> <li>Supports 1 EPP/ECP LPT port. (Using IRQ7 or 5</li> </ul>                                                                         |
|-----------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| • GREEN FUNCTION                                                                              | <ul> <li>and DMA3 or 1)</li> <li>Supports 1 1.44MB Floppy port. (Using DMA2 &amp; IRQ6)</li> <li>Supports PS/2 Mouse. (Using IRQ12)</li> <li>Supports Standby &amp; Suspend mode.</li> <li>Supports Green switch &amp; LED.</li> <li>Supports IDE &amp; Display power down.</li> </ul>     |
| • BIOS<br>• DIMENSION                                                                         | <ul> <li>Monitors all IRQ / DMA / Display / I/O events.</li> <li>128KB FLASH EEPROM.</li> <li>Supports Plug &amp; Play Function.</li> <li>22cm*28cm, 4 layers.</li> </ul>                                                                                                                  |
| 2.2. SOFTWARE                                                                                 |                                                                                                                                                                                                                                                                                            |
| • BIOS<br>• O.S.                                                                              | <ul> <li>Licensed AWARD BIOS.</li> <li>AT CMOS Setup, BIOS / Chipset Setup, Green Setup, Hard Disk Utility included.</li> <li>Operation with MS-DOS V6.22, Windows for workgroup 3.11, Windows 95, WINDOWS NT 3.51, OS/2 Warp 3.0, NOVELL 3.12 / 4.01 / 4.1 and SCO UNIX 3.2.4.</li> </ul> |
| 2.3. ENVIRONMENT                                                                              |                                                                                                                                                                                                                                                                                            |
| <ul> <li>Ambient Temp.</li> <li>Relative Hum.</li> <li>Altitude</li> <li>Vibration</li> </ul> | <ul> <li>- 0°C to +50°C (Operating).</li> <li>- 0 to +85% (Operating).</li> <li>- 0 to 10,000 feet (Operating).</li> <li>- 0 to 1,000 Hz.</li> </ul>                                                                                                                                       |

- Vibration
- Electricity
- 2-2

- 4.9 V to 5.2 V.

# 3. HARDWARE INSTALLATION

# 3.1. UNPACKING

The mainboard package should contain the following:

- The MP 586IVX mainboard.
- USER'S MANUAL.
- Cable set for I/O Device.
- Diskette for BUS Master IDE Driver.

The mainboard contains sensitive electric components which can be easily damaged by static electricity, so the mainboard should be left in its original packing until it is installed.

Unpacking and installation should be done on a grounded anti-static mat. The operator should be wearing an anti static wristband, grounded at the same point as the anti-static mat.

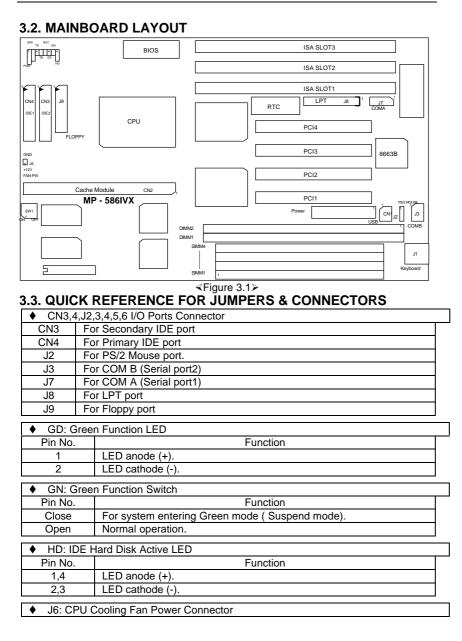
Inspect the mainboard carton for obvious damage. Shipping and handling may cause damage to your board. Be sure there are no shipping and handling damages on the board before proceeding.

After opening the mainboard carton, extract the system board and place it only on a grounded anti-static surface component side up. Again inspect the board for damage. Press down on all of the socket IC's to make sure that they are properly seated. Do this only on with the board placed on a firm flat surface.

# ● DO NOT APPLY POWER TO THE BOARD IF IT HAS BEEN DAMAGED.

You are now ready to install your mainboard. The mounting hole pattern on the mainboard matches the IBM-AT system board. It is assumed that the chassis is designed for a standard IBM XT/AT mainboard mounting.

Place the chassis on the anti-static mat and remove the cover. Take the plastic clips, Nylon stand-off and screws for mounting the system board, and keep them separate.



| Pin No.              |               |                | Function  |                |     |    |
|----------------------|---------------|----------------|-----------|----------------|-----|----|
| 1                    | +12V          |                |           |                |     |    |
| 2                    | GND           |                |           |                |     |    |
| PWR: Pc              | wer LED Conr  | ector          |           |                |     |    |
| Pin No.              |               |                | Function  |                |     |    |
| 1                    | LED anode     | (+).           |           |                |     |    |
| 2                    | NC.           |                |           |                |     |    |
| 3                    | LED cathod    | e (-).         |           |                |     |    |
| POWE1:               | Power Connec  | ctor           |           |                |     |    |
| Pin No.              |               |                | Function  |                |     |    |
| 1                    | Power Good    | l signal       |           |                |     |    |
| 2,10,11,12           | VCC (+5V)     |                |           |                |     |    |
| 3                    | +12V          |                |           |                |     |    |
| 4                    | -12V          |                |           |                |     |    |
| 5,6,7,8              | GND           |                |           |                |     |    |
| 9                    | -5V           |                |           |                |     |    |
| RST: Re              | set Switch    |                |           |                |     |    |
| Open                 | For normal    | operation.     |           |                |     |    |
| Close                |               | e reset system |           |                |     |    |
| CDK Cn               | eaker Connect |                |           |                |     |    |
| Pin No.              |               | JI             | Function  |                |     |    |
| 1                    | VCC.          |                | T unction |                |     | -  |
| 2                    | NC.           |                |           |                |     | -  |
| 3                    | NC.           |                |           |                |     | -  |
| 4                    | Data.         |                |           |                |     |    |
|                      | U INT./EXT. F |                |           |                |     |    |
| SW1                  | SW2           |                | SW3       | SW4            | MHz |    |
| OFF                  | OFF           | X 1.5          | ON        | <br>ON         | 50  | -  |
| ON                   | OFF           | X 1.5          | ON        | OFF            | 55  | 1  |
| OFF                  | ON            | X 2.5          | OFF       | ON             | 60  | -  |
| ON                   | ON            | X 3            | OFF       | OFF            | 66  | -1 |
| TB <sup>.</sup> Turb | o Switch *Dem | t switch TB fu |           | watam ia live  |     |    |
| Pin No.              |               |                | Function  | ystenn is nve. |     | -  |
| Close                | For low spe   | ad (50MHz)     | FUNCTION  |                |     | -  |
| Open                 |               | ed (Normal).   |           |                |     | -  |
|                      |               | , ,            |           |                |     |    |
|                      | o LED Connec  | tor            |           |                |     | -  |
| Pin No.              |               | (.)            | Function  |                |     | _  |
| 1 2                  | LED anode     |                |           |                |     | 4  |
|                      |               |                |           |                |     | 1  |

The mainboard can be installed with 4 / 8 / 16 / 32 MB 72 pins SIMM module

 TOTAL

 SIZE

 8MB

 16MB

 16MB

 24MB

 32MB

 40MB

 48MB

 64MB

 64MB

 72MB

 80MB

 96MB

DRAM, and the DRAM speed must be 60 or 70 ns. The DRAM memory system on mainboard consists of bank 0, & bank 1. Each bank consists of 2 socket for 72 pins SIMM module DRAM. Because the 72 pins SIMM module is 32 bits width, using 2 PCs which can match a 64 bits system. The total memory size is 8 - 128 MB.

For the DRAM installation position, please refer to Figure 3.1. Note that the Pin 1 of SIMM module must match with the Pin 1 of SIMM socket when the DRAM SIMM module is installed.

Insert the DRAM SIMM module into the SIMM socket at 45 degree angle. If there is a wrong direction of Pin 1, the DRAM SIMM module couldn't be inserted into socket completely. After completely insert SIMM module into socket, then press the SIMM module in vertical direction until the left and right metal holders can keep the SIMM module standing up firmly.

### DRAM Installation (SYNC. DRAM)

The mainboard can be installed with 8 / 16 / 32 / 64 MB Sync. DRAM. The Sync. DRAM memory system on mainboard consists of DIMM1 & DIMM2.

For the Sync. DRAM installation position, please refers to Figure 3.1. Note that the Pin 1 of DIMM module must match with the Pin 1 of DIMM socket when the Sync. DRAM DIMM module is installed.

| BANK0          | BANK1          | TOTAL<br>SIZE | DIMM1 | DIMM2 |
|----------------|----------------|---------------|-------|-------|
| 4MB *<br>2pcs. |                | 8MB           | 8MB   |       |
| 4MB *<br>2pcs. | 4MB *<br>2pcs. | 16MB          | 8MB   | 8MB   |
| 8MB *<br>2pcs. |                | 16MB          | 16MB  |       |
| 8MB *<br>2pcs. | 4MB *<br>2pcs. | 24MB          | 16MB  | 8MB   |
| 8MB *<br>2pcs. | 8MB *<br>2pcs. | 32MB          | 16MB  | 16MB  |
| 16MB * 2pcs.   |                | 32MB          | 32MB  |       |
| 16MB * 2pcs.   | 4MB *<br>2pcs. | 40MB          | 32MB  | 8MB   |
| 16MB * 2pcs.   | 8MB *<br>2pcs. | 48MB          | 32MB  | 16MB  |
| 16MB * 2pcs.   | 16MB * 2pcs.   | 64MB          | 32MB  | 32MB  |
| 32MB * 2pcs.   |                | 64MB          | 64MB  |       |
| 32MB * 2pcs.   | 4MB *<br>2pcs. | 72MB          | 64MB  | 8MB   |
| 32MB * 2pcs.   | 8MB *<br>2pcs. | 80MB          | 64MB  | 16MB  |
| 32MB * 2pcs.   | 16MB * 2pcs.   | 96MB          | 64MB  | 32MB  |
|                |                |               |       |       |

|              |              |           | _ |      |      |       |
|--------------|--------------|-----------|---|------|------|-------|
| 32MB * 2pcs. | 32MB * 2pcs. | 128<br>MB |   | 64MB | 64MB | 128MB |
|              |              | IND       |   |      |      |       |

## 3.5. SRAM INSTALLATION

### Sync. SRAM (PipeLine Burst SRAM)

If Sync SRAM Module is installed, it consists of Pipeline Burst 2 PCs 32 K x 32

from 256K upgradable to 512K.

# There is no jumper for cache size setting.

### 3.6. CPU INSTALLATION AND JUMPERS SETUP

The system speed depends on the frequency of CLOCK GENERATOR. The user can change SW1 selection to set up the system speed to 50 MHz, 60 MHz or 66 MHz for 3.3V/2.5V Pentium Processor (75-200 MHz)/AMDK5(P-75/P-90/P-100/P-120/P-133/P-166/P-200)/Cyrix 6x86 (P-120+/P-133/P-150+/P-166+), Cyrix 6x86L(P-150+/P-166+ 2.8V).

The mainboard can use PENTIUM processor, P54CT, P55C or P54CTB, AMDK5, Cyrix6x86 CPU, and the CPU speed must match with the frequency of CLOCK GEN. It will cause system hanging up if the CLOCK GEN.'S frequency is faster than CPU's.

- The CPU is a sensitive electric component and it can be easily damaged by static electricity, so users must keep it away from metal surface when the CPU is installed onto mainboard.
- When the user installs the CPU on socket, please notice that the PIN 1 of CPU is in the same corner as the PIN 1 of socket!
- Before the CPU is installed, the mainboard must be placed on a flat plane in order to avoid being broken by the pressure of CPU installation.

# 3.7. CMOS RTC & ISA CFG CMOS SRAM

There're RTC & CMOS SRAM on board, they have a power supply from internal battery to keep the DATA inviolate & effective. The RTC is a REAL-TIME CLOCK device which provides the DATE & TIME to system. The CMOS SRAM is used for keeping the information of ISA device system configuration, so the system can automatically boot OS. every time.

Due to the fact that the life-time of RTC internal battery is 5 years, the user can change a new RTC to replace old one after it does not work. The new one's brand and type must be same with the old one.

### **3.8. SPEAKER CONNECTOR INSTALLATION**

There is always a speaker in AT system for sound purpose. The 4 - Pins connector **SPK** is used to connect speaker. The speaker can work well in both direction of connector when it is installed to the connector **SPK** on mainboard.

### 3.9. POWER LED CONNECTOR INSTALLATION

There is a system power LED light on the panel of case. The power LED will light on when system is powered-on. The connector should be installed to **PWR** of mainboard in correct direction.

# 3.10. TURBO SWITCH CONNECTOR INSTALLATION

The TURBO switch on the panel is used for controlling the system speed.

Some program developed on XT should be executed with a low speed system, so a high speed system needs the speed switching function to change its running speed. The mainboard uses 50 MHz speed method to implement TURBO switching function. The **TB** on mainboard should be connected to the TURBO switch on panel, and user can push in or pop out the TURBO switch to enable or disable the turbo function of system.

Please don't use switch ON/OFF when power is ON.

### 3.11. TURBO LED CONNECTOR INSTALLATION

The TURBO LED on panel can indicate the current speed status of system. The TURBO LED connector should be installed to **TD** in correct direction.

### 3.12. HARDWARE RESET SWITCH CONNECTOR INSTALLATION

The RESET switch on panel provides users with HARDWARE RESET function which is almost the same as power-on/off. The system will do a cold start after the RESET switch is pushed and released by user. The RESET switch is a 2 PIN connector and should be installed to **RST** on mainboard.

## **3.13. GREEN FUNCTION INSTALLATION**

For the purpose of power saving, there are two jumpers, **GN** and **GD**, to make sure that the power saving function is working. The **GD** is an indicator (green LED) for green function. If the green LED is ON, the system is operating in green mode. The **GN** is a switch to force the system to get into green mode immediately.

### 3.14. PERIPHERAL DEVICE INSTALLATION

After installation of the device and setup of the jumpers, the mainboard can be mounted into the case and fixed by screw. To complete the mainboard installation, the peripheral devices could be installed now. The basic system needs a display interface card and a storage device.

If a PCI - Bus device is to be installed in the system, any one of four PCI - Bus slots can be used for Slave or Master PCI - Bus device.

After installing the peripheral device, the user should check everything again and prepare to power-on the system.

### 3.15. KEYBOARD SETTING FUNCTION

After booting the O.S., there are some special functions used by keyboard as follows:

| "CTRL_ALT_DEL" | <ul> <li>Pressing these keys simultaneously will cause</li> </ul> |
|----------------|-------------------------------------------------------------------|
|                | system to Warm Start (Software Reset).                            |

# 4. **BIOS CONFIGURATION**

Award's BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in batterybacked CMOS SRAM so that it retains the Setup information when the power is turned off.

### **4.1. ENTERING SETUP**

To Power ON the computer and press <Del> immediately will allow you to enter Setup. The other way to enter Setup is to power on the computer. When the below message appears briefly at the bottom of the screen during the POST (Power On Self Test), press <Del> key or simultaneously press <Ctrl>, <Alt>, and <Esc> keys.

• TO ENTER SETUP BEFORE BOOT PRESS CTRL-ALT-ESC OR DEL KEY

If the message disappears before you respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" bottom on the system case. You may also restart by simultaneously pressing <Ctrl>,<Alt>, and <Del> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to,

# • PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP

### **4.2. CONTROL KEYS**

| Up arrow          | Move to previous item                                                                                                                               |  |  |
|-------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------|--|--|
| Down arrow        | Move to next item                                                                                                                                   |  |  |
| Left arrow        | Move to the item in the left hand                                                                                                                   |  |  |
| Right arrow       | Move to the item in the right hand                                                                                                                  |  |  |
| Esc key           | Main Menu - Quit and not save changes into CMOS<br>Status Page Setup Menu and Option Page Setup Menu - Exit current<br>page and return to Main Menu |  |  |
| PgUp key          | Increase the numeric value or make changes                                                                                                          |  |  |
| PgDn key          | Decrease the numeric value or make changes                                                                                                          |  |  |
| F1 key            | General help, only for Status Page Setup Menu and Option Page<br>Setup Menu                                                                         |  |  |
| F2 key            | Change color from total 16 colors                                                                                                                   |  |  |
| F3 key            | Calendar, only for Status Page Setup Menu                                                                                                           |  |  |
| F4 key            | Reserved                                                                                                                                            |  |  |
| F5 key            | Restore the previous CMOS value from CMOS, only for Option Page Setup Menu                                                                          |  |  |
| F6 key            | Load the default CMOS value from BIOS default table, only for Option Page Setup Menu                                                                |  |  |
| F7 key            | Load the default                                                                                                                                    |  |  |
| F8 key            | Reserved                                                                                                                                            |  |  |
| F9 key            | Reserved                                                                                                                                            |  |  |
| F10 key           | Save all the CMOS changes, only for Main Menu                                                                                                       |  |  |
| 1.3. GETTING HELP |                                                                                                                                                     |  |  |

### 4.3.1. Main Menu

The on-line description of the highlighted setup function is displayed at the bottom of the screen.

## 4.3.2. Status Page Setup Menu / Option Page Setup Menu

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window press <Esc>.

## 4.4. THE MAIN MENU

Once you enter Award BIOS CMOS Setup Utility, the Main Menu (Figure 4.1) will appear on the screen. The Main Menu allows you to select from twelve setup functions and two exit choices. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

| STANDARD CMOS SETUP         | INTEGRATED PERIPHERALS                                               |  |  |
|-----------------------------|----------------------------------------------------------------------|--|--|
| BIOS FEATURES SETUP         | SUPERVISOR PASSWORD                                                  |  |  |
| CHIPSET FEATURES SETUP      | USER PASSWORD                                                        |  |  |
| POWER MANAGEMENT SETUP      | IDE HDD AUTO DETECTION                                               |  |  |
| PNP/PCI CONFIGURATION       | HDD LOW LEVEL FORMAT                                                 |  |  |
| LOAD BIOS DEFAULTS          | SAVE & EXIT SETUP                                                    |  |  |
| LOAD SETUP DEFAULTS         | EXIT WITHOUT SAVING                                                  |  |  |
| ESC : Quit                  | $\wedge \downarrow \rightarrow \leftarrow {}_{: \text{Select Item}}$ |  |  |
| F10 : Save & Exit Setup     | (Shift)F2 : Chang Color                                              |  |  |
| Time, Date, Hard Disk Type, |                                                                      |  |  |

Figure 4.1: Main Menu

Standard CMOS setup

This setup page includes all the items in a standard compatible BIOS.

- MP 586IVX
- BIOS features setup

This setup page includes all the items of Award special enhanced features.

- Chipset features setup
   This setup page includes all the items of chipset special features.
- Power management setup
   This setup page includes all the items of Green function features.
- PNP/PCI configuration

This setup page includes all the items of PNP/PCI configuration features.

Load BIOS defaults

BIOS defaults indicates the most appropriate value of the system parameter which the system would be in safe configuration.

- Load setup defaults
   BIOS defaults indicates the most appropriate value of the system parameter which the system would be in safe configuration.
- Integrated Peripherals
   This setup page includes all the items of peripherals features.
- Supervisor Password
   Change, set, or disable password. It allows you to limit access to the system and Setup, or just to Setup.
- User Password
   Change, set, or disable password. It allows you to limit access to the system.
- IDE HDD auto detection Automatically configure hard disk parameter.
- HDD low level format Low level format IDE Hard Disk.
- Save & exit setup
   Save CMOS value changes to CMOS and exit setup.
- Exit without save
- Abandon all CMOS value changes and exit setup.

# 4.5. STANDARD CMOS SETUP MENU

The items in Standard CMOS Setup Menu (Figure 4.2) are divided into 8 categories. Each category includes no, one or more than one setup items. Use the arrows to highlight the item and then use the PgUp or PgDn keys to

select the value you want in each item.

ROM PCI / ISA BIOS STANDARD CMOS SETUP AWARD SOFTWARE, INC.

| HARD DISKS                                                      | TYPE       | SIZE | CYLS | HEAD        | PRECOMP | LANDZ                  | SECTOR           | MODE |
|-----------------------------------------------------------------|------------|------|------|-------------|---------|------------------------|------------------|------|
| Primary Master                                                  | : Auto     | 0    | 0    | 0           | 0       | 0                      | 0                | AUTO |
| Primary Slave                                                   | : None     | 0    | 0    | 0           | 0       | 0                      | 0                |      |
| Secondary Master                                                | : None     | 0    | 0    | 0           | 0       | 0                      | 0                |      |
| Secondary Slave                                                 | : None     | 0    | 0    | 0           | 0       | 0                      | 0                |      |
| Floppy 3 Mode Support<br>Video : EGA/VGA<br>Halt On : No Errors | : Disabled |      |      |             |         | l Memory:<br>r Memory: | 15360 K<br>384 K |      |
|                                                                 |            |      |      |             | Tota    | l Memory:              | 16384 K          |      |
|                                                                 | •          | 1    |      | Select Iter |         |                        |                  |      |

Date

•

Г

The date format is <day>, <date> <month> <year>. Press <F3> to show the calendar.

| day   | The day, from Sun to Sat, determined by the BIOS and is display-only |
|-------|----------------------------------------------------------------------|
| date  | The date, from 1 to 31 (or the maximum allowed in the month)         |
| month | The month, Jan. through Dec.                                         |
| year  | The year, from 1900 through 2099                                     |

Time

The time format in <hour> <minute> <second>. The time is calculated base on the 24-hour military-time clock. For example, 1 p.m. is 13:00:00.

Primary HDDs / Secondary HDDs

The category identify the types of hard disk drive that has been installed in the computer. There are 45 pre-defined types and a user definable type.

Type 1 to Type 45 are pre-defined. Type User is user-definable and type Auto will automatically detect HDD's type..

Press PgUp or PgDn to select a numbered hard disk type or type the number and press <Enter>. Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category. If your hard disk drive type is not matched or listed, you can use Type User to define your own drive type manually.

If you select Type User, related information is asked to be entered to the following items. Enter the information directly from the keyboard and press <Enter>. Those information should be provided in the documentation form of your hard disk vendor or the system manufacturer.

| CYLS.    | number of cylinders |
|----------|---------------------|
| HEADS    | number of heads     |
| PRECOMP  | write precomp       |
| LANDZONE | landing zone        |
| SECTORS  | number of sectors   |

If a hard disk has not been installed select NONE and press <Enter>.

Drive A type / Drive B type

The category identify the types of floppy disk drive A or drive B that has been installed in the computer.

| None           | No floppy drive installed                            |
|----------------|------------------------------------------------------|
| 360K, 5.25 in. | 5-1/4 inch PC-type standard drive; 360 kilobyte      |
|                | capacity.                                            |
| 1.2M, 5.25 in. | 5-1/4 inch AT-type high-density drive; 1.2 megabyte  |
|                | capacity (3-1/2 inch when 3 Mode is Enabled).        |
| 720K, 3.5 in.  | 3-1/2 inch double-sided drive; 720 kilobyte capacity |
| 1.44M, 3.5 in. | 3-1/2 inch double-sided drive; 1.44 megabyte         |
|                | capacity.                                            |
| 2.88M, 3.5 in. | 3-1/2 inch double-sided drive; 2.88 megabyte         |
|                | capacity.                                            |

• Floppy 3 Mode Support

| Disabled | No 3 mode floppy drive installed.              |
|----------|------------------------------------------------|
| Drive A  | Installed 3 mode drive at drive A.             |
| Drive B  | Installed 3 mode drive at drive B.             |
| Both     | Installed 3 mode drive at drive A and drive B. |

Video

The category detects the type of adapter used for the primary system monitor that must match your video display card and monitor. Although

secondary monitors are supported, you do not have to select the type in setup.

| EGA/VGA | Enhanced Graphics Adapter/Video Graphics Array. For EGA, VGA, SVGA, or PGA monitor adapters |
|---------|---------------------------------------------------------------------------------------------|
| CGA 40  | Color Graphics Adapter, power up in 40 column mode                                          |
| CGA 80  | Color Graphics Adapter, power up in 80 column mode                                          |
| MONO    | Monochrome adapter, includes high resolution<br>monochrome adapters                         |

### Halt on

The category determines whether the computer will stop if an error is detected during power up.

| NO errors         | The system boot will not be stopped for any error       |  |
|-------------------|---------------------------------------------------------|--|
|                   | that may be detected                                    |  |
| All errors        | Whenever the BIOS detects a non-fatal error the         |  |
|                   | system will be stopped and you will be prompted         |  |
| All,But Keyboard  | The system boot will not stop for a keyboard error;     |  |
|                   | it will stop for all other errors                       |  |
| All, But Diskette | The system boot will not stop for a disk error; it will |  |
|                   | stop for all other errors                               |  |
| All, But Disk/Key | The system boot will not stop for a keyboard or         |  |
|                   | disk error; it will stop for all other errors           |  |

### Memory

The category is display-only which is determined by POST (Power On Self Test) of the BIOS.

### **Base Memory**

The POST of the BIOS will determine the amount of base (or conventional) memory installed in the system. The value of the base memory is typically 512 Kfor systems with 512K memory installed on the motherboard, or 640K for systems with 640K or more memory installed on the motherboard.

### **Extended Memory**

The BIOS determines how much extended memory is presented during the POST. This is the amount of memory located above 1MB in the CPU's memory address map.

### **Expanded Memory**

Expanded Memory is defined by the Lotus/Intel/Microsoft (LIM) standard as EMS. Many standard DOS applications can not utilize memory above 640K. The Expanded Memory Specification (EMS) swaps memory which is not utilized by DOS with a section, or frame, so these applications can access all of the system memory. Memory which can be swapped by EMS is

usually 64K within 1MB or memory above 1MB, depending on the chipset design.

Expanded memory device driver is required to use memory as Expanded Memory.

### **Other Memory**

This refers to the memory located in the 640K to 1024 K address space. This is memory that can be used for different applications. DOS uses this area to load device drivers to keep as much base memory free for application programs. Most usage for this area is Shadow RAM.

### 4.6. BIOS FEATURES SETUP

ROM PCI / ISA BIOS BIOS FEATURES SETUP AWARD SOFTWARE, INC.

| Virus Warning            | : Disabled | Video BIOS Shadow : Enabled                               |
|--------------------------|------------|-----------------------------------------------------------|
| CPU Internal Cache       | : Enabled  | C8000 - CBFFF Shadow : Disabled                           |
| External Cache           | : Enabled  | CC000 - CFFFF Shadow : Disabled                           |
| Quick Power On Self Test | : Enabled  | D0000 - D3FFF Shadow : Disabled                           |
| Boot Sequence            | : A, C     | D4000 - D7FFF Shadow : Disabled                           |
| Swap Floppy Drive        | : Disabled | D8000 - DBFFF Shadow : Disabled                           |
| Boot Up Floppy Seek      | : Enabled  | DC000 - DFFFF Shadow : Disabled                           |
| Boot Up NumLock Status   | : On       |                                                           |
| Security Option          | : Setup    |                                                           |
| PCI/VGA Palette Snoop    | : Disabled |                                                           |
| OS Select For DRAM >64MB | : Non-OS2  | ESC : Quit $\land \lor \lor \to \leftarrow$ : Select Item |
|                          |            | F1 : Help PU/PD/+/- : Modify                              |
|                          |            | F5 : Old Values (Shift)F2 : Color                         |
|                          |            | F6 : Load BIOS Defaults                                   |
|                          |            | F7 : Load Setup Defaults                                  |

Figure 4.3: BIOS Features Setup

# Virus Warning

This category flashes on the screen. During and after the system boots up, any attempt to write to the boot sector or partition table of the hard disk drive will halt the system and the following error message will appear. In the mean time, you can run anti-virus program to locate the problem. Default value is Disabled.

| Enabled  | Activate automatically when anything attempts to access<br>the boot sector or hard disk partition table. Which is<br>informed by a warning message. |
|----------|-----------------------------------------------------------------------------------------------------------------------------------------------------|
| Disabled | No warning message appears when anything attempts to<br>access the boot sector or hard disk partition table                                         |

CPU Internal Cache / External Cache

These two categories speed up memory access. However, it depends on CPU / chipset design. The default value is Enabled.

| Enabled  | Enable cache function.  |
|----------|-------------------------|
| Disabled | Disable cache function. |

• Quick Power On Self Test

This category speeds up Power On Self Test (POST) after you power on the computer. If it is set to Enable, BIOS will skip some check items during POST. The default value is Enabled.

| Enabled  | Enable quick POST |
|----------|-------------------|
| Disabled | Normal POST       |

Boot Sequence

This category determines which drive computer searches first for the disk operating system (i.e., DOS). Default value is A,C.

| A,C | System will first search for floppy disk drive then hard disk drive. |
|-----|----------------------------------------------------------------------|
| C,A | System will first search for hard disk drive then floppy disk drive. |

# Swap Floppy Drive

The default value is Disabled.

| Enabled  | Floppy A & B will be swapped under DOS |
|----------|----------------------------------------|
| Disabled | Floppy A & B will be normal definition |

Boot Up Floppy Seek

During POST, BIOS will determine if the floppy disk drive installed is 40 or 80 tracks. 360 KB type is 40 tracks while 720 KB, 1.2 MB, 1.44 MB and 2.88 MB are all 80 tracks. The default value is Enabled.

| BIOS searches for floppy disk drive to determine if it is 40 or |
|-----------------------------------------------------------------|
| 30 tracks, Note that BIOS can not tell from 720 KB, 1.2 MB,     |
| .44 MB and 2.88 MB drive type as they are all 80 tracks.        |
| BIOS will not search for the type of floppy disk drive by track |
| number. Note that there will not be any warning message if      |
| he drive installed is 360 KB.                                   |
| 3                                                               |

Boot Up NumLock Status

The default value is On.

| On  | Keypad is number keys |
|-----|-----------------------|
| Off | Keypad is arrow keys  |

Security Option

This category allows you to limit access to the system and Setup, or just to Setup. The default value is Setup.

| System                                                        | The system will not boot and access to Setup will be denied |  |
|---------------------------------------------------------------|-------------------------------------------------------------|--|
| -                                                             | if the correct password is not entered at the prompt        |  |
| Setup The system will boot, but access to Setup will be denie |                                                             |  |
|                                                               | the correct password is not entered at the prompt           |  |

- To disable security, select PASSWORD SETTING at Main Menu and then you will be asked to enter password. If the user does not type anything and just press <Enter>, it will disable security. Once the security is disabled, the system will boot and you can enter Setup freely.
- PCI/VGA Palette Snoop

| The default value is Disabled. |                                      |                 |
|--------------------------------|--------------------------------------|-----------------|
| Enabled                        | For having Video Card on ISA BUS and | VGA Card on PCI |
|                                | BUS.                                 |                 |
| Disabled                       | For VGA Card only.                   |                 |

OS Select For DRAM>64MB

The default value is Non-OS2.

| Non-OS2 | Using non-OS2 operating system.           |
|---------|-------------------------------------------|
| OS2     | Using OS2 operating system and DRAM>64MB. |

Video BIOS Shadow

It determines whether video BIOS will be copied to RAM, however, it is

**BIOS Configuration** 

optional from chipset design. Video Shadow will increase the video speed. The default value is Enable.

| Enabled  | Video shadow is enabled.  |
|----------|---------------------------|
| Disabled | Video shadow is disabled. |
|          |                           |

C8000 - CFFFF Shadow / D0000 - DFFFF Shadow •

These categories determine whether optional ROM will be copied to RAM by 16 K byte. The default value are Disabled.

| Enabled  | Optional shadow is enabled.  |
|----------|------------------------------|
| Disabled | Optional shadow is disabled. |

# **4.7. CHIPSET FEATURES SETUP**

ROM PCI / ISA BIOS CHIPSET FEATURES SETUP AWARD SOFTWARE INC

|    | AWARD SOFTWARE, INC.       |             |                                                                                  |
|----|----------------------------|-------------|----------------------------------------------------------------------------------|
|    | Auto Configuration         | : Enabled   | Passive Release : Enabled                                                        |
| *  | DRAM Timing                | : 70 ns     | Delayed Transaction : Disabled                                                   |
|    | DRAM RAS# Precharge Time   | : 4         |                                                                                  |
|    | DRAM R/W Leadoff Timing    | : 7         |                                                                                  |
|    | Fast RAS To CAS Delay      | : 3         |                                                                                  |
|    | DRAM Read Burst (EDO/FP)   | : x222/x333 |                                                                                  |
|    | DRAM Write Burst Timing    | : x333      |                                                                                  |
|    | Fast MA to RAS# Delay CLK  | : 2         |                                                                                  |
|    | Refresh RAS# Assertion     | : 5 CLKS    |                                                                                  |
|    | ISA Bus Clock              | : PCI CLK/4 |                                                                                  |
|    | Pipeline Cache Timing      | : Faster    |                                                                                  |
|    | System BIOS Cacheable      | : Enabled   |                                                                                  |
|    | Video BIOS Cacheable       | : Enabled   |                                                                                  |
|    | 8 Bit I/O Recovery Time    | :1          | ESC : Quit $\uparrow \downarrow \downarrow \rightarrow \leftarrow$ : Select Item |
|    | 16 Bit I/O Recovery Time   | :1          | F1 : Help PU/PD/+/- : Modify                                                     |
|    | Memory Hole At 15M-16M     | : Disabled  | F5 : Old Values (Shift)F2 : Color                                                |
|    | Peer Concurrency           | : Enabled   | F6 : Load BIOS Defaults                                                          |
| ** | SDRAM (CAS Lat/RAS-to CAS) | : 3/3       | F7 : Load Setup Defaults                                                         |

# Figure 4.4: Chipset Features Setup

\* This option will show up if Auto configuration is been Enabled. \*\* This option will show up if SDRAM is using.

#### Auto Configuration •

The default value is Enabled.

| Enabled  | Enable auto configuration.  |
|----------|-----------------------------|
| Disabled | Disable auto configuration. |
| DDANT    |                             |

DRAM Timing •

The default value is 70 ns.

| 60 ns Using 60ns DRAM speed.  |  |
|-------------------------------|--|
| 70 no. Using 70no DDAM around |  |
| 70 ns Using 70ns DRAM speed.  |  |

DRAM RAS# Precharge Time

| The default | value is 4.                        |
|-------------|------------------------------------|
| 3           | Set DRAM RAS# precharge time to 3. |
| 4           | Set DRAM RAS# precharge time to 4. |

• DRAM R/W Leadoff Timing

| The default value is 7. |                      |              |
|-------------------------|----------------------|--------------|
| 7                       | Set DRAM R/W leadoff | timing to 7. |
| 6                       | Set DRAM R/W leadoff | timing to 6. |

• Fast RAS To CAS Delay

| The default value is 3. |                                 |
|-------------------------|---------------------------------|
| 2                       | Set Fast RAS to CAS delay to 2. |
| 3                       | Set Fast RAS to CAS delay to 3. |

• DRAM Read Burst (EDO/FP)

|                                             | The default value is x222/x333. |                                   |  |
|---------------------------------------------|---------------------------------|-----------------------------------|--|
|                                             | x444/x444                       | Set DRAM read burst to x444/x444. |  |
| x333/x444 Set DRAM read burst to x333/x444. |                                 |                                   |  |
|                                             | x222/x333                       | Set DRAM read burst to x222/x333. |  |

 DRAM write Burst timing The default value is x333

| The default value is x333. |                                      |  |
|----------------------------|--------------------------------------|--|
| x222                       | Set DRAM write burst timing to x222. |  |
| x333                       | Set DRAM write burst timing to x333. |  |
| x444                       | Set DRAM write burst timing to x444. |  |

• Fast MA to RAS# Delay CLK

| The default value is 2. |                           |        |
|-------------------------|---------------------------|--------|
| 2                       | Set Fast MA to RAS# Delay | 2 CLK. |
| 1                       | Set Fast MA to RAS# Delay | 1 CLK. |

Refresh RAS# Assertion

The default value is 5 CLKS.

| 4 | Set Refresh RAS# Assertion to 4 CLKS |
|---|--------------------------------------|
| 5 | Set Refresh RAS# Assertion to 5 CLKS |

ISA Bus Clock

The default value is PCICLK/4.

| PCICLK/3 | For 50 MHz system.    |  |
|----------|-----------------------|--|
| PCICLK/4 | For 66,60 MHz system. |  |

• Pipeline Cache Timing

The default value is Faster.

| Faster  | Set Pipeline Cache Timing to Faster.  |
|---------|---------------------------------------|
| Fastest | Set Pipeline Cache Timing to Fastest. |

System BIOS Cacheable

The default value is Enabled.

| Enabled                                 | Enabled Enable system BIOS cacheable. |  |
|-----------------------------------------|---------------------------------------|--|
| Disabled Disable system BIOS cacheable. |                                       |  |

• Video BIOS Cacheable

| The default value is Enabled. |                               |
|-------------------------------|-------------------------------|
| Enabled                       | Enable video BIOS cacheable.  |
| Disabled                      | Disable video BIOS cacheable. |

• 8 Bit I/O Recovery Time

The default value is 1.

| NA None.    |  |
|-------------|--|
| INA INOIIE. |  |

- 16 Bit I/O Recovery Time
  - The default value is 1.

| 1~4 | Set 16 Bit I/O recovery time from 1 to 4. |
|-----|-------------------------------------------|
| NA  | None.                                     |
|     |                                           |

• Memory Hole At 15M-16M

The default value is Disabled.

| Disabled | Normal Setting.                          |
|----------|------------------------------------------|
| Enabled  | Set Address=15~16MB relocate to ISA BUS. |

Peer Concurrency

The default value is Enabled.

| Disabled | Disable peer concurrency. |
|----------|---------------------------|
| Enabled  | Enable peer concurrency.  |
| 00011/0  |                           |

• SDRAM (CAS Lat/RAS-to CAS)

The default value is 3/3.

| 3/3 | SDRAM (CAS Lat/RAS-to CAS) is 3/3. |
|-----|------------------------------------|
| 3/2 | SDRAM (CAS Lat/RAS-to CAS) is 3/2. |
| 2/2 | SDRAM (CAS Lat/RAS-to CAS) is 2/2. |

Passive Release

The default value is Enabled

| Disabled | led Disable Passive Release. |  |
|----------|------------------------------|--|
| Enabled  | Enable Passive Release.      |  |

Delayed Transaction

The default value is Disabled.

| Disabled | Disable Delayed Transaction. |  |
|----------|------------------------------|--|
| Enabled  | Enable Delayed Transaction.  |  |

# **4.8. POWER MANAGEMENT SETUP**

ROM PCI / ISA BIOS POWER MANAGEMENT SETUP AWARD SOFTWARE, INC.

| ESC : Quit $1  \bigvee  X  :$ select item<br>F1 : Help PU/PD/+/- : Modify<br>F5 : Old Values (Shift)F2 : Color<br>F6 : Load BIOS Defaults<br>F7 : Load Setup Defaults | PM Control by APM : Y<br>Video Off Method : I<br>Standby Mode : I<br>Suspend Mode : I | MS IRQ4 (COM 1) : ON<br>abled IRQ5 (LPT 2) : OFF<br>abled IRQ6 (Floppy Disk) : ON<br>abled IRQ7 (LPT 1) : OFF<br>IRQ9 (IRQ2 Redir) : OFF<br>*** IRQ10 (Reserved) : OFF<br>N IRQ11 (Reserved) : ON<br>IRQ12 (PS/2 Mouse) : ON<br>IRQ14 (Hard Disk) : ON<br>IRQ15 (Reserved) : ON<br>IRQ16 (Reserved) : ON<br>IRQ17 (LPT 1) : Select Item<br>F1 : Help PU/PD/+/- : Modify<br>F5 : Old Values (Shift)F2 : Color<br>F6 : Load BIOS Defaults |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|

Figure 4.5: Power Management Setup

 Power Management The default value is Enabled.

**BIOS Configuration** 

| Enabled Ena                                                                           | ble Green function.                                                                       |  |  |  |  |
|---------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------|--|--|--|--|
|                                                                                       |                                                                                           |  |  |  |  |
|                                                                                       |                                                                                           |  |  |  |  |
| Please disable Green Function for Non-S CPU in OS/2, Unix, Window NT & Novell system. |                                                                                           |  |  |  |  |
| ,                                                                                     |                                                                                           |  |  |  |  |
| PM Control by A                                                                       |                                                                                           |  |  |  |  |
| The default value                                                                     |                                                                                           |  |  |  |  |
|                                                                                       | able software APM function.                                                               |  |  |  |  |
| Video off Method                                                                      |                                                                                           |  |  |  |  |
|                                                                                       | -                                                                                         |  |  |  |  |
| The default value                                                                     |                                                                                           |  |  |  |  |
| V/H SYNC+Bla                                                                          |                                                                                           |  |  |  |  |
| Blank Screen                                                                          | mode for Green monitor power saving.<br>BIOS will only black monitor when gets into Green |  |  |  |  |
| DIALIK SCIEELI                                                                        | mode.                                                                                     |  |  |  |  |
| DPMS                                                                                  | BIOS will use DPMS Standard to control VGA                                                |  |  |  |  |
| DINIO                                                                                 | card. (The Green type VGA card will turn of V/H-                                          |  |  |  |  |
|                                                                                       | SYNC automatically.)                                                                      |  |  |  |  |
| Standby Mode                                                                          | · · · ·                                                                                   |  |  |  |  |
| The default value                                                                     | e is Disable.                                                                             |  |  |  |  |
| Disable                                                                               | Disable Standby Mode.                                                                     |  |  |  |  |
| 1 min - 1 Hour                                                                        | Setup the timer to enter Standby Mode.                                                    |  |  |  |  |
| Suspend mode                                                                          |                                                                                           |  |  |  |  |
| The default value                                                                     | e is Disable.                                                                             |  |  |  |  |
| Disable Disable Suspend Mode.                                                         |                                                                                           |  |  |  |  |
| 1 min - 1 Hour                                                                        | Setup the timer to enter Suspend Mode.                                                    |  |  |  |  |
| HDD Power Dov                                                                         |                                                                                           |  |  |  |  |
| The default value                                                                     | e is Disable.                                                                             |  |  |  |  |
|                                                                                       | isable HDD Power Down mode function.                                                      |  |  |  |  |
| 1-15 mins Enable HDD enter Power Down mode between 1 to 15                            |                                                                                           |  |  |  |  |
| mins.                                                                                 |                                                                                           |  |  |  |  |
| IRQX ( 3,4,5,6,7                                                                      | 7,9,10,11,12,14,15 )                                                                      |  |  |  |  |
| The default value                                                                     | · · · · · · · ·                                                                           |  |  |  |  |
|                                                                                       | system will return to normal mode from Green Mode                                         |  |  |  |  |
|                                                                                       | en the IRQX is active.                                                                    |  |  |  |  |
| Off The                                                                               | system will not return to normal mode from Green                                          |  |  |  |  |
| Mod                                                                                   | de when the IRQX is active.                                                               |  |  |  |  |

4-14

## 4.9. PNP/PCI CONFIGURATION

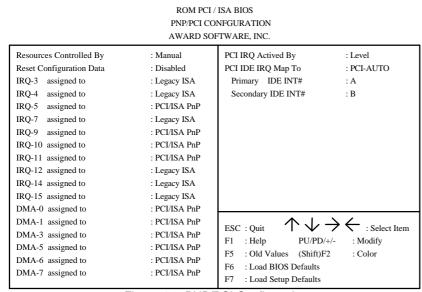


Figure 4.6: PNP/PCI Configuration

Resources Controlled By

The default value is Manual.

| Manual | Disable resources controlled. |
|--------|-------------------------------|
| Auto   | Enable resources controlled.  |

Reset Configuration Data

The default value is Disabled.

| Disabled | Disable reset configuration data. |
|----------|-----------------------------------|
| Enabled  | Enable reset configuration data.  |

• Assigned to IRQ(3,4,5,7,9,10,11,12,14,15) / DMA (0,1,3,5,6,7)

| Legacy ISA  | Assigned IRQX/DMAX to ISA Bus.                       |
|-------------|------------------------------------------------------|
| PCI/ISA PnP | Assigned IRQX/DMAX to PCI/ISA Bus with PnP function. |

PCI IRQ Actived By

|   | Level   | For some PCI SCSI or Lan device using same PCI INT. |  |  |
|---|---------|-----------------------------------------------------|--|--|
|   | Edge    | Normal operating.                                   |  |  |
| • | PCI IDE | IRQ Map To                                          |  |  |

**BIOS Configuration** 

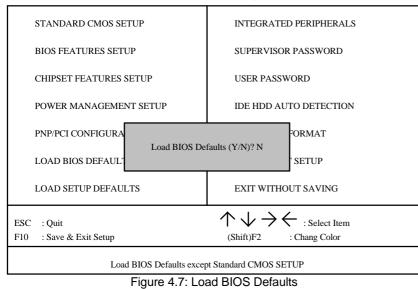
| 1.0 |                     |                                            |
|-----|---------------------|--------------------------------------------|
|     | PCI-Auto            | Map PCI IDE IRQ to PCI slot automatically. |
|     | ISA                 | Map PCI IDE IRQ to ISA slot.               |
|     | PCI-Slot1~PCI-Slot4 | Map PCI IDE-IRQ to PCI-Slot1~PCI-Slot4.    |

Primary/Secondary IDE INT#

| Α | Set INTA for primary/secondary PCI IDE. |
|---|-----------------------------------------|
| В | Set INTB for primary/secondary PCI IDE. |
| С | Set INTC for primary/secondary PCI IDE. |
| D | Set INTD for primary/secondary PCI IDE. |

## 4.10. LOAD BIOS DEFAULTS

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.



Load BIOS Defaults

To load SETUP defaults value to CMOS SRAM, enter "Y". If not, enter "N".

# 4.11. LOAD SETUP DEFAULTS

### ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

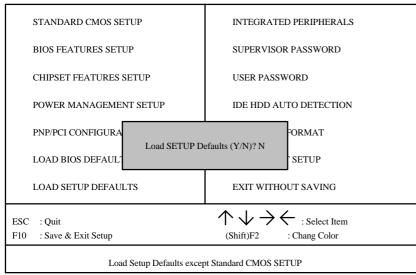


Figure 4.8: Load Setup Defaults

Load SETUP Defaults

To load SETUP defaults value to CMOS SRAM, enter "Y". If not, enter "N".

• If there is any problem occurred, loading SETUP DEFAULTS step is recommended.

# **4.12. INTEGRATED PERIPHERALS**

|                                                                                                                                                                                                                                                                                                                    | INTEGRA                                                                                                                                             | PCI / ISA BIOS<br>FED PERIPHERALS<br>SOFTWARE, INC.                                                                                                                                                                  |  |  |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|--|
| IDE HDD Block Mode<br>IDE Primary Master PIO<br>IDE Primary Slave PIO<br>IDE Secondary Master PIO<br>IDE Secondary Slave PIO<br>On-Chip Primary PCI IDE<br>On-Chip Secondary PCI IDE<br>Onboard FDD Controller<br>Onboard Serial Port 1<br>Onboard Serial Port 2<br>Onboard Parallel Port<br>Onboard Parallel Mode | : Enabled<br>: Auto<br>: Auto<br>: Auto<br>: Auto<br>: Enabled<br>: Enabled<br>: Enabled<br>: 3F8/IRQ4<br>: 2F8/IRQ3<br>: 378H/IRQ7<br>: SPP<br>: 3 | USB Controller : Disabled                                                                                                                                                                                            |  |  |
|                                                                                                                                                                                                                                                                                                                    |                                                                                                                                                     | ESC : Quit $\uparrow \downarrow \rightarrow \leftarrow$ : Select Item<br>F1 : Help PU/PD/+/- : Modify<br>F5 : Old Values (Shift)F2 : Color<br>F6 : Load BIOS Defaults<br>F7 : Load Setup Defaults<br>tod Poriphorals |  |  |
| Figure 4.9: Integrated Peripherals                                                                                                                                                                                                                                                                                 |                                                                                                                                                     |                                                                                                                                                                                                                      |  |  |

- \* This item will show up only if On board Parallel Mode set to ECP or ECP/EPP.
- IDE HDD Block Mode

The default value is Enabled.

| Enabled  | Enable IDE HDD Block Mode  |
|----------|----------------------------|
| Disabled | Disable IDE HDD Block Mode |

• IDE Primary Master PIO (for onboard IDE 1st channel).

### The default value is Auto.

| Auto    | BIOS will automatically defect the IDE HDD Accessing mode. |  |
|---------|------------------------------------------------------------|--|
| Mode0~4 | Manually set the IDE Accessing mode.                       |  |

• IDE Primary Slave PIO (for onboard IDE 1st channel).

| The  | default | value | is | Δυτο  |
|------|---------|-------|----|-------|
| 1116 | uelauli | value | 15 | Auto. |

.

|   | Auto    | BIOS will automatically defect the IDE HDD Accessing mode. |
|---|---------|------------------------------------------------------------|
|   | Mode0~4 | Manually set the IDE Accessing mode.                       |
| 1 |         |                                                            |

IDE Secondary Master PIO (for onboard IDE 2nd channel).

| Auto                                                                                                                 | BIOS will automatically defect the IDE HDD Accessing mode. |  |  |  |
|----------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|--|--|--|
| Mode0~4                                                                                                              | Manually set the IDE Accessing mode.                       |  |  |  |
| IDE Second                                                                                                           | Secondary Slave PIO (for onboard IDE 2nd channel).         |  |  |  |
|                                                                                                                      | value is Auto.                                             |  |  |  |
| Auto                                                                                                                 | BIOS will automatically defect the IDE HDD Accessing mode  |  |  |  |
| Mode0~4                                                                                                              | Manually set the IDE Accessing mode.                       |  |  |  |
| On-chip Pri                                                                                                          | mary/Secondary PCI IDE                                     |  |  |  |
|                                                                                                                      | value is Enabled.                                          |  |  |  |
| Enabled                                                                                                              | Enable On-chip Primary/Secondary PCI IDE.                  |  |  |  |
| Disabled                                                                                                             | Disable On-chip Primary/Secondary PCI IDE.                 |  |  |  |
| Onboard FE                                                                                                           | DD Controller                                              |  |  |  |
| The default                                                                                                          | value is Enabled.                                          |  |  |  |
| Enabled                                                                                                              | Enable onboard FDD port.                                   |  |  |  |
| Disabled                                                                                                             | Disable onboard FDD port.                                  |  |  |  |
| Onboard Se                                                                                                           | erial Port 1                                               |  |  |  |
| The default                                                                                                          | value is 3F8/IRQ4.                                         |  |  |  |
| COM1/3F8                                                                                                             |                                                            |  |  |  |
| COM2/2F8                                                                                                             | Enable onboard Serial port 1 and address is 2F8H.          |  |  |  |
| COM3/3E8                                                                                                             | Enable onboard Serial port 1 and address is 3E8H.          |  |  |  |
| COM4/2E8                                                                                                             |                                                            |  |  |  |
| Disabled                                                                                                             | Disable onboard Serial port 1.                             |  |  |  |
| Auto                                                                                                                 | Automatically assign proper com port address.              |  |  |  |
| Onboard Se                                                                                                           | erial Port 2                                               |  |  |  |
| The default                                                                                                          | value is 2F8/IRQ3.                                         |  |  |  |
| COM1/3F8                                                                                                             |                                                            |  |  |  |
| COM2/2F8                                                                                                             |                                                            |  |  |  |
| COM3/3E8                                                                                                             |                                                            |  |  |  |
| COM4/2E8                                                                                                             |                                                            |  |  |  |
| Disabled                                                                                                             | Disable onboard Serial port 2.                             |  |  |  |
| Auto                                                                                                                 | Automatically assign proper com port address.              |  |  |  |
| Onboard Pa                                                                                                           | •                                                          |  |  |  |
|                                                                                                                      | value is 378H/IRQ7.                                        |  |  |  |
| 3BCH/IRQ                                                                                                             |                                                            |  |  |  |
| 378H/IRQ7                                                                                                            |                                                            |  |  |  |
| 278H/IRQ5         Enable onboard LPT port and address is 278H/I           Disabled         Disable onboard LPT port. |                                                            |  |  |  |

The default value is SPP.

**BIOS Configuration** 

| SPP     | Using Parallel port as Normal Printer Port.                     |  |  |
|---------|-----------------------------------------------------------------|--|--|
| EPP     | Using Parallel port as Enhanced Parallel Port.                  |  |  |
| ECP     | Using Parallel port as Extended Capabilities Port. 卷            |  |  |
| ECP/EPP | Using Parallel port as ECP & EPP mode/Enhanced Parallel Port. 举 |  |  |

★ As ECP,ECP/EPP Mode is selected, two options can be defined:
 1. ECP Mode use DMA: 3 2. ECP Mode use DMA: 1

USB Controller

The default value is Disabled.

| Enabled  | Enable USB Controller.     |  |  |
|----------|----------------------------|--|--|
| Disabled | ed Disable USB Controller. |  |  |

## 4.13. SUPERVISOR / USER PASSWORD

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.

### ENTER PASSWORD

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

| STANDARD CMOS SETUP                   | INTEGRATED PERIPHERALS                                                     |  |  |
|---------------------------------------|----------------------------------------------------------------------------|--|--|
| BIOS FEATURES SETUP                   | SUPERVISOR PASSWORD                                                        |  |  |
| CHIPSET FEATURES SETUP                | USER PASSWORD                                                              |  |  |
| POWER MANAGEMENT SETUP                | IDE HDD AUTO DETECTION                                                     |  |  |
| PNP/PCI CONFIGURATION                 | HDD LOW LEVEL FORMAT                                                       |  |  |
| LOAD BIOS DEFAULTS Enter Password:    | SETUP                                                                      |  |  |
| LOAD SETUP DEFAULTS                   | EXIT WITHOUT SAVING                                                        |  |  |
| ESC : Quit<br>F10 : Save & Exit Setup | $ \bigwedge \bigcup_{(Shift)F2} \rightarrow \longleftarrow : Select Item $ |  |  |
| Change / Set / D                      | bisable Password                                                           |  |  |

Figure 4.10: Supervisor / User Password

Type the password, up to eight characters, and press <Enter>. The password typed now will clear the previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter

a password.

To disable password, just press <Enter> when you are prompted to enter password. A message will confirm the password being disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

# PASSWORD DISABLED

If you select System at Security Option of BIOS Features Setup Menu, you will be prompted for the password every time the system is rebooted or any time you try to enter Setup. If you select Setup at Security Option of BIOS Features Setup Menu, you will be prompted only when you try to enter Setup.

# 4.14. IDE HDD AUTO DETECTION

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

| STANDARD CMOS SETUP                        | INTEGRATED PERIPHERALS                                                              |  |  |
|--------------------------------------------|-------------------------------------------------------------------------------------|--|--|
| BIOS FEATURES SETUP                        | SUPERVISOR PASSWORD                                                                 |  |  |
| CHIPSET FEATURES SETUP                     | USER PASSWORD                                                                       |  |  |
| POWER MANAGEMENT SETUP                     | IDE HDD AUTO DETECTION                                                              |  |  |
| PNP/PCI CONFIGURATION                      | HDD LOW LEVEL FORMAT                                                                |  |  |
| LOAD BIOS DEFAULTS                         | SAVE & EXIT SETUP                                                                   |  |  |
| LOAD SETUP DEFAULTS                        | EXIT WITHOUT SAVING                                                                 |  |  |
| ESC : Quit<br>F10 : Save & Exit Setup      | $\bigwedge \downarrow \rightarrow \leftarrow : Select Item$ (Shift)F2 : Chang Color |  |  |
| Auto-Configure HDD: Sector, Cylinder, Head |                                                                                     |  |  |

Figure 4.11: IDE HDD Auto Detection

Type "Y" will accept the H.D.D. parameter reported by BIOS. Type "N" will keep the old H.D.D. parameter setup. If the hard disk cylinder NO. is over 1024, then the user can select LBA mode or LARGER mode for DOS partition LARGE than 528 MB.

# 4.15. HDD LOW LEVEL FORMAT

ROM PCI / ISA BIOS CMOS SETUP UTILITY AWARD SOFTWARE, INC.

| STANDARD CMOS SETUP                | INTEGRATED PERIPHERALS                                                 |  |
|------------------------------------|------------------------------------------------------------------------|--|
| BIOS FEATURES SETUP                | SUPERVISOR PASSWORD                                                    |  |
| CHIPSET FEATURES SETUP             | USER PASSWORD                                                          |  |
| POWER MANAGEMENT SETUP             | IDE HDD AUTO DETECTION                                                 |  |
| PNP/PCI CONFIGURATION              | HDD LOW LEVEL FORMAT                                                   |  |
| LOAD BIOS DEFAULTS                 | SAVE & EXIT SETUP                                                      |  |
| LOAD SETUP DEFAULTS                | EXIT WITHOUT SAVING                                                    |  |
| ESC : Quit                         | $\uparrow \downarrow \rightarrow \leftarrow {}_{: \text{Select Item}}$ |  |
| F10 : Save & Exit Setup            | (Shift)F2 : Chang Color                                                |  |
| Hard Disk Low Level Format Utility |                                                                        |  |

Figure 4.12: HDD Low Level Format

HDD Low Level Format Utility:

In main manual: There are three options to choose:

one is: SELECT DRIVE: "C" or "D".

another one is: BAD TRACK LIST: User can auto, add, modify, delete, clear for bad track of HDD.

the other one is : PREFORMAT: Lower Level Format HDD.

# 4.16. SAVE & EXIT SETUP

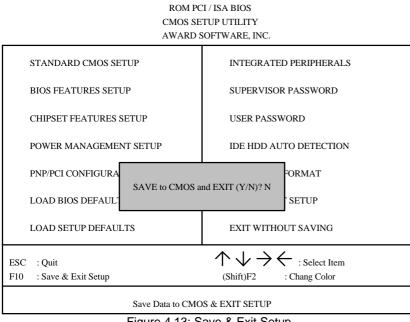


Figure 4.13: Save & Exit Setup

Type "Y" will quit the Setup Utility and save the user setup value to RTC CMOS SRAM.

Type "N" will return to Setup Utility.

# 4.17. EXIT WITHOUT SAVING

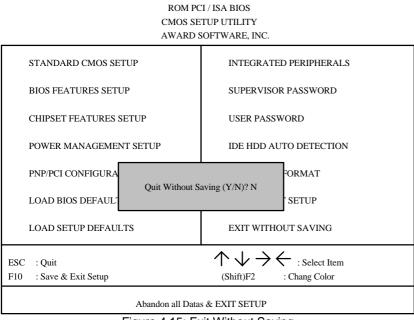


Figure 4.15: Exit Without Saving

Type "Y" will quit the Setup Utility without saving to RTC CMOS SRAM.

Type "N" will return to Setup Utility.

# **5. AT TECHNICAL INFORMATION**

# 5.1. I/O BUS Enhanced Parallel Port CONNECTOR PIN OUT

# 5.1.1. ISA SLOT PIN OUT

| 1        |     |     | 1          |                   |            |                |     |
|----------|-----|-----|------------|-------------------|------------|----------------|-----|
| GND      | B01 | A01 | I/O CH CHK |                   |            |                |     |
| RESET    | B02 | A02 | SD07       |                   |            |                |     |
| +5V      | B03 | A03 | SD06       |                   |            |                |     |
| IRQ9     | B04 | A04 | SD05       |                   |            |                |     |
| -5V      | B05 | A05 | SD04       |                   |            |                |     |
| DRQ2     | B06 | A06 | SD03       |                   |            |                |     |
| -12V     | B07 | A07 | SD02       |                   |            |                |     |
| 0WS      | B08 | A08 | SD01       |                   |            |                |     |
| +12V     | B09 | A09 | SD00       |                   |            |                |     |
| GND      | B10 | A10 | I/O CH RDY |                   |            |                |     |
| -SMEMW   | B11 | A11 | AEN        |                   |            |                |     |
| -SMEMR   | B12 | A12 | SA19       |                   |            |                |     |
| -IOW     | B13 | A13 | SA18       |                   |            |                |     |
| -IOR     | B14 | A14 | SA17       | -MEMCS16          | D01        | C01 SB         | HE  |
| -DACK3   | B15 | A15 | SA16       | -MEMC316          | D01<br>D02 | C013B          |     |
| -DRQ3    | B16 | A16 | SA15       | -1/0C310<br>IRQ10 | D02<br>D03 |                |     |
| -DACK1   | B17 | A17 | SA14       | IRQ11             | D03<br>D04 | C04 LA         |     |
| -DRQ1    | B18 | A18 | SA13       | IRQ11<br>IRQ12    | D04<br>D05 | C04 LA         |     |
| -REFRESH | B19 | A19 | SA12       | IRQ12<br>IRQ15    | D05        | C05 LA         |     |
| BCLK     | B20 | A20 | SA11       | IRQ13<br>IRQ14    | D00<br>D07 |                |     |
| IRQ7     | B21 | A21 | SA10       | -DACK0            | D07<br>D08 | C07 LA         |     |
| IRQ6     | B22 | A22 | SA09       |                   |            |                |     |
| IRQ5     | B23 | A23 | SA08       | DRQ0              | D09        |                | EMR |
| IRQ4     | B24 | A24 | SA07       | -DACK5            | D10        |                | EMW |
| IRQ3     | B25 | A25 | SA06       | DRQ5              | D11        | C11 SD         |     |
| -DACK2   | B26 | A26 | SA05       | -DACK6            | D12        | C12SD          |     |
| T/C      | B27 | A27 | SA04       | DRQ6              | D13<br>D14 | C13 SD         |     |
| BALE     | B28 | A28 | SA03       | -DACK7            |            | C14SD<br>C15SD |     |
| +5V      | B29 | A29 | SA02       | DRQ7<br>+5V       | D15<br>D16 |                |     |
| OSC      | B30 | A30 | SA01       |                   |            |                |     |
| GND      | B31 | A31 | SA00       | -MASTER           | D17        | C17 SD         |     |
|          |     |     | l          | GND               | D18        | C18SD          | 15  |

# 5.2. I/O & MEMORY MAP

| MEMORY MAP: | [0000000-009FFF]<br>[00A0000-00BFFF]<br>[00C0000-00DFFFF]<br>[00E0000-00EFFFF]<br>[00F0000-00FFFFF]<br>[0100000-BFFFFFF]                                                                                                                            | System memory used by DOS and application program.<br>Display buffer memory for VGA/ EGA/CGA/MONOCHROME adapter.<br>Reserved for I/O device BIOS ROM or RAM buffer.<br>Reserved for PCI device ROM.<br>System BIOS ROM.<br>System extension memory.                                                                                                                                                                                                                                                                                |
|-------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| I/O MAP:    | [000-01F]<br>[022-023]<br>[040-05F]<br>[060-06F]<br>[070-07F]<br>[0A0-08F]<br>[0C0-0DF]<br>[0C0-0DF]<br>[0F0-0FF]<br>[1F0-1F8]<br>[278-27F]<br>[280-2DF]<br>[2F8-2FF]<br>[360-36F]<br>[378-37F]<br>[3B0-3BF]<br>[3C0-3CF]<br>[3D0-3DF]<br>[3F0-3F7] | DMA controller.(Master)<br>INTERRUPT controller.(Master)<br>CHIPSET control registers I/O ports.<br>TIMER control registers.<br>KEYBOARD interface controller.(8042)<br>RTC ports & CMOS I/O ports.<br>DMA register.<br>INTERRUPT controller.(Slave)<br>DMA controller.(Slave)<br>MATH COPROCESSOR<br>HARD DISK controller.<br>PARALLEL port-2.<br>GRAPHICS adapter controller.<br>SERIAL port-2.<br>NETWORK ports.<br>PARALLEL port-1<br>MONOCHROME & PRINTER adapter.<br>EGA adapter.<br>CGA adapter.<br>FLOPPY DISK controller. |
|             | [3F8-3FF]                                                                                                                                                                                                                                           | SERIAL port-1.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |

# 5.3. TIMER & DMA CHANNELS MAP

| TIMER MAP:    | TIMER Channel-0 System timer interrupt<br>TIMER Channel-1 DRAM REFRESH request<br>TIMER Channel-2 SPEAKER tone generator                                                                                                                         |
|---------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| DMA CHANNELS: | DMA Channel-0 Available<br>DMA Channel-1 IBM SDLC<br>DMA Channel-2 FLOPPY DISK adapter<br>DMA Channel-3 Available<br>DMA Channel-4 Cascade for DMA controller 1<br>DMA Channel-5 Available<br>DMA Channel-6 Available<br>DMA Channel-7 Available |

### **5.4. INTERRUPT MAP**

NMI: Pa

Parity check error

IRQ (H/W): 0 System TIMER interrupt from TIMER-0 1 KEYBOARD output buffer full 2 Cascade for IRQ 8-15 3 SERIAL port 2 4 SERIAL port 1 5 PARALLEL port 2 6 FLOPPY DISK adapter 7 PARALLEL port 1 8 RTC clock 9 Available 10 Available 11 Available 12 PS/2 Mouse 13 MATH coprocessor 14 HARD DISK adapter 15 Available



# 5.5. RTC & CMOS RAM MAP

| 00<br>01<br>02<br>03<br>04<br>05<br>06<br>07<br>08<br>09<br>0A<br>0B<br>0C<br>0D<br>0E<br>0F<br>10<br>11<br>12<br>13<br>14<br>15<br>16<br>17<br>18<br>19-2d | Seconds<br>Second alarm<br>Minutes<br>Minutes<br>Minutes alarm<br>Hours<br>Hours alarm<br>Day of week<br>Day of week<br>Day of month<br>Month<br>Year<br>Status register A<br>Status register B<br>Status register C<br>Status register D<br>Diagnostic status byte<br>Shutdown byte<br>FLOPPY DISK drive type byte<br>Reserve<br>HARD DISK type byte<br>Reserve<br>Equipment byte<br>Base memory low byte<br>Extension memory low byte |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 2E-2F<br>30<br>31<br>32<br>33<br>34-3F<br>40-7f                                                                                                             | Reserved for extension memory low byte<br>Reserved for extension memory high byte<br>DATE CENTURY byte<br>INFORMATION FLAG<br>Reserve<br>Reserved for CHIPSET SETTING DATA                                                                                                                                                                                                                                                              |
|                                                                                                                                                             | 01<br>02<br>03<br>04<br>05<br>06<br>07<br>08<br>09<br>0A<br>0B<br>0C<br>0D<br>0E<br>0F<br>10<br>11<br>12<br>13<br>14<br>15<br>16<br>17<br>18<br>19-2d<br>2E-2F<br>30<br>31<br>32<br>33<br>34-3F                                                                                                                                                                                                                                         |

# APPENDIX A: POST MESSAGE

When the BIOS encounters an error that requires the user to correct something, either a beep code will sound or a message will be displayed in a box in the middle of the screen and the message PRESS F1 TO CONTINUE, CTRL-ALT-ESC OR DEL TO ENTER SETUP will be shown in the information box at the bottom.

POST BEEP

Currently there is only one beep code in BIOS. This code indicates that a video error has occurred and the BIOS cannot initialize the video screen to display any additional information. This beep code consists of a single long beep followed by two short beeps.

ERROR MESSAGE

Once or more of the following messages may be displayed if the BIOS detects an error during the POST. This list includes message for both the ISA and the EISA BIOS.

CMOS BATTERY HAS FAILED

CMOS battery is no longer functional. It should be replaced.

➢ CMOS CHECKSUM ERROR

Checksum of CMOS is incorrect. This can indicate that CMOS has become corrupt. This error may have been caused by a weak battery. Check the battery and replace if necessary.

☑ DISK BOOT FAILURE, INSERT SYSTEM DISK AND PRESS ENTER

No boot device was found. Insert a system disk into Drive A: and press <Enter>. If you assumed the system would boot from the hard drive, make sure the controller is inserted correctly and all cables are properly attached. Also be sure the disk is formatted as a boot device. Then reboot the system.

☑ DISKETTE DRIVES OR TYPES MISMATCH ERROR - RUN SETUP

Type of diskette drive installed in the system is different from the CMOS definition. Run Setup to re-configure the drive type correctly.

DISPLAY SWITCH IS SET INCORRECTLY

Display switch on the motherboard can be set to either monochrome or color. This indicates the switch is set to a different setting than indicated in Setup. Determine which setting is correct, and then either turn off the system and change the jumper, or enter Setup and change the VIDEO selection.

DISPLAY TYPE HAS CHANGED SINCE LAST BOOT

Since last powering off the system, the display adapter has been changed. You must configure the system for the new display type.

EISA Configuration Checksum Error

PLEASE RUN EISA CONFIGURATION UTILITY

The EISA non-volatile RAM checksum is incorrect or cannot correctly read the EISA slot. This can indicate either the EISA non-volatile memory has become corrupted or the slot has been configured incorrectly. Also be sure the card is installed firmly in the slot.

EISA Configuration Is Not Complete

PLEASE RUN EISA CONFIGURATION UTILITY

The slot configuration information stored in the EISA non-volatile memory is incomplete.

- When either of these errors appear, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.
- ERROR ENCOUNTERED INITIALIZING HARD DRIVE

Hard drive cannot be initialized. Be sure the adapter is installed correctly and all cables are correctly and firmly attached. Also be sure the correct hard drive type is selected in Setup.

ERROR INITIALIZING HARD DISK CONTROLLER

Cannot initialize controller. Make sure the cord is correctly and firmly installed in the bus. Be sure the correct hard drive type is selected in Setup. Also check to see if any jumper needs to be set correctly in the hard drive.

### FLOPPY DISK CNTRLR ERROR OR NO CNTRLR PRESENT

Cannot find or initialize the floppy drive controller. Make sure the controller is installed correctly and firmly. If there are no floppy drives installed, be sure the Diskette Drive selection in Setup is set to NONE.

Invalid EISA Configuration

PLEASE RUN EISA CONFIGURATION UTILITY

The non-volatile memory containing EISA configuration information was programmed incorrectly or has become corrupted. Re-run EISA configuration utility to correctly program the memory.

- When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.
- ➢ KEYBOARD ERROR OR NO KEYBOARD PRESENT

Cannot initialize the keyboard. Make sure the keyboard is attached correctly and no keys are being pressed during the boot.

If you are purposely configuring the system without a keyboard, set the error halt condition in Setup to HALT ON ALL, BUT KEYBOARD. This will cause the BIOS to ignore the missing keyboard and continue the boot.

➢ Memory Address Error at ...

Indicates a memory address error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

☑ Memory parity Error at ...

Indicates a memory parity error at a specific location. You can use this location along with the memory map for your system to find and replace the bad memory chips.

➢ MEMORY SIZE HAS CHANGED SINCE LAST BOOT

Memory has been added or removed since the last boot. In EISA mode, use Configuration Utility to re-configure the memory configuration. In ISA mode, enter Setup and enter the new memory size in the memory fields.

- MP 586IVX
- Memory Verify Error at ...

Indicates an error verifying a value already written to memory. Use the location along with your system's memory map to locate the bad chip.

OFFENDING ADDRESS NOT FOUND

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem cannot be isolated.

OFFENDING SEGMENT:

This message is used in conjunction with the I/O CHANNEL CHECK and RAM PARITY ERROR messages when the segment that has caused the problem has been isolated.

PRESS A KEY TO REBOOT

This will be displayed at the bottom screen when an error occurs that requires you to reboot. Press any key and the system will reboot.

- PRESS F1 TO DISABLE NMI, F2 TO REBOOT When BIOS detects a Non-maskable Interrupt condition during boot, this will allow you to disable the NMI and continue to boot. Or you can reboot the system, which will enable the NMI.
- RAM PARITY ERROR CHECKING FOR SEGMENT ... Indicates a parity error in Random Access Memory.
- Should Be Empty But EISA Board Found

PLEASE RUN EISA CONFIGURATION UTILITY A valid board ID was found in a slot that was configured as having no board ID.

- When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.
- Should Have EISA Board But Not Found

PLEASE RUN EISA CONFIGURATION UTILITY

The board installed is not responding to the ID request, or no board ID has been found in the indicated slot.

- When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.
- Slot Not Empty

Indicates that a slot designated as empty by the EISA Configuration Utility actually contains a board.

- When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.
- SYSTEM HALTED, (CTRL-ALT-DEL) TO REBOOT ...

Indicates the present boot attempt has been aborted and the system must be rebooted. Press and hold down the CTRL and ALT keys and press DEL.

> Wrong Board In Slot

PLEASE RUN EISA CONFIGURATION UTILITY

The board ID does not match the ID stored in the EISA non-volatile memory.

When this error appears, the system will boot in ISA mode, which allows you to run the EISA Configuration Utility.

# **APPENDIX B: POST CODES**

EISA POST codes are typically output to port address 300h. ISA POST codes are typically output to port address 80h.

| POST | Name                                         | Description                                                                                                                                                     |  |
|------|----------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
| C0   | Turn Off Chipset<br>Cache                    | OEM Specific-Cache control.                                                                                                                                     |  |
| 1    | Processor Test 1                             | Processor Status (1 FLAGS) Verification.                                                                                                                        |  |
|      |                                              | Test the following processor status flags                                                                                                                       |  |
|      |                                              | carry, zero, sign, overflow,                                                                                                                                    |  |
|      |                                              | The BIOS will set each of these flags, verify they are set, then turn each flag off and verify it is off.                                                       |  |
| 2    | Processor Test 2                             | Read/Write/Verify all CPU registers except SS, SP, and BP with data pattern FF and 00.                                                                          |  |
| 3    | Initialize Chips                             | Disable NMI, PIE, AIE, UEI, SQWV.                                                                                                                               |  |
|      |                                              | Disable video, parity checking, DMA.                                                                                                                            |  |
|      |                                              | Reset math coprocessor.                                                                                                                                         |  |
|      |                                              | Clear all page registers, CMOS shutdown byte.                                                                                                                   |  |
|      |                                              | Initialize timer 0, 1, and 2, including set EISA timer to a known state.                                                                                        |  |
|      |                                              | Initialize DMA controllers 0 and 1.                                                                                                                             |  |
|      |                                              | Initialize interrupt controllers 0 and 1.                                                                                                                       |  |
|      |                                              | Initialize EISA extended registers.                                                                                                                             |  |
| 4    | Test Memory<br>Refresh Toggle                | RAM must be periodically refreshed in order to keep<br>the memory from decaying. This function assures<br>that the memory refresh function is working properly. |  |
| 5    | Blank video,<br>Initialize keyboard          | Keyboard controller initialization.                                                                                                                             |  |
| 6    | Reserved                                     |                                                                                                                                                                 |  |
| 7    | Test CMOS<br>Interface and<br>Battery Status | Verifies CMOS is working correctly, detects bad battery.                                                                                                        |  |
| BE   | Chipset Default<br>Initialization            | Program chipset registers with power on BIOS defaults.                                                                                                          |  |
| C1   | Memory presence<br>test                      | OEM Specific-Test to size on-board memory.                                                                                                                      |  |
| C5   | Early Shadow                                 | OEM Specific-Early Shadow enable for fast boot.                                                                                                                 |  |
| C6   | Cache presence test                          | External cache size detection.                                                                                                                                  |  |
| 8    | Setup low memory                             | Early chip set initialization.                                                                                                                                  |  |

|       | 1                                           | ſ                                                                                                                    |
|-------|---------------------------------------------|----------------------------------------------------------------------------------------------------------------------|
|       |                                             | Memory presence test.                                                                                                |
|       |                                             | OEM chip set routines.                                                                                               |
|       |                                             | Clear low 64 K of memory.                                                                                            |
|       |                                             | Test first 64 K memory.                                                                                              |
| 9     | Early Cache                                 | Cyrix CPU initialization.                                                                                            |
|       | Initialization                              | Cache initialization.                                                                                                |
| A     | Setup Interrupt<br>Vector Table             | Initialize first 120 interrupt vectors with<br>SPURIOUS_INT-HDLR and initialize INT 00h-1Fh<br>according to INT_TBL. |
| В     | Test CMOS RAM<br>Checksum                   | Test CMOS RAM Checksum, if bad, or insert key<br>pressed, load defaults.                                             |
| С     | Initialize keyboard                         | Detect type of keyboard controller (optional).                                                                       |
|       |                                             | Set NUM_LOCK status.                                                                                                 |
| D     | Initialize Video                            | Detect CPU clock.                                                                                                    |
|       | Interface                                   | Read CMOS location 14h to find out type of video in use.                                                             |
|       |                                             | Detect and Initialize Video Adapter.                                                                                 |
| Е     | Test Video<br>Memory                        | Test video memory, write sign-on message to screen.                                                                  |
|       |                                             | Setup shadow RAM - Enable shadow according to<br>Setup.                                                              |
| F     | Test DMA                                    | BIOS checksum test.                                                                                                  |
|       | Controller 0                                | Keyboard detect and initialization.                                                                                  |
| 10    | Test DMA<br>Controller 1                    |                                                                                                                      |
| 11    | Test DMA Page<br>registers                  | Test DMA Page Registers.                                                                                             |
| 12-13 | Reserved                                    |                                                                                                                      |
| 14    | Test Timer<br>Counter 2                     | Test 8254 Timer 0 Counter 2.                                                                                         |
| 15    | Test 8259-1 Mask<br>Bits                    | Verify 8259 Channel 1 masked interrupts by alternately turning off and on the interrupt lines.                       |
| 16    | Test 8259-2 Mask<br>Bits                    | Verify 8259 Channel 2 masked interrupts by alternately turning off and on the interrupt lines.                       |
| 17    | Test Stuck 8259's<br>Interrupt Bits         | Turn off interrupts then verify no interrupt mask register is on.                                                    |
| 18    | Test 8259 Interrupt<br>Functionality        | Force an interrupt and verify the interrupt occurred.                                                                |
| 19    | Test Stuck NMI<br>Bits (Parity/IO<br>Check) | Verify NMI can be cleared.                                                                                           |

| -     |                                                 |                                                                                                                                                                                           |
|-------|-------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 1A    |                                                 | Display CPU clock.                                                                                                                                                                        |
| 1B-1E | Reserved                                        |                                                                                                                                                                                           |
| 1F    | Set EISA Mode                                   | If EISA non-volatile memory checksum is good,<br>execute EISA initialization. If not, execute ISA tests<br>an clear EISA mode flag.<br>Test EISA Configuration Memory Integrity (checksum |
|       |                                                 | & communication interface).                                                                                                                                                               |
| 20    | Enable Slot 0                                   | Initialize slot 0 (System Board).                                                                                                                                                         |
| 21-2F | Enable Slots 1-15                               | Initialize slot 1 through 15.                                                                                                                                                             |
| 30    | Size Base and<br>Extended Memory                | Size base memory from 256 K to 640 K extended memory above 1 MB.                                                                                                                          |
| 31    | Test Base and<br>Extended Memory                | Test base memory from 256 K to 640 K and extended memory above 1 MB using various patterns.                                                                                               |
|       |                                                 | This will be skipped in EISA mode and can be<br>"skipped" with ESC key in ISA mode.                                                                                                       |
| 32    | Test EISA<br>Extended Memory                    | If EISA Mode flag is set then test EISA memory<br>found in slots initialization.                                                                                                          |
|       |                                                 | This will be skipped in ISA mode and can be<br>"skipped" with ESC key in EISA mode.                                                                                                       |
| 33-3B | Reserved                                        |                                                                                                                                                                                           |
| 3C    | Setup Enabled                                   |                                                                                                                                                                                           |
| 3D    | Initialize & Install<br>Mouse                   | Detect if mouse is present, initialize mouse, install interrupt vectors.                                                                                                                  |
| 3E    | Setup Cache<br>Controller                       | Initialize cache controller.                                                                                                                                                              |
| 3F    | Reserved                                        |                                                                                                                                                                                           |
| BF    | Chipset<br>Initialization                       | Program chipset registers with Setup values.                                                                                                                                              |
| 40    |                                                 | Display virus protest disable or enable.                                                                                                                                                  |
| 41    | Initialize Floppy<br>Drive & Controller         | Initialize floppy disk drive controller and any drives.                                                                                                                                   |
| 42    | Initialize Hard<br>Drive & Controller           | Initialize hard drive controller and any drives.                                                                                                                                          |
| 43    | Detect & Initialize<br>Serial/Parallel<br>Ports | Initialize any serial and parallel ports (also game port).                                                                                                                                |
| 44    | Reserved                                        |                                                                                                                                                                                           |
| 45    | Detect & Initialize<br>Math Coprocessor         | Initialize math coprocessor.                                                                                                                                                              |
| 46    | Reserved                                        |                                                                                                                                                                                           |

Appendix B: Post Codes

| 47    | Reserved                                          |                                                                                                                                                                    |  |  |  |  |
|-------|---------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|--|--|--|
| 48-4D | Reserved                                          |                                                                                                                                                                    |  |  |  |  |
| 4E    | Manufacturing<br>POST Loop or<br>Display Messages | Reboot if Manufacturing POST Loop pin is set.<br>Otherwise display any messages (i.e., any non-fatal<br>errors that were detected during POST) and enter<br>Setup. |  |  |  |  |
| 4F    | Security Check                                    | Ask password security (optional).                                                                                                                                  |  |  |  |  |
| 50    | Write CMOS                                        | Write all CMOS values back to RAM and clear screen.                                                                                                                |  |  |  |  |
| 51    | Pre-boot Enable                                   | Enable parity checker.                                                                                                                                             |  |  |  |  |
|       |                                                   | Enable NMI, Enable cache before boot.                                                                                                                              |  |  |  |  |
| 52    | Initialize Option<br>ROMs                         | Initialize any option ROMs present from C8000h to EFFFFh.                                                                                                          |  |  |  |  |
|       |                                                   | When FSCAN option is enabled, will initialize from C8000h to F7FFFh.                                                                                               |  |  |  |  |
| 53    | Initialize Time<br>Value                          | Initialize time value in 40h: BIOS area.                                                                                                                           |  |  |  |  |
| 60    | Setup Virus<br>Protect                            | Setup virus protect according to Setup                                                                                                                             |  |  |  |  |
| 61    | Set Boot Speed                                    | Set system speed for boot                                                                                                                                          |  |  |  |  |
| 62    | Setup NumLock                                     | Setup NumLock status according to Setup                                                                                                                            |  |  |  |  |
| 63    | Boot Attempt                                      | Set low stack.                                                                                                                                                     |  |  |  |  |
|       |                                                   | Boot via INT 19h.                                                                                                                                                  |  |  |  |  |
| B0    | Spurious                                          | If interrupt occurs in protected mode.                                                                                                                             |  |  |  |  |
| B1    | Unclaimed NMI                                     | If unmasked NMI occurs, display                                                                                                                                    |  |  |  |  |
|       |                                                   | Press F1 to disable NMI, F2 reboot.                                                                                                                                |  |  |  |  |
| E1-EF | Setup Pages                                       | E1 - Page 1, E2 - Page 2, etc.                                                                                                                                     |  |  |  |  |
| FF    | Boot                                              |                                                                                                                                                                    |  |  |  |  |

# APPENDIX C: BIOS DEFAULT DRIVE TABLE

| Туре | Size<br>(MB) | Cylinders | Heads | Sectors | Write /<br>Precomp | Land<br>Zone | Example Model            |  |
|------|--------------|-----------|-------|---------|--------------------|--------------|--------------------------|--|
| 1    | 10 MB        | 306       | 4     | 17      | 128                | 305          | TEAC SD510               |  |
|      |              |           |       |         |                    |              | MMI 112, 5412            |  |
| 2    | 20 MB        | 615       | 4     | 17      | 300                | 615          | Seagate ST225,<br>ST4026 |  |
| 3    | 31 MB        | 615       | 6     | 17      | 300                | 615          |                          |  |
| 4    | 62 MB        | 940       | 8     | 17      | 512                | 940          |                          |  |
| 5    | 47 MB        | 940       | 6     | 17      | 512                | 940          |                          |  |
| 6    | 20 MB        | 615       | 4     | 17      | 65535              | 615          | Seagate ST125            |  |
|      |              |           |       |         |                    |              | Tandon TM262             |  |
| 7    | 31 MB        | 462       | 8     | 17      | 256                | 511          |                          |  |
| 8    | 30 MB        | 733       | 5     | 17      | 65535              | 733          | Tandon TM703             |  |
| 9    | 112 MB       | 900       | 15    | 17      | 65535              | 901          |                          |  |
| 10   | 20 MB        | 820       | 3     | 17      | 65535              | 820          |                          |  |
| 11   | 35 MB        | 855       | 5     | 17      | 65535              | 855          |                          |  |
| 12   | 50 MB        | 855       | 7     | 17      | 65535              | 855          |                          |  |
| 13   | 20 MB        | 306       | 8     | 17      | 128                | 319          | Disctron526,             |  |
|      |              |           |       |         |                    |              | MMI M125                 |  |
| 14   | 43 MB        | 733       | 7     | 17      | 65535              | 733          |                          |  |
| 16   | 20 MB        | 612       | 4     | 17      | 0                  | 663          | Microscience<br>HH725    |  |
|      |              |           |       |         |                    |              | Syquest3250,<br>3425     |  |
| 17   | 41 MB        | 977       | 5     | 17      | 300                | 977          |                          |  |
| 18   | 57 MB        | 977       | 7     | 17      | 65535              | 977          |                          |  |
| 19   | 60 MB        | 1024      | 7     | 17      | 512                | 1023         |                          |  |
| 20   | 30 MB        | 733       | 5     | 17      | 300                | 732          |                          |  |
| 21   | 43 MB        | 733       | 7     | 17      | 300                | 732          |                          |  |
| 22   | 30 MB        | 733       | 5     | 17      | 300                | 733          | Seagate ST4038           |  |
| 23   | 10 MB        | 306       | 4     | 17      | 0                  | 336          | 200300011000             |  |
| 24   | 54 MB        | 925       | 7     | 17      | 0                  | 925          | Seagate ST4051           |  |
| 25   | 69 MB        | 925       | 9     | 17      | 65535              | 925          | Seagate ST4096           |  |
| 26   | 44 MB        | 754       | 7     | 17      | 754                | 754          | Maxtor2085               |  |
| 27   | 69 MB        | 754       | 11    | 17      | 65535              | 754          | Maxtor2140,              |  |
|      |              |           |       |         |                    |              | Priam S14                |  |
| 28   | 41 MB        | 699       | 7     | 17      | 256                | 699          | Maxtor2190,              |  |
| -    |              |           |       |         |                    |              | Priam S19                |  |
| 29   | 68 MB        | 823       | 10    | 17      | 65535              | 823          | Maxtor1085               |  |
|      |              |           |       |         |                    |              | Micropolis1325           |  |
| 30   | 53 MB        | 918       | 7     | 17      | 918                | 918          | Maxtor1105.              |  |

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# Appendix C: BIOS Default Drive Table

|      |        | 1    |    |    | 1     |      | 1120, 4780             |  |
|------|--------|------|----|----|-------|------|------------------------|--|
|      |        |      |    |    |       |      |                        |  |
| 31   | 94 MB  | 1024 | 11 | 17 | 65535 | 1024 | Maxtor1170             |  |
| 32   | 128 MB | 1024 | 15 | 17 | 65535 | 1024 | CDC9415                |  |
| 33   | 43 MB  | 1024 | 5  | 17 | 1024  | 1024 |                        |  |
| 34   | 10 MB  | 612  | 2  | 17 | 128   | 612  |                        |  |
| 35   | 77 MB  | 1024 | 9  | 17 | 65535 | 1024 |                        |  |
| 36   | 68 MB  | 1024 | 8  | 17 | 512   | 1024 |                        |  |
| 37   | 41 MB  | 615  | 8  | 17 | 128   | 615  |                        |  |
| 38   | 25 MB  | 987  | 3  | 17 | 987   | 987  |                        |  |
| 39   | 57 MB  | 987  | 7  | 17 | 987   | 987  | Maxtor1140,<br>4380    |  |
| 40   | 41 MB  | 820  | 6  | 17 | 820   | 820  | Seagate ST251          |  |
| 41   | 41 MB  | 977  | 5  | 17 | 977   | 977  | Seagate ST4053         |  |
|      |        |      |    |    |       |      | Miniscribe3053/        |  |
|      |        |      |    |    |       |      | 6053                   |  |
| 42   | 41 MB  | 981  | 5  | 17 | 981   | 981  | Miniscribe3053/        |  |
|      |        |      |    |    |       |      | 6053 RLL               |  |
| 43   | 48 MB  | 830  | 7  | 17 | 512   | 830  | Miniscribe 3650        |  |
| 44   | 69 MB  | 830  | 10 | 17 | 65535 | 830  | Miniscribe 3650<br>RLL |  |
| 45   | 114 MB | 917  | 15 | 17 | 65535 | 918  | Conner CP3104          |  |
| 46   | 152 MB | 1224 | 15 | 17 | 65535 | 1223 | Conner CP3204          |  |
| User |        |      |    |    |       |      |                        |  |

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# **APPENDIX D: PROBLEM SHEET**

| 1. Customer Data                      |                 |            |          |          |               |             |  |  |  |  |
|---------------------------------------|-----------------|------------|----------|----------|---------------|-------------|--|--|--|--|
| Name                                  |                 |            | Tel. No. |          |               |             |  |  |  |  |
| Address                               |                 |            |          | Fax. No. |               |             |  |  |  |  |
|                                       |                 |            |          |          | Purchase Date |             |  |  |  |  |
| 2. Mainboard Date                     |                 |            |          |          |               |             |  |  |  |  |
| Model NO.                             | GA-             |            |          |          | Rev. No.      |             |  |  |  |  |
| Serial No.                            |                 |            |          |          |               |             |  |  |  |  |
| 3. System Config                      | uration         |            |          |          |               |             |  |  |  |  |
| CPU Type:                             |                 |            |          |          |               |             |  |  |  |  |
| CPU Brand:                            |                 |            |          |          |               |             |  |  |  |  |
| CPU Speed:                            |                 |            |          |          |               |             |  |  |  |  |
| DRAM Type:                            | <b>1</b>        | 2          | □ 4      | □ 8      | <b>□</b> 16   | 32 MB       |  |  |  |  |
| DRAM Speed:                           | □ 80            | <b>7</b> 0 | 🛛 60 ns  |          |               |             |  |  |  |  |
| DRAM Total Size:                      |                 | MB         |          |          |               |             |  |  |  |  |
| DRAM Brand:                           |                 |            |          |          |               |             |  |  |  |  |
| SRAM Size:                            | 🛛 64KB          | 🖬 128 KB   | 🗆 256 KB |          | 🖬 512 KB      |             |  |  |  |  |
| SRAM Part No.                         | TAG:            |            |          |          | DATA:         |             |  |  |  |  |
| Video Card:                           |                 |            |          |          |               |             |  |  |  |  |
| Video Chip or Brar                    | nd:             |            |          |          |               |             |  |  |  |  |
| Floppy Drive A Ca                     | pacity & Brand: |            |          |          |               |             |  |  |  |  |
| Floppy Drive B Capacity & Brand:      |                 |            |          |          |               |             |  |  |  |  |
| Storage Controller                    | Туре            | D MFM      | 🗆 RLL    | 🗆 IDE    | 🗆 EDSI        | SCSI        |  |  |  |  |
| Hard Drive C Brand & Type:            |                 |            |          |          |               |             |  |  |  |  |
| Hard Drive D Brand & Type:            |                 |            |          |          |               |             |  |  |  |  |
| LAN Controller Type:                  |                 |            |          |          |               |             |  |  |  |  |
| LAN Card Brand & Model:               |                 |            |          |          |               |             |  |  |  |  |
| Serial / Parallel Chip Brand & Model: |                 |            |          |          |               |             |  |  |  |  |
| Mouse Brand & Model:                  |                 |            |          |          |               |             |  |  |  |  |
| 0.S.                                  | DOS             | □ OS/2     | NETWARE  |          | UNIX / 1      | XENIX Ver.: |  |  |  |  |
| 4. AUTOEXEC.BAT & CONFIG.SYS File:    |                 |            |          |          |               |             |  |  |  |  |

5. Problem Description:

R-20-02-061125

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