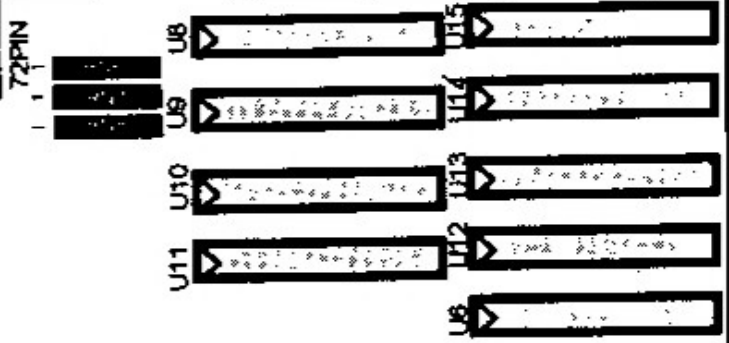
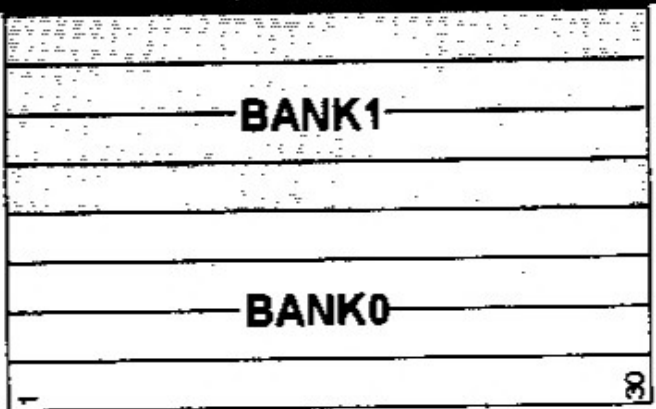


BANK 0/1
BANK 2/3

KEY-BOARD
CON-
NECTOR

BAT-
TERY

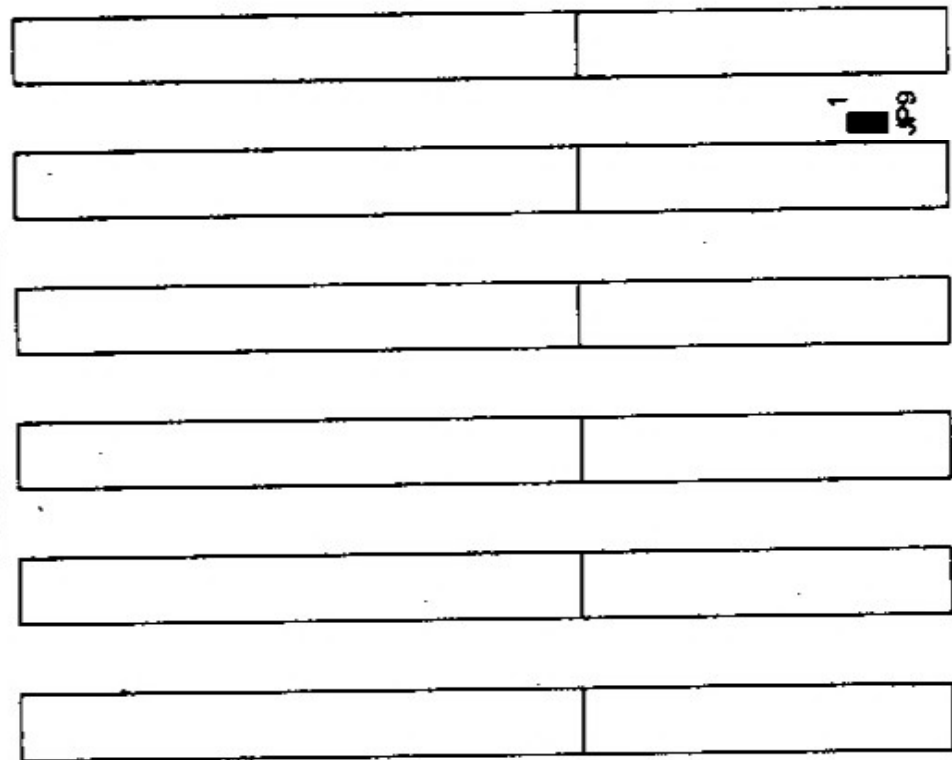
POWER
CONNECTOR



BIOS

493F
UMC

491F
UMC

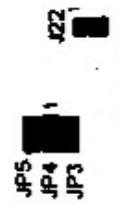
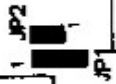


80486

Master/slave VESA 1

Master/ slave VESA 3

Master/slave VESA 2



J6



1

Reset Switch

- 1. GND
- 2. Power good

J22



1

50 MHz CPU Clock Setting Jumper

- 1-2 Default
- 2-3 For 50MHz

JP1-JP2: CPU Model Setup

JP3-JP5: CPU Clock Setup

(Please refer to the next page)

JP9



1

The Power 9000 Chipset Based VGA Cards Setting Jumper

- 1-2 Default
- 2-3 For power 9000

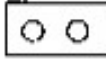
JP12



VL-Bus Clock Setup

- Open: <=33MHz
- Short: >33MHz

JP13



VL-Bus Wait State Setup

- Open=0WS
- Short=1WS (Default)

1-4 CPU TYPE AND CPU CLOCK REFERENCE TABLE

CPU	OSC
486SX-25	25MHz
486SX-33	33MHz
487SX-25	25MHz
487SX-33	33MHz
486DX-33	33MHz
486DX-40	40MHz
486DX-50	50MHz
486DX2-50	25MHz
486DX2-66	33MHz
OVERDRIVE-25	25MHz
OVERDRIVE-33	33MHz

This board is fitted with the clock generator which allows the user to choose a different CPU frequency just by changing the jumper setting. As a result, there is no more need for replacing the oscillator crystal. But you must set correct jumper setting before turning on the power and after replacing the different type CPU.

CPU CLOCK SETTING					
CPU CLOCK	JP3	JP4	JP5	JP12	J22
25MHz	SHORT	OPEN	OPEN	OPEN	1-2
33MHz	SHORT	SHORT	SHORT	OPEN	1-2
40MHz	SHORT	SHORT	OPEN	SHORT	1-2
50MHz	OPEN	OPEN	SHORT	SHORT	2-3

CPU MODEL SETTING		
	JP1	JP2
486SX	2-3	OPEN
486DX/DX2	1-2,3-4	1-2
487SX/ODP	1-2,3-4	2-3