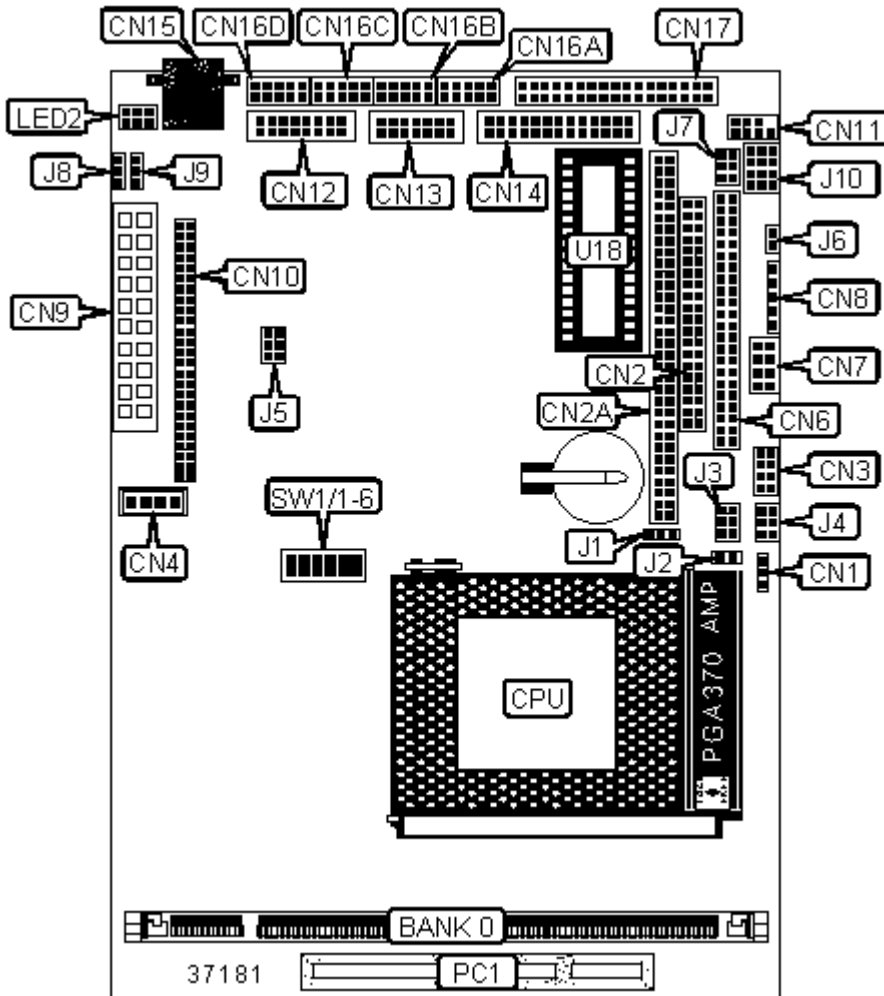


# EMAC, INC.

PCM-6890

<b>Device Type</b>	Single Board Computer
<b>Processor</b>	Celeron
<b>Processor Speed</b>	300/333/366/400/433MHz
<b>Chip Set</b>	Intel 440BX
<b>Video Chip Set</b>	Chips and Technology
<b>Audio Chip Set</b>	Creative Labs
<b>Maximum Onboard Memory</b>	128MB (SDRAM supported)
<b>Maximum Video Memory</b>	2MB
<b>Cache</b>	0/128KB (located on the Celeron CPU)
<b>BIOS</b>	Award
<b>Dimensions</b>	203mm x 146mm
<b>I/O Options</b>	32-bit PCI slot, ATX power connector, Audio in - CD-ROM, Ethernet 10BaseT connector, Flat panel connector, Floppy drive interface, IDE interface, IR connector, Parallel interface, PC/104 connectors (2), PS/2 mouse/AT keyboard interface, Serial interfaces (4), Solid-state flash disk socket, Sound connector, System monitor interface, USB interface, VGA connector



## CONNECTIONS

Purpose	Location	Purpose	Location
CPU fan power	CN1	Parallel interface	CN14

16-bit PC/104 connector	CN2	10BaseT Ethernet connector	CN15
8-bit PC/104 connector	CN2A	Serial interface 1	CN16A
USB interface	CN3	Serial interface 2	CN16B
Audio in - CD-ROM	CN4	Serial interface 3	CN16C
IDE interface	CN6	Serial interface 4	CN16D
IDE interface LED	CN7/Pins 1 & 2	Floppy drive interface	CN17
Speaker	CN7/Pins 3 - 5	System monitor interface	J2
Reset switch	CN7/Pins 7 & 8	Power switch	J6
IR connector	CN8	Ethernet Rx LED connector	LED2/Pins 1 & 2
ATX power connector	CN9	Ethernet Link LED connector	LED2/Pins 3 & 4
Flat panel connector	CN10	Ethernet Tx LED connector	LED2/Pins 5 & 6
PS/2 mouse/AT keyboard interface	CN11	32-bit PCI slot	PC1
VGA connector	CN12	Solid-state flash disk (DOC) socket	U18
Sound connector	CN13		
Note: Pin 1 locations of CN7 & LED2 are unidentified.			

### USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	J1	Pins 1 & 2 closed
	CMOS memory clear	J1	Pins 2 & 3 closed
»	Audio output amplified	J5	Pins 3 & 5, 4 & 6 closed
	Audio output not amplified	J5	Pins 1 & 3, 2 & 4 closed
»	Flat panel clock signal select SHF	J8	Pins 1 & 2 closed
	Flat panel clock signal select ASHF	J8	Pins 2 & 3 closed
»	Flat panel connector voltage select 3.3V	J9	Pins 2 & 3 closed
	Flat panel connector voltage select 5V	J9	Pins 1 & 2 closed
»	Factory configured - do not alter	SW1/1	Unidentified

Note: Pin 1 locations of J1, J5, J8 & J9 are unidentified. Switch 1 location on SW1 is unidentified

### DIMM CONFIGURATION

Size	Bank 0
16MB	(1) 2M x 64
32MB	(1) 4M x 64
64MB	(1) 8M x 64
128MB	(1) 16M x 64

Note: Board supports SDRAM memory.

### CPU MULTIPLIER SELECTION

Multiplier	SW1/4	SW1/5	SW1/6
2.0x	On	Off	Off
2.5x	On	Off	On
3.0x	On	On	Off
3.5x	On	On	On
4.0x	Off	Off	Off
4.5x	Off	Off	On
5.0x	Off	On	Off
» 5.5x	Off	On	On

Note: Switch 1 location of SW1 is unidentified.

### SERIAL INTERFACE 2 SELECTION

Setting	J7	J10
» RS-232	Pins 1 & 2 closed	Pins 1 & 2, 4 & 5, 7 & 8, 10 & 11 closed
RS-422	Pins 3 & 4 closed	Pins 2 & 3, 5 & 6, 8 & 9, 11 & 12 closed
RS-485	Pins 5 & 6 closed	Pins 2 & 3, 5 & 6, 8 & 9, 11 & 12 closed

Note: Pin 1 locations of J7 & J10 are unidentified. All pins should be open unless designated as closed.

**SERIAL INTERFACE 3 VOLTAGE SELECTION**

	Setting	J4/Pins 1 & 2	J4/Pins 3 & 4	J4/Pins 5 & 6
»	RI	Open	Open	Closed
	+5V	Open	Closed	Open
	+12V	Closed	Open	Open

Note: Pin 1 location of J4 is unidentified.

**SERIAL INTERFACE 4 VOLTAGE SELECTION**

	Setting	J3/Pins 1 & 2	J3/Pins 3 & 4	J3/Pins 5 & 6
»	RI	Open	Open	Closed
	+5V	Open	Closed	Open
	+12V	Closed	Open	Open

Note: Pin 1 location of J3 is unidentified.

**SOLID-STATE FLASH DISK (DOC) ADDRESS SELECTION**

	Address	SW1/2	SW1/3
	D400	Off	Off
»	D800	On	Off
	DC00	Off	On
	Disable	On	On

Note: Switch 1 location of SW1 is unidentified.

**MISCELLANEOUS TECHNICAL NOTES**

Solid-state flash disk socket supports (DOC 2000 & 1000 series) devices from 2MB to 144MB.