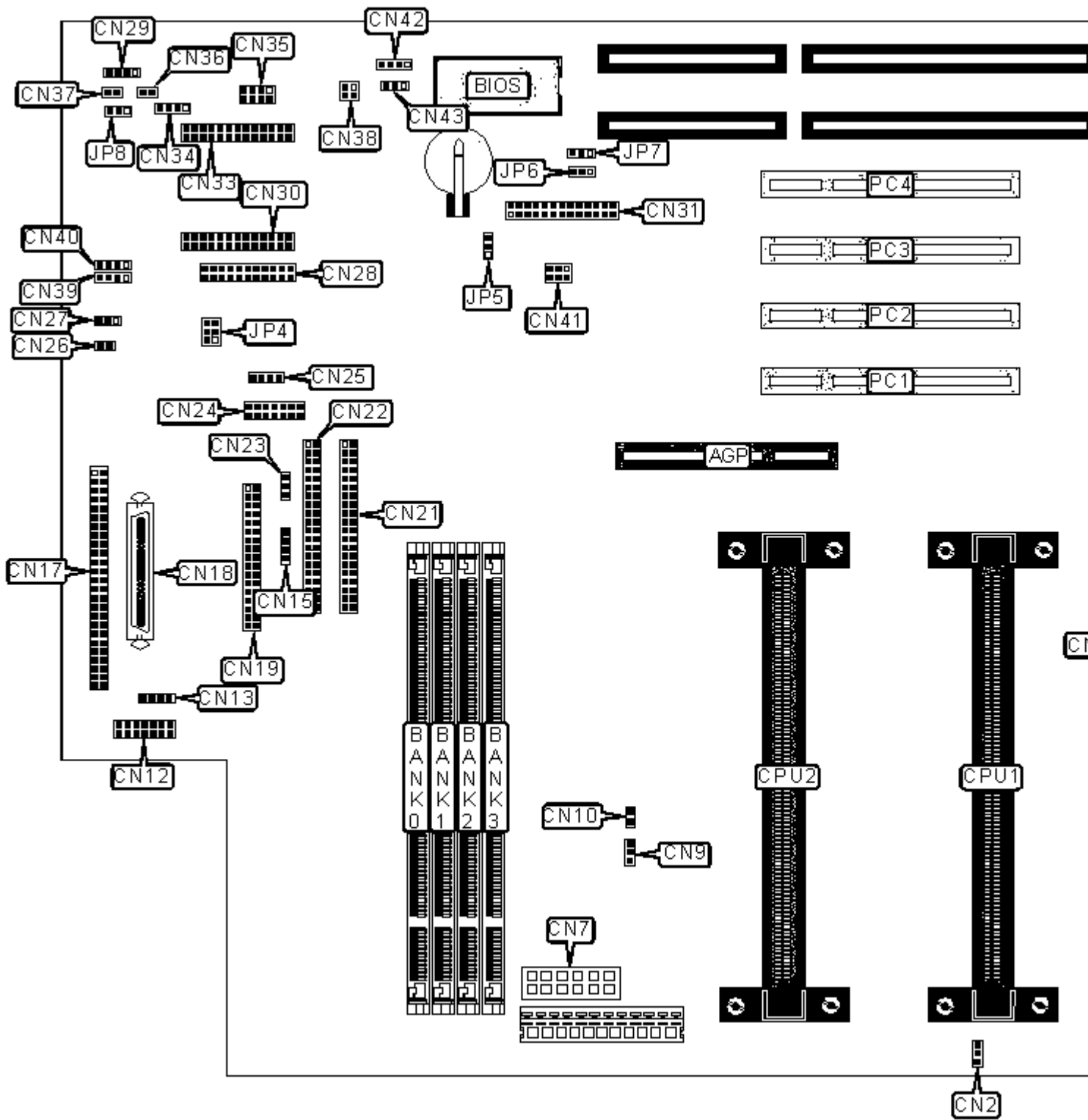


ACER, INC.

ACERALTOS 1100 (M19A)

Device Type	Mainboard
Processor	Pentium II (2)
Processor Speed	333/350/400/450MHz
Chip Set	Intel 440BX
Maximum Onboard Memory	512MB (SDRAM supported)
Cache	256/512KB (located on the Pentium II CPU)
BIOS	Acer
Dimensions	Unidentified
I/O Options	32-bit PCI slots (4), Ethernet 10BaseT connector, floppy drive interface, IDE interfaces (2), SCSI interface, Wide SCSI interface, parallel port, PS/2 mouse port, serial ports (2), Feature connector, USB ports (2), AGP slot, Wake-on-LAN connector, Wake-on-modem connector



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	Housing fan power 3	CN23
Parallel port	CN1	RDM PS status connector	CN24
SPP control connector	CN2	Housing fan power 4	CN25
PS/2 mouse port	CN4	Hardware monitoring reset switch	CN26

USB connector 1	CN5	Power LED	CN27
Serial port 1	CN6	RAS testing connector	CN28
AUX power connector	CN7	IDE interface LED	CN29
USB connector 2	CN8	RDM connector	CN30
CPU2 fan power	CN9	Feature connector	CN31
CPU2 temperature connector	CN10	Wake-on-LAN connector	CN32
Serial port 2	CN11	RDM connector	CN33
LDD fail LED	CN12	Speaker	CN34
Housing fan power 1	CN13	PSU/IDE/Fan fail LED	CN35
RJ-45 unshielded twisted pair	CN14	Power switch	CN36
Housing fan power 2	CN15	Chassis intrusion connector	CN37
CPU1 temperature connector	CN16	RDM LED	CN38
SCSI interface	CN17	External IDE LED	CN39
Wide SCSI interface	CN18	External IDE LED	CN40
Floppy drive interface	CN19	PC/PCI connector for sound card	CN41
CPU1 fan power	CN20	Wake-on-modem connector	CN42
IDE interface 2	CN21	LAN panel LED	CN43
IDE interface 1	CN22	32-bit PCI slots	PC1 - PC4

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	LM80 only	JP4	Pins 2 & 3, 5 & 6 closed
	Alert pack and LM80	JP4	Pins 1 & 2, 4 & 5 closed
»	Factory configured - do not alter	JP5	Unidentified
»	BIOS logo selection EOM	JP6	Pins 2 & 3 closed
	BIOS logo selection ACER	JP6	Pins 1 & 2 closed
»	Password disabled	JP7	Pins 2 & 3 closed
	Password enabled	JP7	Pins 1 & 2 closed
»	Beep message from internal buzzer	JP8	Pins 1 & 2 closed

Beep message from speaker	JP8	Pins 2 & 3 closed
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DIMM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
32MB	(1) 4M x 64	None	None	None
64MB	(1) 8M x 64	None	None	None
64MB	(1) 4M x 64	(1) 4M x 64	None	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
128MB	(1) 16M x 64	None	None	None
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64	None
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
224MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64	None
256MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
416MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64
448MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64
512MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64

Note: Board supports SDRAM memory.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.

MISCELLANEOUS TECHNICAL NOTE

Note: If only one CPU is installed, then a termination board must be installed into the empty slot.