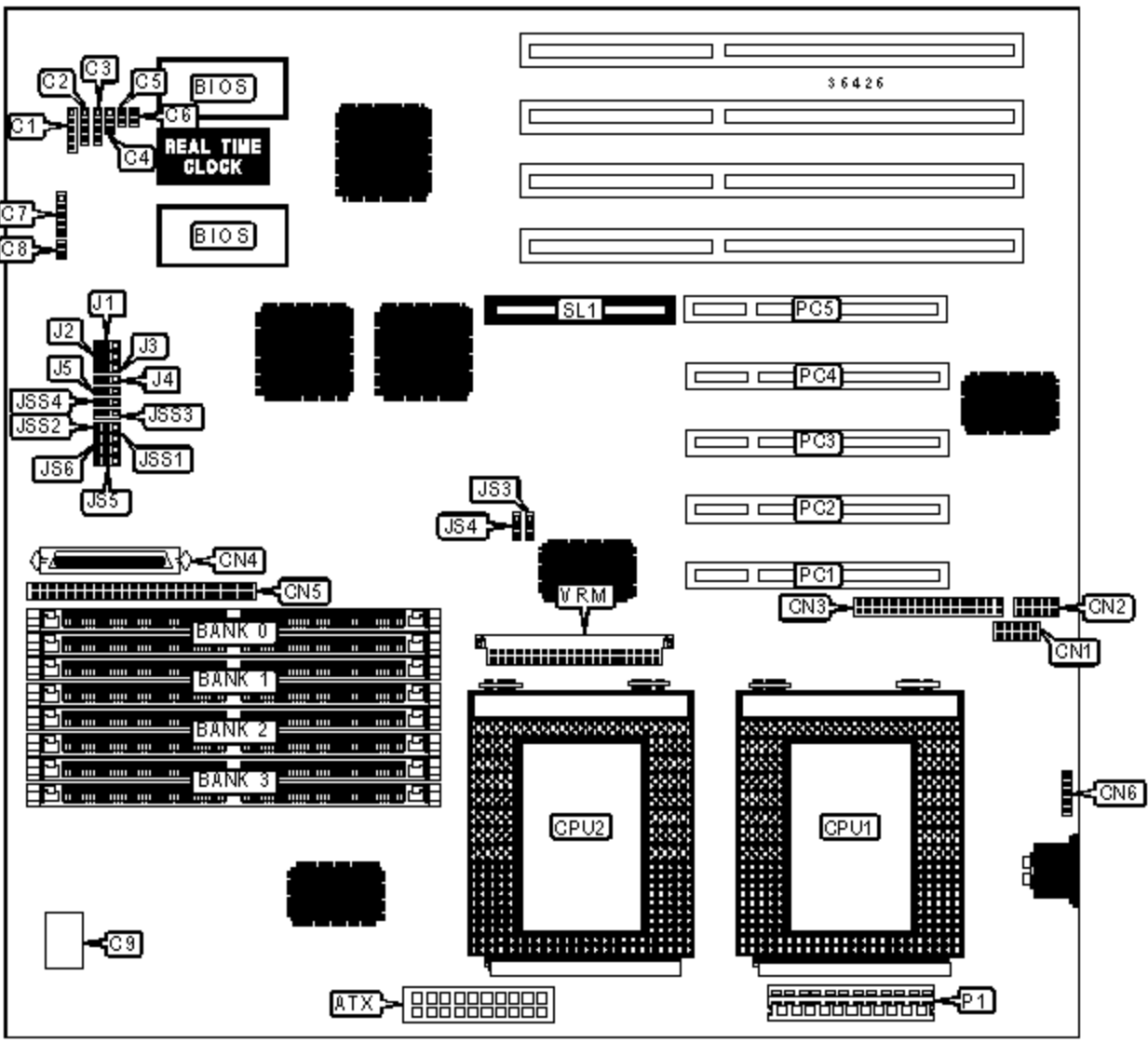


# KAM-TRONIC COMPUTER CO., LTD.

## P6NDP

<b>Device Type</b>	Mainboard
<b>Processor</b>	Pentium Pro
<b>Processor Speed</b>	150/180/200MHz
<b>Chip Set</b>	Intel 430FX
<b>Maximum Onboard Memory</b>	1GB (EDO supported)
<b>Cache</b>	256/512KB (located on Pentium Pro CPU)
<b>BIOS</b>	AMI
<b>Dimensions</b>	305mm x 305mm
<b>I/O Options</b>	32-bit PCI slots (5), floppy drive interface, green PC connector, Ultra Wide SCSI interface, Fast SCSI interface, PS/2 mouse interface, serial ports (2), IR connector, VRM connector, ATX power connector, RAID slot



CONNECTIONS			
Purpose	Location	Purpose	Location
ATX power connector	ATX	Serial port 1	CN2
Power LED & keylock	C1	Floppy drive interface	CN3
Speaker	C2	Ultra Wide SCSI interface	CN4

SCSI interface LED	C3	Fast SCSI interface	CN5
Turbo LED	C4	PS/2 mouse interface	CN6
Green PC connector	C5	5v power	P1
Reset switch	C6	32-bit PCI slots	PC1 - PC5
IR connector	C7	RAID slot	SL1
Soft off power supply	C8	VRM connector	VRM
Serial port 2	CN1		

### USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Password normal operation	J1	Pins 1 & 2 closed
	Password clear	J1	Pins 2 & 3 closed
»	Flash BIOS voltage select 12v	J2	Pins 1 & 2 closed
	Flash BIOS voltage select 5v	J2	Pins 2 & 3 closed
»	CMOS memory normal operation	J3	Pins 1 & 2 closed
	CMOS memory clear	J3	Pins 2 & 3 closed
»	Monitor type select color	J4	Pins 1 & 2 closed
	Monitor type select monochrome	J4	Pins 2 & 3 closed
»	SCSI high byte termination select always	J5	Pins 2 & 3 closed
	SCSI high byte termination select auto	J5	Pins 1 & 2 closed

### SIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(2) 1M x 36	None	None	None
16MB	(2) 2M x 36	None	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None	None

24MB	(2) 2M x 36	(2) 1M x 36	None	None
24MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	None
32MB	(2) 4M x 36	None	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None	None
32MB	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	None
64MB	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None	None
72MB	(2) 8M x 36	(2) 1M x 36	None	None
80MB	(2) 8M x 36	(2) 2M x 36	None	None
96MB	(2) 8M x 36	(2) 4M x 36	None	None
96MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	None
128MB	(2) 16M x 36	None	None	None
128MB	(2) 8M x 36	(2) 8M x 36	None	None
128MB	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
136MB	(2) 16M x 36	(2) 1M x 36	None	None
144MB	(2) 16M x 36	(2) 2M x 36	None	None

**SIMM CONFIGURATION (CON'T)**

Size	Bank 0	Bank 1	Bank 2	Bank 3
160MB	(2) 16M x 36	(2) 4M x 36	None	None
192MB	(2) 16M x 36	(2) 8M x 36	None	None
192MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	None
224MB	(2) 16M x 36	(2) 4M x 36	(2) 4M x 36	(2) 4M x 36
256MB	(2) 32M x 36	None	None	None
256MB	(2) 16M x 36	(2) 16M x 36	None	None
256MB	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36

272MB	(2) 16M x 36	(2) 16M x 36	(2) 1M x 36	(2) 1M x 36
280MB	(2) 32M x 36	(2) 1M x 36	(2) 1M x 36	(2) 1M x 36
288MB	(2) 16M x 36	(2) 16M x 36	(2) 2M x 36	(2) 2M x 36
304MB	(2) 32M x 36	(2) 2M x 36	(2) 2M x 36	(2) 2M x 36
320MB	(2) 16M x 36	(2) 16M x 36	(2) 4M x 36	(2) 4M x 36
384MB	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36	None
448MB	(2) 32M x 36	(2) 8M x 36	(2) 8M x 36	(2) 8M x 36
512MB	(2) 32M x 36	(2) 32M x 36	None	None
512MB	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36
640MB	(2) 32M x 36	(2) 16M x 36	(2) 16M x 36	(2) 16M x 36
768MB	(2) 32M x 36	(2) 32M x 36	(2) 32M x 36	None
1024MB	(2) 32M x 36	(2) 32M x 36	(2) 32M x 36	(2) 32M x 36
Note: Board accepts EDO memory.				

### CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium Pro CPU.

### CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JS3	JS4	JS5	JS6
150MHz	60MHz	2.5x	1 & 2	1 & 2	1 & 2	1 & 2
180MHz	60MHz	3x	1 & 2	1 & 2	1 & 2	1 & 2
200MHz	66MHz	3x	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

### CPU SPEED SELECTION (CON'T)

CPU speed	Clock speed	Multiplier	JSS1	JSS2	JSS3	JSS4
150MHz	60MHz	2.5x	1 & 2	1 & 2	1 & 2	2 & 3

180MHz	60MHz	3x	1 & 2	1 & 2	2 & 3	1 & 2
200MHz	66MHz	3x	1 & 2	1 & 2	2 & 3	2 & 3

Note: Pins designated should be in the closed position.