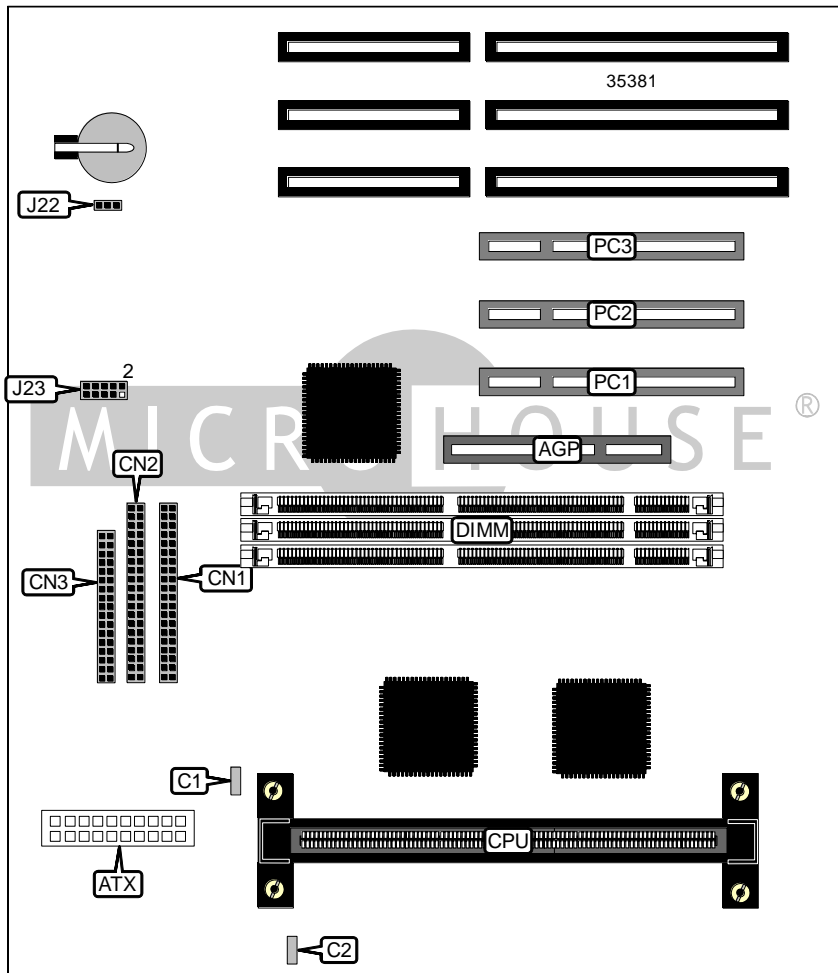


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HP BRIO 80XX, 81XX, 83XX (VER. 1)

Device Type	Mainboard
Processor	Pentium II
Processor Speed	233/266/300/333MHz
Chip Set	Unidentified
Video Chip Set	Unidentified
Maximum Onboard Memory	192MB (EDO supported)
Maximum Video Memory	2MB
Cache	256/512KB (located on Pentium II CPU)
BIOS	Unidentified
Dimensions	305mm x 244mm
I/O Options	32-bit PCI slots (3), floppy drive interface, IDE interfaces (2), ATX power connector, AGP slot
NPU Options	None



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 HP BRIO 80XX, 81XX, 83XX (VER. 1)

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CONNECTIONS			
Purpose	Location	Purpose	Location
AGP slot	AGP	IDE interface 2	CN1
ATX power connector	ATX	IDE interface 1	CN2
Power supply fan	C1	Floppy drive interface	CN3
Chassis fan power	C2	32-bit PCI slots	PC1 – PC3
Note: The type and location of the rear panel connectors are unidentified.			

USER CONFIGURABLE SETTINGS		
Function	Label	Position
CMOS memory normal operation	J22	Pins 1 & 2 closed
CMOS memory clear	J22	Pins 2 & 3 closed

DIMM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
16MB	(1) 2M x 64	None	None
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None
48MB	(1) 4M x 64	(1) 2M x 64	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
80MB	(1) 8M x 64	(1) 2M x 64	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
112MB	(1) 8M x 64	(1) 4M x 64	(1) 2M x 64
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
136MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64
144MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64
160MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64

CACHE CONFIGURATION
Note: 256KB/512KB cache is located on the Pentium II CPU.

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HP BRIO 80XX, 81XX, 83XX (VER. 1)

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CPU SPEED SELECTION			
CPU speed	Clock speed	Multiplier	J23
233MHz	66MHz	3.5x	1 & 2, 9 & 10
266MHz	66MHz	4x	5 & 6, 7 & 8, 9 & 10
300MHz	66MHz	4.5x	5 & 6, 9 & 10
333MHz	66MHz	5x	7 & 8, 9 & 10

Note: Pins designated should be in the closed position.