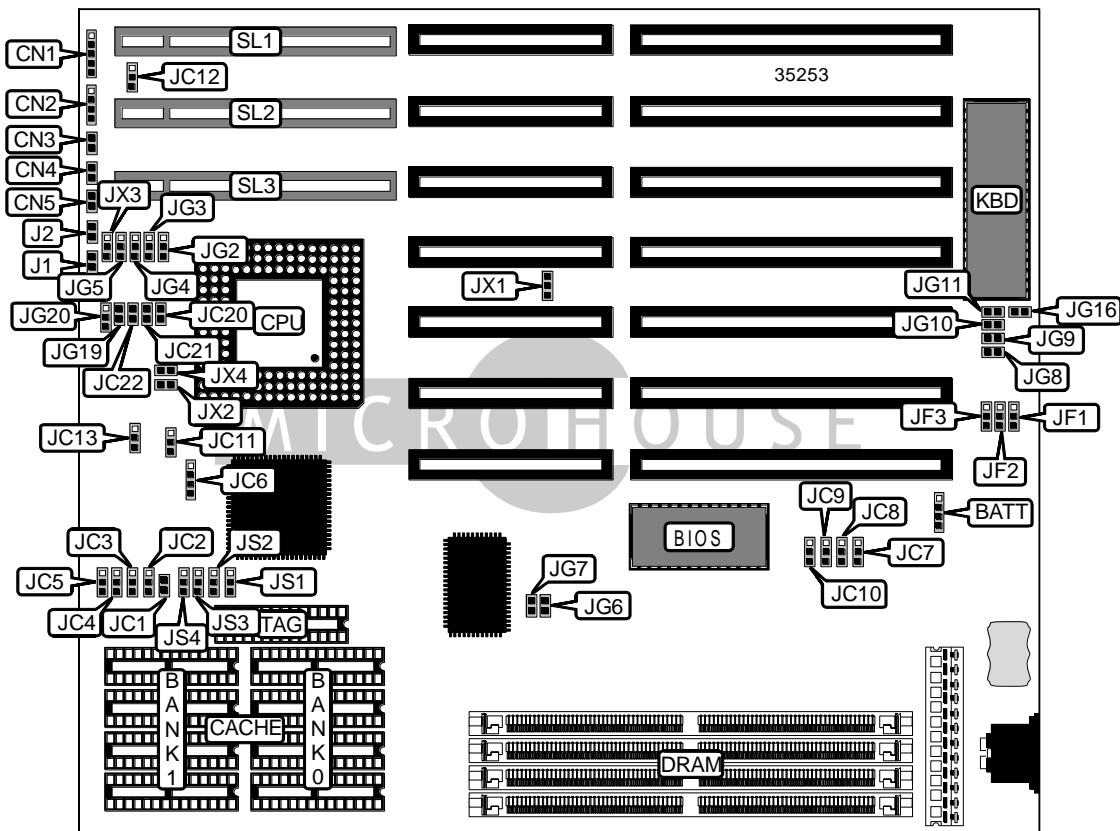


# MAGITRONICS

## A-BL2443G, A-BL2463, A-BL2483

|                               |  |
|-------------------------------|--|
| <b>Device Type</b>            | Mainboard  |
| <b>Processor</b>              | CX486/80486SX/SL80486SX/CX486S/CX486DX/AM486DXL/<br>80486DX/SL80486DX/CX486DX2/80486DX2/SL80486DX2/P24C/<br>P24T |
| <b>Processor Speed</b>        | 25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz  |
| <b>Chip Set</b>               | ALI  |
| <b>Video Chip Set</b>         | None   |
| <b>Maximum Onboard Memory</b> | 64MB   |
| <b>Maximum Video Memory</b>   | None   |
| <b>Cache</b>                  | 64/128/256KB   |
| <b>BIOS</b>                   | Unidentified   |
| <b>Dimensions</b>             | 254mm x 218mm  |
| <b>I/O Options</b>            | 32-bit VESA local bus slots (3), green PC connector  |
| <b>NPU Options</b>            | None   |



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| CONNECTIONS         |          |                             |           |
|---------------------|----------|-----------------------------|-----------|
| Purpose             | Location | Purpose                     | Location  |
| External battery    | BATT     | Wake on modem connector     | JG7       |
| Power LED & keylock | CN1      | Green PC connector          | JG8       |
| Speaker             | CN2      | Green PC connector          | JG9       |
| Reset switch        | CN3      | Green PC connector          | JG10      |
| Turbo switch        | CN4      | Green PC connector          | JG11      |
| Turbo LED           | CN5      | 32-bit VESA local bus slots | SL1 – SL3 |
| Green PC connector  | JG6      |                             |           |

| USER CONFIGURABLE SETTINGS          |       |                   |
|-------------------------------------|-------|-------------------|
| Function                            | Label | Position          |
| Battery type select internal        | BATT  | Pins 2 & 3 closed |
| Battery type select external        | BATT  | Open              |
| CMOS memory clear                   | BATT  | Pins 3 & 4 closed |
| í Factory configured - do not alter | JX1   | Unidentified      |

| SIMM CONFIGURATION |               |               |               |               |
|--------------------|---------------|---------------|---------------|---------------|
| Size               | Bank 0        | Bank 1        | Bank 2        | Bank 3        |
| 1MB                | (1) 256K x 36 | None          | None          | None          |
| 2MB                | (1) 256K x 36 | (1) 256K x 36 | None          | None          |
| 3MB                | (1) 256K x 36 | (1) 256K x 36 | (1) 256K x 36 | None          |
| 4MB                | (1) 1M x 36   | None          | None          | None          |
| 4MB                | (1) 256K x 36 | (1) 256K x 36 | (1) 256K x 36 | (1) 256K x 36 |
| 5MB                | (1) 1M x 36   | (1) 256K x 36 | None          | None          |
| 5MB                | (1) 256K x 36 | (1) 1M x 36   | None          | None          |
| 6MB                | (1) 1M x 36   | (1) 256K x 36 | (1) 256K x 36 | None          |
| 7MB                | (1) 1M x 36   | (1) 256K x 36 | (1) 256K x 36 | (1) 256K x 36 |
| 8MB                | (1) 1M x 36   | (1) 1M x 36   | None          | None          |
| 12MB               | (1) 2M x 36   | (1) 1M x 36   | None          | None          |
| 12MB               | (1) 1M x 36   | (1) 1M x 36   | (1) 1M x 36   | None          |
| 12MB               | (1) 1M x 36   | (1) 2M x 36   | None          | None          |
| 16MB               | (1) 4M x 36   | None          | None          | None          |
| 16MB               | (1) 2M x 36   | (1) 1M x 36   | (1) 1M x 36   | None          |
| 16MB               | (1) 2M x 36   | (1) 2M x 36   | None          | None          |
| 16MB               | (1) 1M x 36   | (1) 1M x 36   | (1) 1M x 36   | (1) 1M x 36   |
| 17MB               | (1) 4M x 36   | (1) 256K x 36 | None          | None          |
| 18MB               | (1) 4M x 36   | (1) 256K x 36 | (1) 256K x 36 | None          |

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| SIMM CONFIGURATION (CON'T) |             |               |               |               |
|----------------------------|-------------|---------------|---------------|---------------|
| Size                       | Bank 0      | Bank 1        | Bank 2        | Bank 3        |
| 19MB                       | (1) 4M x 36 | (1) 256K x 36 | (1) 256K x 36 | (1) 256K x 36 |
| 20MB                       | (1) 4M x 36 | (1) 1M x 36   | None          | None          |
| 20MB                       | (1) 2M x 36 | (1) 1M x 36   | (1) 1M x 36   | (1) 1M x 36   |
| 20MB                       | (1) 1M x 36 | (1) 2M x 36   | (1) 2M x 36   | None          |
| 24MB                       | (1) 4M x 36 | (1) 1M x 36   | (1) 1M x 36   | None          |
| 24MB                       | (1) 4M x 36 | (1) 2M x 36   | None          | None          |
| 24MB                       | (1) 2M x 36 | (1) 2M x 36   | (1) 2M x 36   | None          |
| 24MB                       | (1) 2M x 36 | (1) 4M x 36   | None          | None          |
| 28MB                       | (1) 4M x 36 | (1) 1M x 36   | (1) 1M x 36   | (1) 1M x 36   |
| 28MB                       | (1) 1M x 36 | (1) 2M x 36   | (1) 2M x 36   | (1) 2M x 36   |
| 32MB                       | (1) 4M x 36 | (1) 2M x 36   | (1) 2M x 36   | None          |
| 32MB                       | (1) 4M x 36 | (1) 4M x 36   | None          | None          |
| 32MB                       | (1) 2M x 36 | (1) 2M x 36   | (1) 2M x 36   | (1) 2M x 36   |
| 36MB                       | (1) 1M x 36 | (1) 4M x 36   | (1) 4M x 36   | None          |
| 40MB                       | (1) 4M x 36 | (1) 2M x 36   | (1) 2M x 36   | (1) 2M x 36   |
| 48MB                       | (1) 4M x 36 | (1) 4M x 36   | (1) 4M x 36   | None          |
| 52MB                       | (1) 1M x 36 | (1) 4M x 36   | (1) 4M x 36   | (1) 4M x 36   |
| 64MB                       | (1) 4M x 36 | (1) 4M x 36   | (1) 4M x 36   | (1) 4M x 36   |

| CACHE CONFIGURATION |             |             |             |
|---------------------|-------------|-------------|-------------|
| Size                | Bank 0      | Bank 1      | TAG         |
| 64KB                | (4) 8K x 8  | (4) 8K x 8  | (1) 8K x 8  |
| 128KB               | (4) 32K x 8 | None        | (1) 8K x 8  |
| 256KB               | (4) 32K x 8 | (4) 32K x 8 | (1) 32K x 8 |

| CACHE JUMPER CONFIGURATION |                   |                   |                   |                   |
|----------------------------|-------------------|-------------------|-------------------|-------------------|
| Size                       | JS1               | JS2               | JS3               | JS4               |
| 64KB                       | Pins 1 & 2 closed | Pins 1 & 2 closed | Pins 1 & 2 closed | Pins 1 & 2 closed |
| 128KB                      | Pins 2 & 3 closed | Pins 2 & 3 closed | Pins 1 & 2 closed | Pins 2 & 3 closed |
| 256KB                      | Pins 2 & 3 closed | Pins 1 & 2 closed | Pins 2 & 3 closed | Pins 2 & 3 closed |

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| CPU SPEED SELECTION |       |       |       |       |       |       |
|---------------------|-------|-------|-------|-------|-------|-------|
| Speed               | JF1   | JF2   | JF3   | JC10  | JC11  | JC13  |
| 25MHz               | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 |
| 33MHz               | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |
| 40MHz               | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| 50iMHz              | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 |
| 50MHz               | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| 66iMHz              | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |
| 75iMHz              | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 |
| 100iMHz             | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2 |

Note: Pins designated should be in the closed position.

| CPU TYPE SELECTION |        |       |       |       |       |              |
|--------------------|--------|-------|-------|-------|-------|--------------|
| Type               | JC1    | JC2   | JC3   | JC4   | JC5   | JC6          |
| CX486              | Open   | 1 & 2 | Open  | 2 & 3 | 2 & 3 | 2 & 3        |
| 80486SX            | Closed | 1 & 2 | Open  | 1 & 2 | Open  | 2 & 3        |
| SL80486SX          | Closed | 1 & 2 | Open  | 1 & 2 | Open  | 2 & 3        |
| CX486S             | Open   | 1 & 2 | Open  | 2 & 3 | 2 & 3 | 2 & 3        |
| CX486DX            | Open   | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2, 3 & 4 |
| AM486DXL           | Closed | 1 & 2 | 2 & 3 | 1 & 2 | Open  | 1 & 2, 3 & 4 |
| 80486DX            | Closed | 1 & 2 | 2 & 3 | 1 & 2 | Open  | 1 & 2, 3 & 4 |
| SL80486DX          | Closed | 1 & 2 | 2 & 3 | 1 & 2 | Open  | 1 & 2, 3 & 4 |
| CX486DX2           | Open   | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2, 3 & 4 |
| 80486DX2           | Closed | 1 & 2 | 2 & 3 | 1 & 2 | Open  | 1 & 2, 3 & 4 |
| SL80486DX2         | Closed | 1 & 2 | 2 & 3 | 1 & 2 | Open  | 1 & 2, 3 & 4 |
| P24C               | Closed | 1 & 2 | 2 & 3 | 1 & 2 | Open  | 1 & 2, 3 & 4 |
| P24T               | Closed | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 | 1 & 2, 3 & 4 |

Note: Pins designated should be in the closed position.

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| CPU TYPE SELECTION (CON'T) |       |       |       |        |        |        |
|----------------------------|-------|-------|-------|--------|--------|--------|
| Type                       | JC7   | JC8   | JC9   | JC20   | JX2    | JX4    |
| CX486                      | 2 & 3 | Open  | 2 & 3 | Open   | Closed | Closed |
| 80486SX                    | Open  | Open  | Open  | Open   | Closed | Closed |
| SL80486SX                  | Open  | Open  | Open  | Closed | Closed | Closed |
| CX486S                     | 2 & 3 | Open  | 2 & 3 | Open   | Closed | Closed |
| CX486DX                    | Open  | Open  | 2 & 3 | Open   | Closed | Closed |
| AM486DXL                   | Open  | Open  | Open  | Open   | Closed | Closed |
| 80486DX                    | Open  | Open  | Open  | Open   | Closed | Closed |
| SL80486DX                  | Open  | Open  | Open  | Closed | Closed | Closed |
| CX486DX2                   | Open  | Open  | 2 & 3 | Open   | Closed | Closed |
| 80486DX2                   | Open  | Open  | Open  | Open   | Closed | Closed |
| SL80486DX2                 | Open  | Open  | Open  | Closed | Closed | Closed |
| P24C                       | Open  | Open  | Open  | Open   | Open   | Open   |
| P24T                       | Open  | 2 & 3 | Open  | Open   | Closed | Closed |

Note: Pins designated should be in the closed position.

| CPU MULTIPLIER SELECTION (P24C ONLY) |                   |
|--------------------------------------|-------------------|
| Multiplier                           | JX3               |
| 2x                                   | Pins 2 & 3 closed |
| 3x                                   | Pins 1 & 2 closed |

| CPU SELECTION |       |        |        |       |       |
|---------------|-------|--------|--------|-------|-------|
| Type          | JC12  | JC21   | JC22   | JG2   | JG3   |
| AMD           | 1 & 2 | Open   | Open   | 1 & 2 | 1 & 2 |
| AM486DXL      | 2 & 3 | Open   | Open   | 2 & 3 | 2 & 3 |
| CX486DX2      | 1 & 2 | Closed | Closed | 1 & 2 | 1 & 2 |
| CX486S        | 1 & 2 | Closed | Closed | 1 & 2 | 1 & 2 |
| Cyrix         | 1 & 2 | Open   | Closed | 1 & 2 | 1 & 2 |
| Intel         | 1 & 2 | Open   | Open   | 2 & 3 | 2 & 3 |
| Intel - S     | 1 & 2 | Open   | Open   | 2 & 3 | 2 & 3 |
| P24C          | 1 & 2 | Open   | Open   | 1 & 2 | 1 & 2 |
| P24T          | 1 & 2 | Closed | Open   | 2 & 3 | 2 & 3 |

Note: Pins designated should be in the closed position.

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| CPU SELECTION (CON'T) |       |       |        |        |       |
|-----------------------|-------|-------|--------|--------|-------|
| Type                  | JG4   | JG5   | JG16   | JG19   | JG20  |
| AMD                   | 1 & 2 | 1 & 2 | Open   | Open   | Open  |
| AM486DXL              | 2 & 3 | 2 & 3 | Open   | Open   | Open  |
| CX486DX2              | 1 & 2 | 1 & 2 | Open   | Closed | 1 & 2 |
| CX486S                | 1 & 2 | 1 & 2 | Open   | Closed | 1 & 2 |
| Cyrix                 | 1 & 2 | 1 & 2 | Open   | Closed | 1 & 2 |
| Intel                 | 2 & 3 | 2 & 3 | Open   | Open   | Open  |
| Intel - S             | 2 & 3 | 2 & 3 | Open   | Open   | 2 & 3 |
| P24C                  | 1 & 2 | Open  | Closed | Open   | Open  |
| P24T                  | 2 & 3 | 2 & 3 | Open   | Open   | Open  |

Note: Pins designated should be in the closed position.

| VL BUS WAIT STATE SELECTION |        |
|-----------------------------|--------|
| Setting                     | J2     |
| 0                           | Open   |
| 1                           | Closed |

| VL BUS SPEED SELECTION |        |
|------------------------|--------|
| Speed                  | J1     |
| <= 33MHz               | Open   |
| >33 MHz                | Closed |