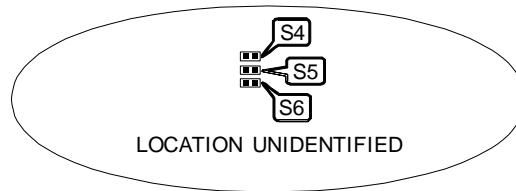
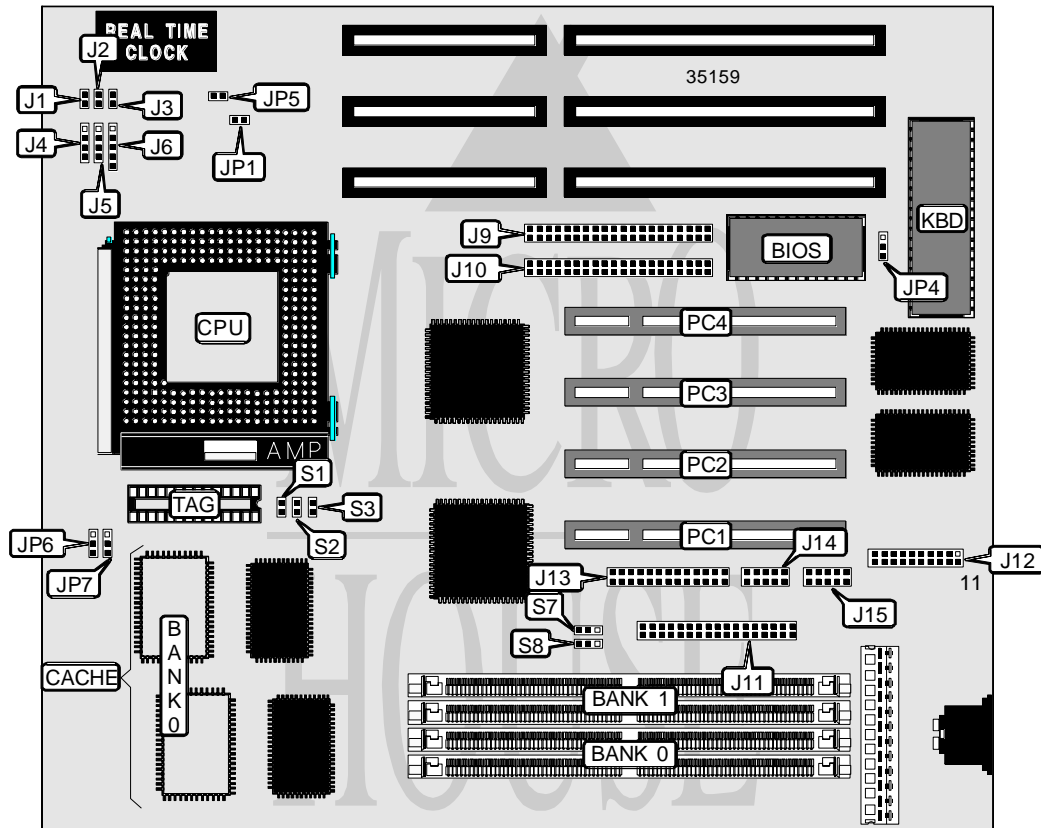


DTK COMPUTER INC. PAM-00561 (VER. 3.01)

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	AMI/Award
Dimensions	230mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, USB connectors (2)
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J1	USB connector 1	J12 pins 1 - 5
Green PC connector	J2	PS/2 mouse interface	J12 pins 6 - 10
Green PC LED	J3	USB connector 2	J12 pins 11 - 15
Speaker	J4	IR connector	J12 pins 16 - 20
IDE interface LED	J5	Parallel port	J13
Power LED & keylock	J6	Serial port 2	J14
IDE interface 1	J9	Serial port 1	J15
IDE interface 2	J10	32-bit PCI slots	PC1 - PC4
Floppy drive interface	J11		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Flash BIOS voltage select 12v	JP4	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP4	Pins 2 & 3 closed
í CMOS memory normal operation	JP5	Open
CMOS memory clear	JP5	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(2) 512K x 32	None
8MB	(2) 1M x 32	None
8MB	(2) 512K x 32	(2) 512K x 32
12MB	(2) 1M x 32	(2) 512K x 32
16MB	(2) 2M x 32	None
16MB	(2) 1M x 32	(2) 1M x 32
20MB	(2) 2M x 32	(2) 512K x 32
24MB	(2) 2M x 32	(2) 1M x 32
32MB	(2) 4M x 32	None
32MB	(2) 2M x 32	(2) 2M x 32
36MB	(2) 4M x 32	(2) 512K x 32
40MB	(2) 1M x 32	(2) 4M x 32
48MB	(2) 4M x 32	(2) 2M x 32
64MB	(2) 8M x 32	None
64MB	(2) 4M x 32	(2) 4M x 32
68MB	(2) 512K x 32	(2) 8M x 32
72MB	(2) 8M x 32	(2) 1M x 32
80MB	(2) 2M x 32	(2) 8M x 32
96MB	(2) 8M x 32	(2) 4M x 32
128MB	(2) 8M x 32	(2) 8M x 32

Note: Board accepts EDO memory. Banks are interchangeable.

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DRAM VOLTAGE CONFIGURATION		
Voltage	S7	S8
3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed
5v	Pins 1 & 2 closed	Pins 1 & 2 closed

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(2) 32K x 32	(1) 16K/32K x 8
512KB	(2) 64K x 32	(1) 16K/32K x 8

CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	JP6	JP7	S1	S2	S3
120MHz	50MHz	2x	1 & 2	2 & 3	Closed	Closed	Open
133MHz	55MHz	2x	1 & 2	2 & 3	Open	Open	Open
150MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open	Open
166MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed	Open
200MHz	66MHz	2x	1 & 2	2 & 3	Open	Open	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP6	JP7	S1	S2	S3
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open
120MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open
150MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	Open	Open

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JP6	JP7	S1	S2	S3
75MHz	50MHz	1.5x	1 & 2	1 & 2	Closed	Closed	Open
90MHz	60MHz	1.5x	1 & 2	1 & 2	Closed	Open	Open
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	Closed	Open
120MHz	60MHz	2x	1 & 2	2 & 3	Closed	Open	Open
133MHz	66MHz	2x	1 & 2	2 & 3	Open	Closed	Open
150MHz	60MHz	2.5x	2 & 3	2 & 3	Closed	Open	Open
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	Closed	Open
200MHz	66MHz	3x	2 & 3	1 & 2	Open	Closed	Open

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION					
Voltage	V core	JP1	S4	S5	S6
3.4v (single)	N/A	Closed	Closed	Closed	Closed
3.4v (dual)	2.8v	Closed	Open	Open	Open
3.5v (single)	N/A	Open	Closed	Closed	Closed

