M TECHNOLOGY, INC.
R 5 3 4 G   M U S T A N G

Processor	CX M1/IBM/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	SIS
Video Chip Set	None
Maximum Onboard Memory	384MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	280mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), USB connectors (2), IR connector
NPU Options	None
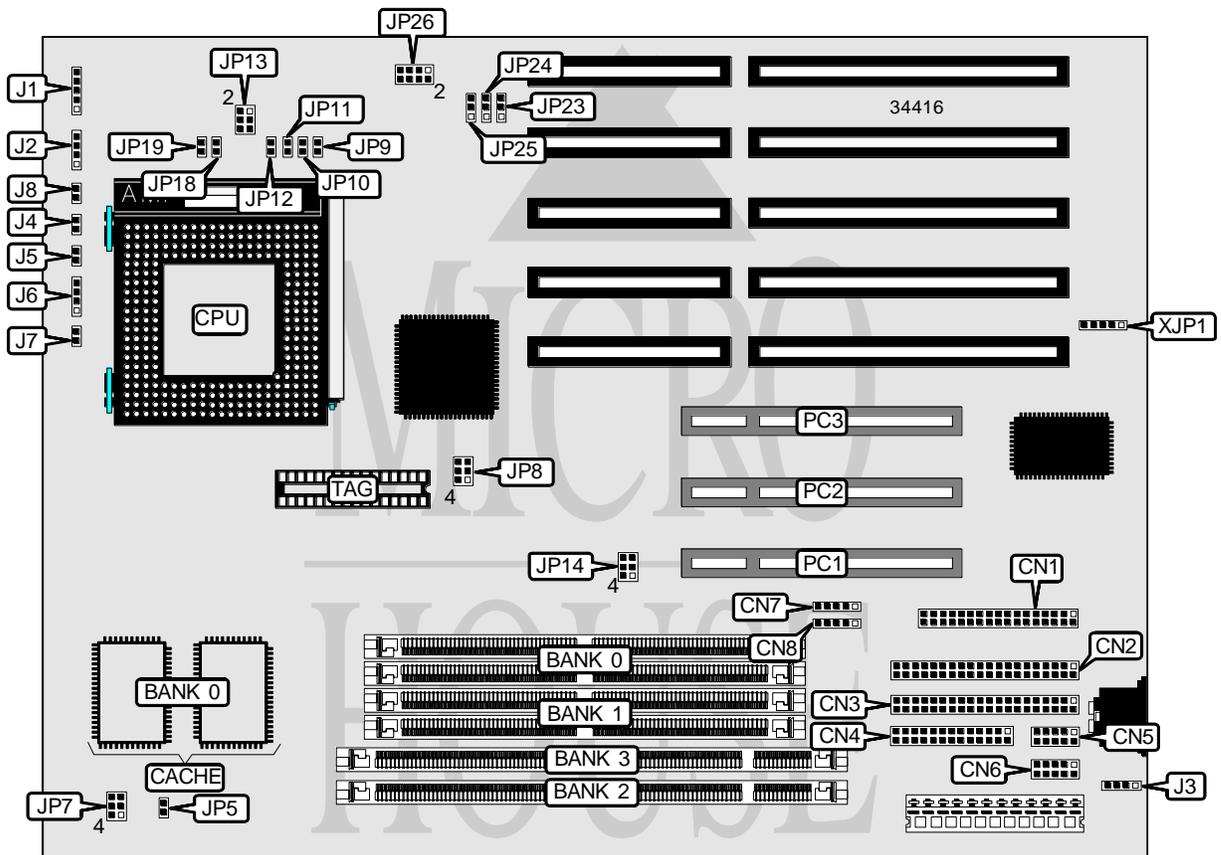
M TECHNOLOGY, INC.
R 5 3 4 G   M U S T A N G

CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Floppy drive interface	CN1	Speaker	J2
IDE interface 2	CN2	PS/2 mouse interface	J3
IDE interface 1	CN3	Turbo LED	J4
Parallel port	CN4	Reset switch	J5
Serial port 2	CN5	IDE interface LED	J6
Serial port 1	CN6	Green PC LED	J7
USB connector 2	CN7	Turbo switch	J8
USB connector 1	CN8	32-bit PCI slots	PC1 – PC3
Power LED & keylock	J1	IR connector	XJP1

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
Cyrix & IBM linear mode enabled	JP5	Closed
P54C & P55C togger mode enabled	JP5	Open
í CMOS memory normal operation	JP25	Pins 1 & 2 closed
CMOS memory clear	JP25	Pins 2 & 3 closed

DIMM/DRAM CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
8MB	(2) 1M x 36	None	None	None
16MB	(2) 2M x 36	None	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None	None
16MB	None	None	(1) 2M x 64	None
24MB	(2) 2M x 36	(2) 1M x 36	None	None
24MB	(2) 1M x 36	None	(1) 2M x 64	None
32MB	(2) 4M x 36	None	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None	None
32MB	None	None	(1) 2M x 64	(1) 2M x 64
32MB	None	None	(1) 4M x 64	None
32MB	(2) 2M x 36	None	(1) 2M x 64	None
40MB	(2) 4M x 36	(2) 1M x 36	None	None
40MB	(2) 1M x 36	None	(1) 4M x 64	None
48MB	(2) 4M x 36	(2) 2M x 36	None	None
48MB	None	None	(1) 2M x 64	(1) 4M x 64
48MB	(2) 4M x 36	None	(1) 2M x 64	None
48MB	(2) 2M x 36	None	(1) 4M x 64	None
64MB	(2) 8M x 36	None	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None	None
64MB	None	None	(1) 4M x 64	(1) 4M x 64
64MB	(2) 4M x 36	None	(1) 4M x 64	None
72MB	(2) 8M x 36	(2) 1M x 36	None	None

M TECHNOLOGY, INC.
R 5 3 4 G   M U S T A N G

DIMM/DRAM CONFIGURATION (CON'T)				
Size	**Bank 0**	**Bank 1**	**Bank 2**	**Bank 3**
80MB	(2) 8M x 36	(2) 2M x 36	None	None
80MB	(2) 8M x 36	None	(1) 2M x 64	None
96MB	(2) 8M x 36	(2) 4M x 36	None	None
96MB	(2) 8M x 36	None	(1) 4M x 64	None
128MB	(2) 16M x 36	None	None	None
128MB	(2) 8M x 36	(2) 8M x 36	None	None
136MB	(2) 16M x 36	(2) 1M x 36	None	None
144MB	(2) 16M x 36	(2) 2M x 36	None	None
160MB	(2) 16M x 36	(2) 4M x 36	None	None
192MB	(2) 16M x 36	(2) 8M x 36	None	None
256MB	(2) 16M x 36	(2) 16M x 36	None	None
256MB	(2) 32M x 36	None	None	None
264MB	(2) 32M x 36	(2) 1M x 36	None	None
272MB	(2) 32M x 36	(2) 2M x 36	None	None
288MB	(2) 32M x 36	(2) 4M x 36	None	None
320MB	(2) 32M x 36	(2) 8M x 36	None	None
384MB	(2) 32M x 36	(2) 16M x 36	None	None
Note: Board accepts EDO memory. Banks 0 & 1 are interchangeable.				

DRAM JUMPER CONFIGURATION				
DRAM type	**Bank 0**	**Bank 1**	**Bank 2**	**JP14**
í FP or EDO	SIMMs 3 & 4 or DIMM 2	SIMMs 1 & 2 or DIMM 1	None	1 & 3, 2 & 4
í SDRAM	DIMM 2	DIMM 1	None	1 & 3, 2 & 4
FP or EDO	SIMMs 3 & 4 or DIMM 2	DIMM 1	SIMMs 1 & 2	3 & 5, 4 & 6
Note: Bank 0 = SIMMs 3 & 4. Bank 1 = SIMMs 1 & 2. DIMM 1 = Bank 2. DIMM 2 = Bank 3. Pins designated should be in the closed position.				

DRAM VOLTAGE CONFIGURATION		
Voltage	**JP7**	**JP8**
3.3v	Pins 2 & 3, 5 & 6 closed	Pins 2 & 3, 5 & 6 closed
í 5v	Pins 1 & 2, 4 & 5 closed	Pins 1 & 2, 4 & 5 closed

CACHE CONFIGURATION		
Size	**Bank 0**	**TAG**
256KB	(2) 32K x 32	(1) 32K x 8
512KB	(2) 64K x 32	(1) 32K x 8

M TECHNOLOGY, INC.
R534G MUSTANG

CPU SPEED SELECTION (CYRIX)								
CPU speed	**Clock speed**	**Multiplier**	**JP9**	**JP10**	**JP11**	**JP12**	**JP18**	**JP19**
120MHz	50MHz	2x	Open	Open	Open	Closed	Closed	Open
133MHz	55MHz	2x	Closed	Open	Open	Closed	Closed	Open
150MHz	60MHz	2x	Open	Closed	Open	Closed	Closed	Open
166MHz	66MHz	2x	Open	Open	Closed	Closed	Closed	Open
200MHz	75MHz	2x	Closed	Closed	Open	Open	Closed	Open

CPU SPEED SELECTION (IBM)								
CPU speed	**Clock speed**	**Multiplier**	**JP9**	**JP10**	**JP11**	**JP12**	**JP18**	**JP19**
120MHz	50MHz	2x	Open	Open	Open	Closed	Closed	Open
133MHz	55MHz	2x	Closed	Open	Open	Closed	Closed	Open
150MHz	60MHz	2x	Open	Closed	Open	Closed	Closed	Open
166MHz	66MHz	2x	Open	Open	Closed	Closed	Closed	Open
200MHz	75MHz	2x	Closed	Closed	Open	Open	Closed	Open

CPU SPEED SELECTION (AMD)								
CPU speed	**Clock speed**	**Multiplier**	**JP9**	**JP10**	**JP11**	**JP12**	**JP18**	**JP19**
75MHz	50MHz	1.5x	Open	Open	Open	Closed	Open	Open
90MHz	60MHz	1.5x	Open	Closed	Open	Closed	Open	Open
100MHz	66MHz	1.5x	Open	Open	Closed	Closed	Open	Open

CPU SPEED SELECTION (INTEL)								
CPU speed	**Clock speed**	**Multiplier**	**JP9**	**JP10**	**JP11**	**JP12**	**JP18**	**JP19**
75MHz	50MHz	1.5x	Open	Open	Open	Closed	Open	Open
90MHz	60MHz	1.5x	Open	Closed	Open	Closed	Open	Open
100MHz	66MHz	1.5x	Open	Open	Closed	Closed	Open	Open
120MHz	60MHz	2x	Open	Closed	Open	Closed	Closed	Open
133MHz	66MHz	2x	Open	Open	Closed	Closed	Closed	Open
150MHz	60MHz	2.5x	Open	Closed	Open	Closed	Closed	Closed
166MHz	66MHz	2.5x	Open	Open	Closed	Closed	Closed	Closed
180MHz	60MHz	3x	Open	Closed	Open	Closed	Open	Closed
200MHz	66MHz	3x	Open	Open	Closed	Closed	Open	Closed

CPU VOLTAGE SELECTION (SINGLE)		
Voltage	**JP13**	**JP26**
3.4v	Pins 1 & 2, 3 & 4, 5 & 6 closed	Pins 3 & 4 closed
3.5v	Pins 1 & 2, 3 & 4, 5 & 6 closed	Pins 1 & 2 closed

CPU VOLTAGE SELECTION (DUAL)			
Voltage	**V core**	**JP13**	**JP26**
3.4v	2.5v	Open	Pins 7 & 8 closed
3.4v	2.8v	Open	Pins 5 & 6 closed
3.5v	2.5v	Open	Pins 7 & 8 closed
3.5v	2.8v	Open	Pins 5 & 6 closed

M TECHNOLOGY, INC.
R 5 3 4 G M U S T A N G

FLASH BIOS SELECTION		
Setting	**JP23**	**JP24**
5v	Pins 1 & 2 closed	Pins 2 & 3 closed
12v	Pins 2 & 3 closed	Pins 2 & 3 closed
None	Pins 1 & 2 closed	Pins 1 & 2 closed