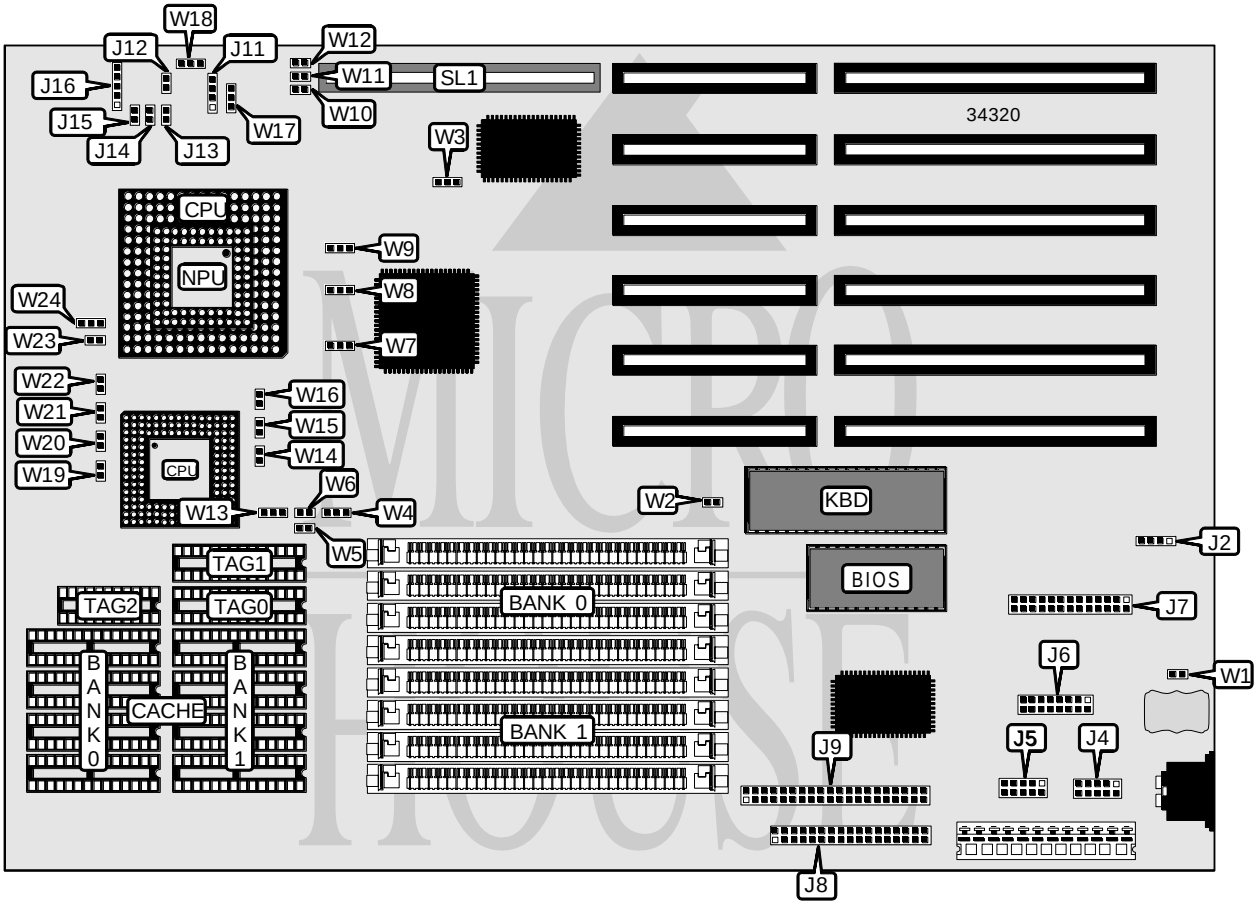
COMPUDYNE
UTD 4700 ALL IN ONE

Processor	80386DX/CX486DLC/80486SX/80486DX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	32MB
Maximum Video Memory	None
Cache	64/128/256KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slot, floppy drive interface, game interface, IDE interface, parallel port, serial ports (2)
NPU Options	80387DX



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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
External battery	J2	Speaker	J11
Serial port 2	J4	Reset switch	J12
Serial port 1	J5	IDE interface LED	J13
Game interface	J6	Turbo switch	J14
Parallel port	J7	Turbo LED	J15
Floppy drive interface	J8	Power LED & keylock	J16
IDE interface	J9	32-bit VESA local bus slot	SL1

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
Battery type select internal	W1	Closed
Battery type select external	W1	Open
Monitor type select color	W2	Closed
Monitor type select monochrome	W2	Open
í Factory configured - do not alter	W8	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	**Bank 0**	**Bank 1**
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	None
256KB	(4) 32K x 8	(4) 32K x 8

CACHE TAG CONFIGURATION			
Size	**TAG 0**	**TAG 1**	**TAG 2**
64KB	None	(1) 8K x 8	(1) 64K x 1
128KB	None	(1) 8K x 8	(1) 64K x 1
256KB	(1) 8K x 8	(1) 8K x 8	(1) 64K x 1

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CACHE JUMPER CONFIGURATION				
Size	**W4**	**W5**	**W6**	**W13**
64KB	Pins 2 & 3 closed	Open	Open	Pins 2 & 3 closed
128KB	Pins 1 & 2 closed	Open	Closed	Pins 1 & 2 closed
256KB	Pins 2 & 3 closed	Closed	Closed	Pins 2 & 3 closed

CACHE JUMPER CONFIGURATION (CON'T)				
Size	**W19**	**W20**	**W21**	**W22**
64KB	Open	Open	Open	Open
128KB	Open	Open	Open	Closed
256KB	Open	Open	Closed	Closed

CPU SPEED SELECTION			
Speed	**W14**	**W15**	**W16**
20MHz	Closed	Open	Open
25MHz	Open	Closed	Closed
33MHz	Open	Closed	Open
40MHz	Open	Open	Closed
50iMHz	Open	Closed	Closed
50MHz	Open	Closed	Closed
66iMHz	Open	Closed	Open

CPU TYPE SELECTION							
Type	**W3**	**W7**	**W9**	**W17**	**W18**	**W23**	**W24**
80386DX	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3	Open	Open
CX486DLC	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3	Open	Open
80486SX	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	Open	1 & 2
80486DX	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	Open	2 & 3
80486DX-50	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	Closed	2 & 3
80486DX2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	Open	2 & 3
Note: Pins designated should be in the closed position.							

VL BUS WAIT STATE SELECTION	
Setting	**W11**
0	Open
í 1	Closed

VL BUS SPEED SELECTION		
Speed	**W10**	**W12**
<= 33MHz	Open	Closed
40MHz	Closed	Open
50MHz	Open	Open