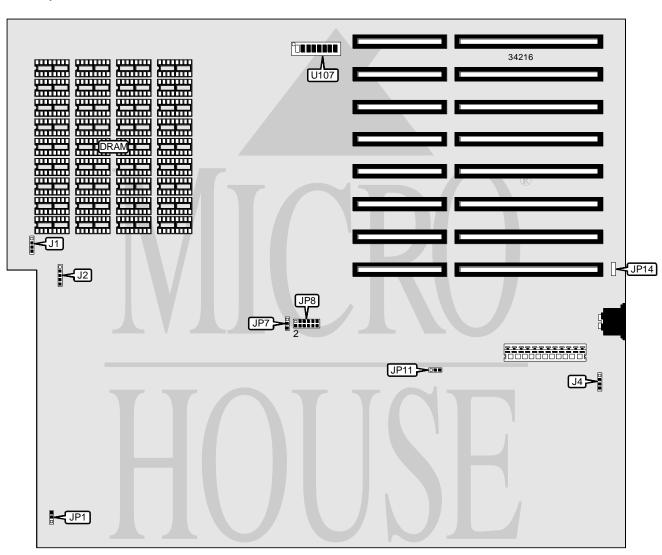
NBI 4200

Processor 80286 **Processor Speed** 6/10MHz Unidentified **Chip Set Video Chip Set** None **Maximum Onboard Memory** 1MB **Maximum Video Memory** None Cache None **BIOS** Award

Dimensions 355mm x 304mm

I/O Options None NPU Options 80287



CONNECTIONS				
Purpose	Location	Purpose	Location	
Speaker	J1	External battery	J4	
Power LED & keylock	J2			

USER CONFIGURABLE SETTINGS				
Function	Label	Position		
í Factory configured - do not alter	JP1	Unidentified		
í Factory configured - do not alter	JP14	Unidentified		
í Factory configured - do not alter	U107/3	Unidentified		
Monitor type select color	U107/4	On		
Monitor type select monochrome	U107/4	Off		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
256KB	(9) 41256	None	None	None
512KB	(9) 41256	(9) 41256	None	None
640KB	(9) 41256	(9) 41256	(9) 4164	(9) 4164
768KB	(9) 41256	(9) 41256	(9) 41256	None
1MB	(9) 41256	(9) 41256	(9) 41256	(9) 41256
Note: The location o	of the banks are unidenti	fied.	. , ,	

DRAM JUMPER CONFIGURATION			
Size	JP7	JP8	
256KB	Pins 2 & 3 closed	Pins 1 & 2, 7 & 8 closed	
512KB	Pins 1 & 2 closed	Pins 1 & 2, 7 & 8 closed	
640KB	Pins 1 & 2 closed	Pins 3 & 4, 9 & 10 closed	
768KB	Pins 2 & 3 closed	Pins 5 & 6, 11 & 12 closed	
1MB	Pins 1 & 2 closed	Pins 5 & 6, 11 & 12 closed	

CPU SPEED SELECTION			
Speed	JP11		
6MHz	Pins 2 & 3 closed		
10MHz	Pins 1 & 2 closed		

BIOS SELECTION						
Setting	U107/1	U107/2	U107/5	U107/6	U107/7	U107/8
27128	Off	On	On	On	Off	Off
27256	On	Off	Off	Off	On	On