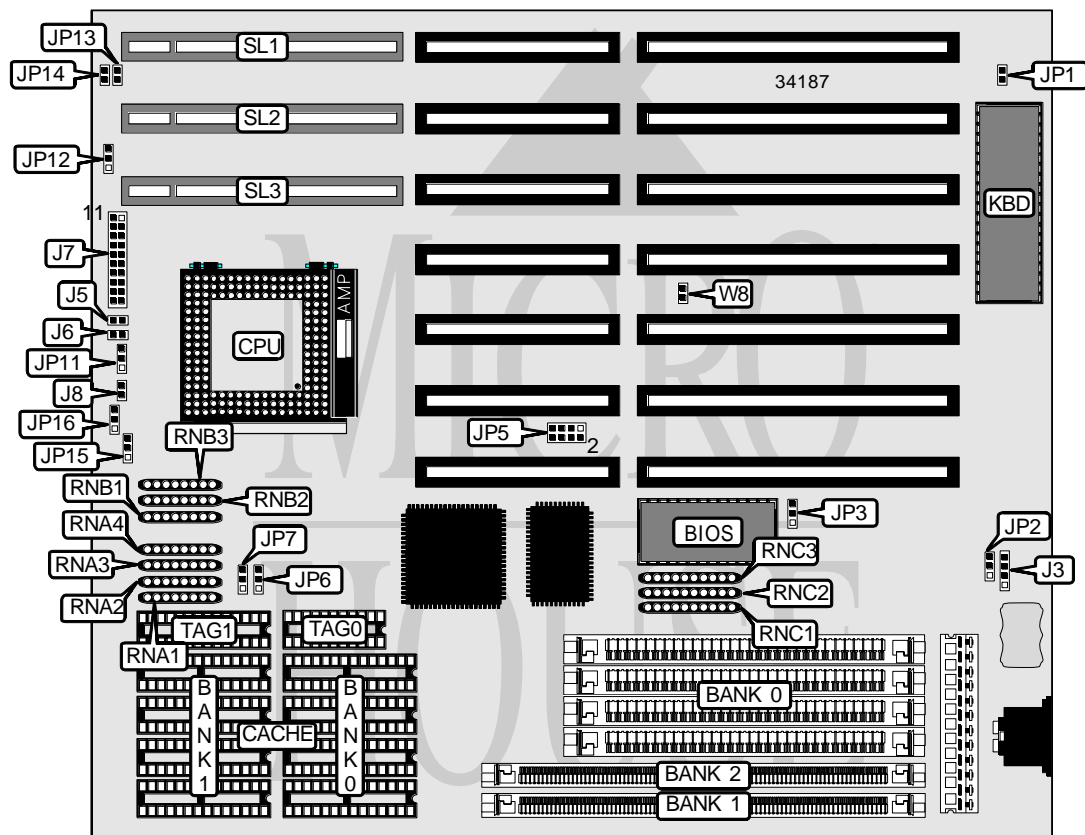


TMC RESEARCH CORPORATION

PAT48PG4 (VER. 1.2A)

Processor	80486SX/SL80486SX/ODP486/CX486DX/80486DX/SL80486DX/ 80486DX2/SL80486DX2/AM486DX4/(SL)AM486DX4/80486DX4/ SL80486DX4/P24T/P24CT/CX5X86
Processor Speed	25/33/40/50(internal)/50/63(internal)/66(internal)/80(internal)/ 83(internal)/100(internal)/120(internal)MHz
Chip Set	OPTI
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	128/256KB
BIOS	Unidentified
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



Continued on next page . . .

TMC RESEARCH CORPORATION
 PAT48PG4 (VER. 1.2A)

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Reset switch	J7 pins 9 & 19
Green PC connector	J5	IDE interface LED	J7 pins 10 & 20
IDE interface LED	J6	Power LED & keylock	J7 pins 11 - 15
Speaker	J7 pins 1 - 4	Green PC LED	J8
Turbo switch	J7 pins 7 & 17	32-bit VESA local bus slots	SL1 - SL3
Turbo LED	J7 pins 8 & 18		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
Battery type select internal	JP2	Pins 2 & 3 closed
Battery type select external	JP2	Open
CMOS memory clear	JP2	Pins 1 & 2 closed
BIOS type select EPROM	JP3	Pins 2 & 3 closed
BIOS type select flash BIOS	JP3	Pins 1 & 2 closed
í Factory configured - do not alter	JP11	Unidentified

DRAM CONFIGURATION 1			
Size	Bank 0	Bank 1	Bank 2
2MB	None	(1) 256K x 36	(1) 256K x 36
4MB	None	(1) 1M x 36	None
5MB	None	(1) 256K x 36	(1) 1M x 36
6MB	(4) 1M x 9	(1) 256K x 36	(1) 256K x 36
8MB	None	(1) 1M x 36	(1) 1M x 36
8MB	(4) 1M x 9	(1) 1M x 36	None
16MB	None	(1) 4M x 36	None
17MB	None	(1) 256K x 36	(1) 4M x 36
20MB	None	(1) 1M x 36	(1) 4M x 36
20MB	(4) 4M x 9	(1) 1M x 36	None
32MB	None	(1) 4M x 36	(1) 4M x 36
32MB	(4) 4M x 9	(1) 4M x 36	None
64MB	None	(1) 16M x 36	None
128MB	None	(1) 16M x 36	(1) 16M x 36
128MB	(4) 16M x 9	(1) 16M x 36	None

Continued on next page...

TMC RESEARCH CORPORATION
 PAT48PG4 (VER. 1.2A)

... continued from previous page

DRAM CONFIGURATION 2			
Size	Bank 0	Bank 1	Bank 2
2MB	(4) 256K x 9	None	(1) 256K x 36
4MB	(4) 1M x 9	None	None
5MB	(4) 256K x 9	None	(1) 1M x 36
6MB	(4) 256K x 9	(1) 1M x 36	(1) 256K x 36
8MB	(4) 1M x 9	None	(1) 1M x 36
8MB	(4) 1M x 9	(1) 1M x 36	None
16MB	(4) 4M x 9	None	None
17MB	(4) 256K x 9	None	(1) 4M x 36
20MB	(4) 1M x 9	None	(1) 4M x 36
20MB	(4) 1M x 9	(1) 4M x 36	None
32MB	(4) 4M x 9	None	(1) 4M x 36
32MB	(4) 4M x 9	(1) 4M x 36	None
64MB	(4) 16M x 9	None	None
128MB	(4) 16M x 9	None	(1) 16M x 36
128MB	(4) 16M x 9	(1) 16M x 36	None

DRAM CONFIGURATION 3			
Size	Bank 0	Bank 1	Bank 2
2MB	None	(1) 512K x 36	None
4MB	None	(1) 1M x 36	None
4MB	None	(1) 512K x 36	(1) 512K x 36
6MB	None	(1) 512K x 36	(1) 1M x 36
6MB	(4) 1M x 9	None	(1) 512K x 36
8MB	None	(1) 2M x 36	None
8MB	None	(1) 1M x 36	(1) 1M x 36
8MB	(4) 1M x 9	None	(1) 1M x 36
10MB	None	(1) 512K x 36	(1) 2M x 36
12MB	None	(1) 1M x 36	(1) 2M x 36
16MB	None	(1) 4M x 36	None
16MB	None	(1) 2M x 36	(1) 2M x 36
20MB	None	(1) 1M x 36	(1) 4M x 36
20MB	(4) 4M x 9	None	(1) 1M x 36
32MB	None	(1) 8M x 36	None
32MB	None	(1) 4M x 36	(1) 4M x 36
64MB	None	(1) 16M x 36	None
64MB	None	(1) 8M x 36	(1) 8M x 36
128MB	None	(1) 32M x 36	None
128MB	None	(1) 16M x 36	(1) 16M x 36
128MB	(4) 16M x 9	None	(1) 16M x 36

Continued on next page...

TMC RESEARCH CORPORATION
 PAT48PG4 (VER. 1.2A)

... continued from previous page

DRAM JUMPER CONFIGURATION			
Configuration	RNC1	RNC2	RNC3
1	Installed	Not installed	Not installed
2	Not installed	Installed	Not installed
3	Not installed	Not installed	Installed

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG0	TAG1
128KB	(4) 32K x 8	None	None	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	None	(1) 16K/32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP6	JP7
128KB	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU SPEED SELECTION				
Speed	JP5	JP12	JP13	JP14
25MHz	1 & 2, 5 & 6	1 & 2	Open	Open
33MHz	1 & 2, 3 & 4	1 & 2	Open	Open
40MHz	3 & 4, 5 & 6, 7 & 8	2 & 3	Open	Closed
50iMHz	1 & 2, 5 & 6	1 & 2	Open	Open
50MHz	1 & 2, 5 & 6, 7 & 8	2 & 3	Closed	Closed
63iMHz	1 & 2, 5 & 6	1 & 2	Open	Open
66iMHz	1 & 2, 3 & 4	1 & 2	Open	Open
80iMHz	3 & 4, 5 & 6, 7 & 8	2 & 3	Open	Closed
83iMHz	1 & 2, 3 & 4	1 & 2	Open	Open
100iMHz	1 & 2, 3 & 4	1 & 2	Open	Open
120iMHz	3 & 4, 5 & 6, 7 & 8	2 & 3	Open	Closed

Note: Pins designated should be in the closed position.

Continued on next page. . .

TMC RESEARCH CORPORATION
 PAT48PG4 (VER. 1.2A)

... continued from previous page

CPU TYPE SELECTION				
Type	RNA1	RNA2	RNA3	RNA4
80486SX	Not installed	Not installed	Installed	Not installed
Intel S-series	Not installed	Not installed	Not installed	Not installed
ODP486	Not installed	Installed	Not installed	Not installed
CX486DX	Not installed	Not installed	Not installed	Installed
80486DX	Installed	Not installed	Not installed	Not installed
80486DX2	Installed	Not installed	Not installed	Not installed
AM486DX4	Installed	Not installed	Not installed	Not installed
AMD enhanced	Not installed	Not installed	Not installed	Not installed
80486DX4	Installed	Not installed	Not installed	Not installed
P24T	Not installed	Installed	Not installed	Not installed
P24CT	Not installed	Installed	Not installed	Not installed
CX 5X86	Installed	Not installed	Not installed	Not installed

CPU TYPE SELECTION (CON'T)				
Type	RNB1	RNB2	RNB3	W8
80486SX	Installed	Not installed	Not installed	Closed
Intel S-series	Not installed	Installed	Not installed	N/A
ODP486	Installed	Not installed	Not installed	Open
CX486DX	Not installed	Not installed	Installed	Closed
80486DX	Installed	Not installed	Not installed	Closed
80486DX2	Installed	Not installed	Not installed	Closed
AM486DX4	Not installed	Installed	Not installed	Open
AMD enhanced	Not installed	Installed	Not installed	N/A
80486DX4	Installed	Not installed	Not installed	Open
P24T	Installed	Not installed	Not installed	Open
P24CT	Installed	Not installed	Not installed	Open
CX 5X86	Not installed	Installed	Not installed	Open

CPU TYPE SELECTION (WRITE BACK)		
Type	JP15	JP16
AMD write back	Pins 1 & 2 closed	Pins 2 & 3 closed
Intel write back	Pins 1 & 2 closed	Pins 2 & 3 closed
Other CPU types	Open	Open