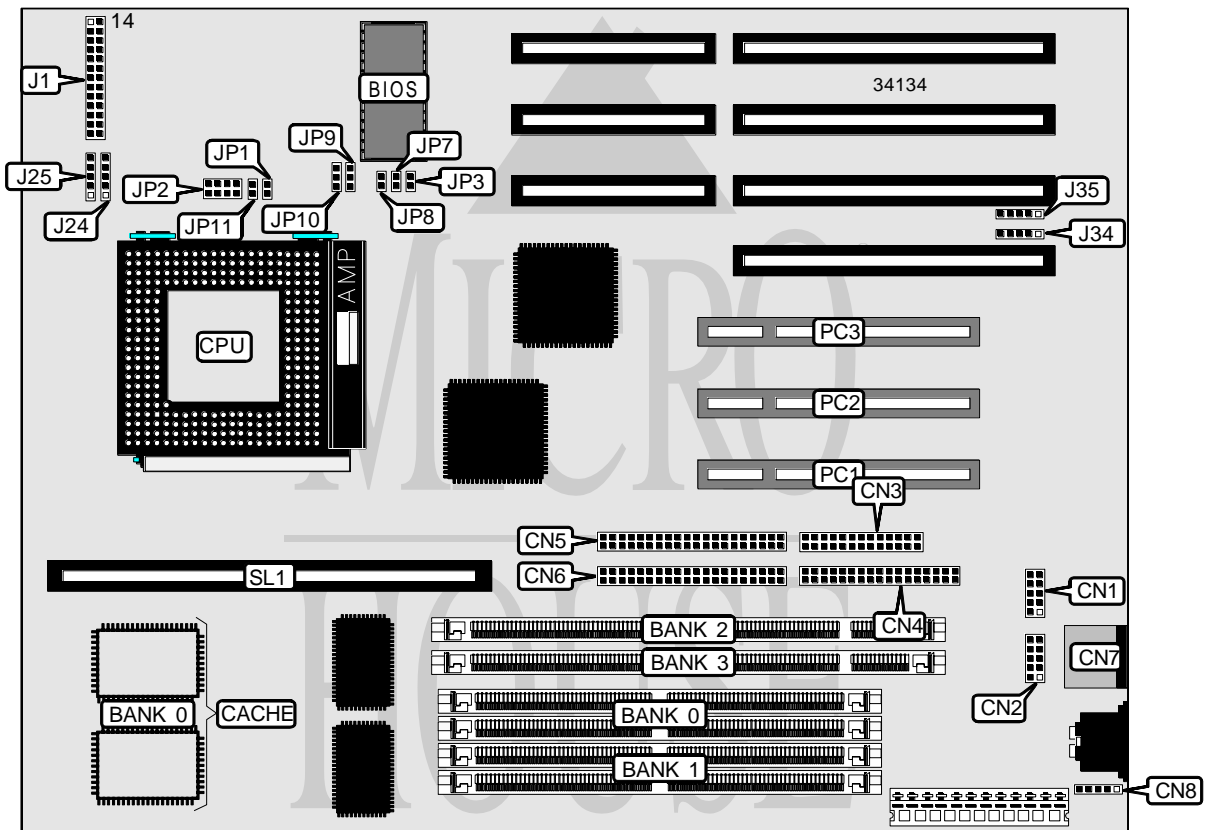


# BCM ADVANCED RESEARCH, INC. SQ593

<b>Processor</b>	CX M1/Pentium
<b>Processor Speed</b>	75/90/100/120/133/150/166/180/200MHz
<b>Chip Set</b>	Intel
<b>Video Chip Set</b>	None
<b>Maximum Onboard Memory</b>	256MB (EDO supported)
<b>Maximum Video Memory</b>	None
<b>Cache</b>	256/512KB
<b>BIOS</b>	Award
<b>Dimensions</b>	254mm x 218mm
<b>I/O Options</b>	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 mouse interface, serial ports (2), cache slot, IR connectors (2), USB connectors (2)
<b>NPU Options</b>	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Speaker	J1 pins 10 - 13
Serial port 2	CN2	IDE interface LED	J1 pins 14 - 17
Parallel port	CN3	Green PC LED	J1 pins 19 & 20
Floppy drive interface	CN4	Power LED & keylock	J1 pins 22 - 26
IDE interface 2	CN5	Fast IR connector	J24
IDE interface 1	CN6	IR connector	J25
PS/2 mouse port	CN7	USB connector 2	J34
PS/2 mouse interface	CN8	USB connector 1	J35
Reset switch	J1 pins 1 & 2	32-bit PCI slots	PC1 – PC3
Turbo LED	J1 pins 4 & 5	Cache slot	SL1
Green PC connector	J1 pins 7 & 8		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP3	Open
CMOS memory clear	JP3	Closed

DIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory. Board also accepts x 32 SIMMs.

CACHE CONFIGURATION		
Size	Bank 0	SL1
256KB	(2) 32K x 32	Not installed
512KB	(2) 32K x 32	256KB module installed

CPU SPEED SELECTION (CYRIX)						
CPU speed	Clock speed	Multiplier	JP7	JP8	JP9	JP10
120MHz	50MHz	2x	Closed	Closed	2 & 3	1 & 2
133MHz	55MHz	2x	Open	Open	2 & 3	1 & 2
150MHz	60MHz	2x	Open	Closed	2 & 3	1 & 2
166MHz	66MHz	2x	Closed	Open	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	JP7	JP8	JP9	JP10
75MHz	50MHz	1.5x	Closed	Closed	1 & 2	1 & 2
90MHz	60MHz	1.5x	Open	Closed	1 & 2	1 & 2
100MHz	66MHz	1.5x	Closed	Open	1 & 2	1 & 2
120MHz	60MHz	2x	Open	Closed	2 & 3	1 & 2
133MHz	66MHz	2x	Closed	Open	2 & 3	1 & 2
150MHz	60MHz	2.5x	Open	Closed	2 & 3	2 & 3
166MHz	66MHz	2.5x	Closed	Open	2 & 3	2 & 3
180MHz	60MHz	3x	Open	Closed	1 & 2	2 & 3
200MHz	66MHz	3x	Closed	Open	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION (CYRIX)			
Voltage	JP1	JP2	JP11
2.5v	Closed	Open	Closed
2.8v	Closed	Open	Open
3.15v – 3.6v	Closed	1 & 2, 3 & 4, 5 & 6, 7 & 8	Open
3.4v – 3.6v	Open	1 & 2, 3 & 4, 5 & 6, 7 & 8	Open

Note: Pins designated should be in the closed position.

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<b>CPU VOLTAGE SELECTION (INTEL)</b>			
<b>Voltage</b>	<b>JP1</b>	<b>JP2</b>	<b>JP11</b>
2.5v	Closed	Open	Closed
2.8v	Closed	Open	Open
3.135v – 3.6v	Closed	1 & 2, 3 & 4, 5 & 6, 7 & 8	Open
3.4v – 3.6v	Open	1 & 2, 3 & 4, 5 & 6, 7 & 8	Open

Note: Pins designated should be in the closed position.