

A-TREND TECHNOLOGY CORPORATION

ATC-1000+

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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Green PC connector	J1 pins 4 & 5
Serial port 2	CN2	Turbo LED	J1 pins 7 & 8
Parallel port	CN3	IDE interface LED	J1 pins 10 & 11
Floppy drive interface	CN4	Speaker	J1 pins 12 - 15
IDE interface 1	CN5	Power LED & keylock	J1 pins 17 - 21
IDE interface 2	CN6	Chassis fan power	J2
PS/2 mouse port	CN7	32-bit PCI slots	PC1 – PC3
IR connector	IR	USB connector	USB
Reset switch	J1 pins 1 & 2		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
? CMOS memory normal operation	JP1	Open
CMOS memory clear	JP1	Closed
? Flash BIOS write protect disabled	JP2	Pins 2 & 3 closed
Flash BIOS write protect enabled	JP2	Pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(2) 512K x 36	None
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory. Banks are interchangeable.

CACHE CONFIGURATION		
Size	Bank 0	TAG
256KB	(2) 32K x 32	(1) 16K/32K x 8
512KB	(2) 64K x 32	(1) 16K/32K x 8

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CPU SPEED SELECTION (CYRIX)						
CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9
120MHz	60MHz	2x	Open	Open	1 & 2	2 & 3
150MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3
166MHz	66MHz	2x	Open	Closed	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AMD)						
CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9
75MHz	50MHz	1.5x	Open	Open	1 & 2	1 & 2
90MHz	60MHz	1.5x	Closed	Open	1 & 2	1 & 2
100MHz	66MHz	1.5x	Open	Closed	1 & 2	1 & 2
120MHz	60MHz	1.5x	Closed	Open	1 & 2	1 & 2
133MHz	66MHz	1.5x	Open	Closed	1 & 2	1 & 2
150MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9
75MHz	50MHz	1.5x	Open	Open	1 & 2	1 & 2
90MHz	60MHz	1.5x	Closed	Open	1 & 2	1 & 2
100MHz	66MHz	1.5x	Open	Closed	1 & 2	1 & 2
120MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3
133MHz	66MHz	2x	Open	Closed	1 & 2	2 & 3
150MHz	60MHz	2.5x	Closed	Open	2 & 3	2 & 3
166MHz	66MHz	2.5x	Open	Closed	2 & 3	2 & 3
180MHz	60MHz	3x	Closed	Open	2 & 3	1 & 2
200MHz	66MHz	3x	Open	Closed	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION	
Type	JP5
Cyrix	Pins 1 & 2 closed
AMD	Pins 1 & 2 closed
P54C STD	Pins 3 & 4 closed
P54C VRE	Pins 1 & 2 closed
P55C	Pins 7 & 8 closed

CPU VOLTAGE SELECTION	
Setting	JP6
Single	Pins 1 & 2 closed
Dual	Pins 2 & 3 closed