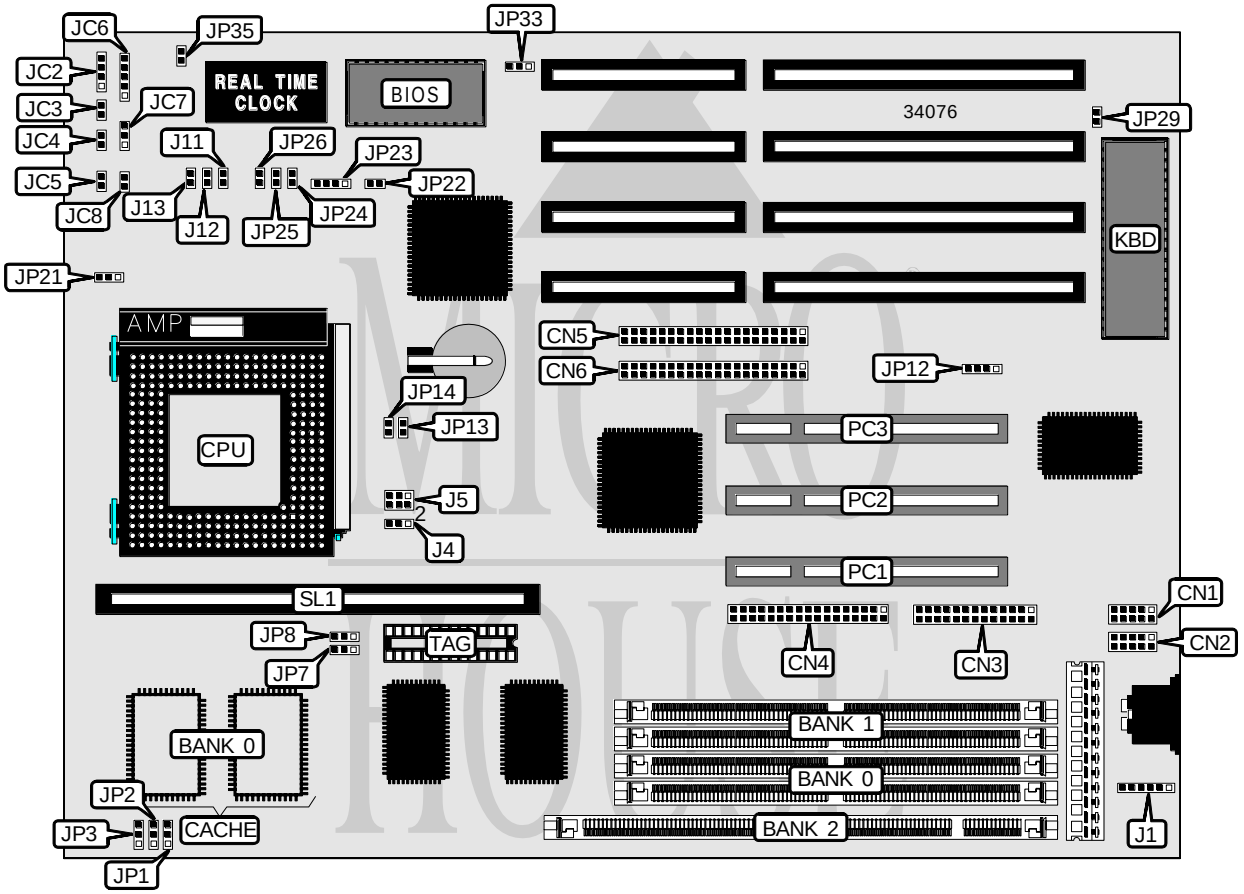
J-MARK COMPUTER CORPORATION
J - 6 5 6 V X B

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	270mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), cache slot, IR connector
NPU Options	None



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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Serial port 1	CN1	IDE interface LED	JC5
Serial port 2	CN2	Power LED & keylock	JC6
Parallel port	CN3	Turbo switch	JC7
Floppy drive interface	CN4	Green PC connector	JC8
IDE interface 2	CN5	IR connector	JP12
IDE interface 1	CN6	Chassis fan power	JP21
PS/2 mouse interface	J1	External battery	JP23
Speaker	JC2	32-bit PCI slots	PC1 – PC3
Reset switch	JC3	Cache slot	SL1
Turbo LED	JC4		

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í Factory configured - do not alter	J4	Unidentified
í Factory configured - do not alter	JP3	Unidentified
PCICLK select PCICLK/4	JP22	Closed
PCICLK select PCICLK/3	JP22	Open
PS/2 IRQ12 enabled	JP29	Closed
PS/2 IRQ12 disabled	JP29	Open
Flash BIOS voltage select 12v	JP33	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP33	Pins 2 & 3 closed

DIMM/DRAM CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**Bank 2**
8MB	(2) 1M x 36	None	None
8MB	None	None	(1) 1M x 64
16MB	(2) 2M x 36	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None
16MB	None	None	(1) 2M x 64
24MB	(2) 2M x 36	(2) 1M x 36	None
32MB	(2) 4M x 36	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None
32MB	None	None	(1) 4M x 64
40MB	(2) 4M x 36	(2) 1M x 36	None
48MB	(2) 4M x 36	(2) 2M x 36	None

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DIMM/DRAM CONFIGURATION (CON'T)			
Size	**Bank 0**	**Bank 1**	**Bank 2**
64MB	(2) 8M x 36	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None
72MB	(2) 8M x 36	(2) 1M x 36	None
80MB	(2) 8M x 36	(2) 2M x 36	None
96MB	(2) 8M x 36	(2) 4M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36	None
Note: Board accepts EDO memory.			

CACHE CONFIGURATION			
Size	**Bank 0**	**SL1**	**TAG**
256KB (A)	(2) 32K x 32	Not installed	(1) 32K x 8
256KB (B)	None	256KB module installed	(1) 32K x 8
512KB (A)	(2) 32K x 32	256KB module installed	(1) 32K x 8
512KB (B)	None	512KB module installed	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	**JP1**	**JP2**	**JP7**	**JP8**
256KB (A)	1 & 2	1 & 2	1 & 2	1 & 2
256KB (B)	1 & 2	1 & 2	1 & 2	1 & 2
512KB (A, Jet-Way module)	2 & 3	1 & 2	2 & 3	1 & 2
512KB (A, Intel module)	1 & 2	2 & 3	1 & 2	2 & 3
512KB (B, Jet-Way module)	2 & 3	1 & 2	2 & 3	1 & 2
512KB (B, Intel module)	1 & 2	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.				

CPU SPEED SELECTION (CYRIX)					
CPU speed	**Clock speed**	**Multiplier**	**J5**	**JP13**	**JP14**
120MHz	50MHz	2x	3 & 4, 5 & 6	Closed	Open
133MHz	55MHz	2x	3 & 4	Closed	Open
150MHz	60MHz	2x	5 & 6	Closed	Open
166MHz	66MHz	2x	Open	Closed	Open
Note: Pins designated should be in the closed position.					

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CPU SPEED SELECTION (AMD)					
CPU speed	**Clock speed**	**Multiplier**	**J5**	**JP13**	**JP14**
90MHz	60MHz	1.5x	5 & 6	Open	Open
100MHz	66MHz	1.5x	Open	Open	Open
Note: Pins designated should be in the closed position.					

CPU SPEED SELECTION (INTEL)					
CPU speed	**Clock speed**	**Multiplier**	**J5**	**JP13**	**JP14**
75MHz	50MHz	1.5x	3 & 4, 5 & 6	Open	Open
90MHz	60MHz	1.5x	5 & 6	Open	Open
100MHz	66MHz	1.5x	Open	Open	Open
120MHz	60MHz	2x	5 & 6	Closed	Open
133MHz	66MHz	2x	Open	Closed	Open
150MHz	60MHz	2.5x	5 & 6	Closed	Closed
166MHz	66MHz	2.5x	Open	Closed	Closed
200MHz	66MHz	3x	Open	Open	Closed
Note: Pins designated should be in the closed position.					

CPU VOLTAGE SELECTION (SINGLE)			
Voltage	**JP24**	**JP25**	**JP26**
3.3v	Closed	Open	Open
3.45v	Open	Closed	Open
3.52v	Open	Open	Closed

CPU VOLTAGE SELECTION (DUAL)							
Voltage	**V core**	**J11**	**J12**	**J13**	**JP24**	**JP25**	**JP26**
3.3v	2.5v	Closed	Open	Open	Closed	Open	Open
3.3v	2.8v	Open	Closed	Open	Open	Closed	Open
3.3v	3.45v	Open	Open	Closed	Open	Open	Closed
3.45v	2.5v	Closed	Open	Open	Closed	Open	Open
3.45v	2.8v	Open	Closed	Open	Open	Closed	Open
3.45v	3.45v	Open	Open	Closed	Open	Open	Closed
3.52v	2.5v	Closed	Open	Open	Closed	Open	Open
3.52v	2.8v	Open	Closed	Open	Open	Closed	Open
3.52v	3.45v	Open	Open	Closed	Open	Open	Closed

CMOS SELECTION		
Setting	**JP23**	**JP35**
í CMOS memory normal operation	Pins 2 & 3 closed	Open
CMOS memory clear	Pins 3 & 4 closed	Closed