## QDI COMPUTER, INC. P51430HX-T2 FRONTIER

CX M1/AM K5/Pentium **Processor** 

**Processor Speed** 75/90/100/120/133/150/166/180/200MHz

**Chip Set Video Chip Set** None

**Maximum Onboard Memory** 128MB (EDO supported)

**Maximum Video Memory** None Cache 256/512KB **BIOS** Award

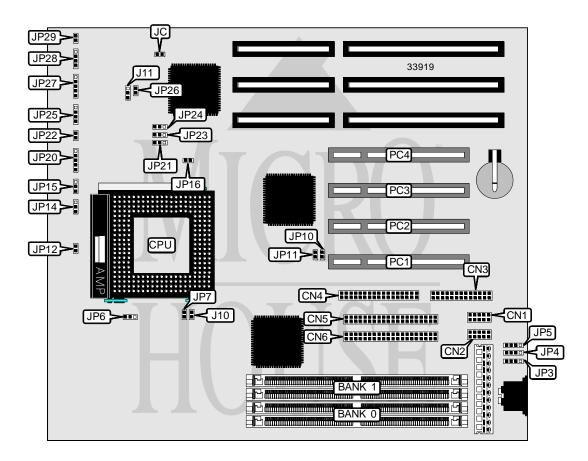
**Dimensions** 250mm x 220mm

32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces I/O Options

(2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, USB

connectors (2)

**NPU Options** None



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CONNECTIONS						
Purpose	Location	Purpose	Location			
Serial port 2	CN1	Chassis fan power	JP12			
Serial port 1	CN2	IR connector	JP20			
Parallel port	CN3	Green PC connector	JP22			
Floppy drive interface	CN4	IDE interface LED	JP25			
IDE interface 2	CN5	Power LED & keylock	JP27			
IDE interface 1	CN6	Speaker	JP28			
PS/2 mouse interface	JP3	Reset switch	JP29			
USB connector 1	JP4	32-bit PCI slots	PC1 - PC4			
USB connector 2	JP5					

USER CONFIGURABLE SETTINGS						
Function	Label	Position				
í CMOS memory normal operation	JC	Open				
CMOS memory clear	JC	Closed				
í Factory configured - do not alter	JP23	Pins 2 & 3 closed				
í Factory configured - do not alter	JP24	Pins 1 & 2 closed				

DRAM CONFIGURATION						
Size	Bank 0	Bank 1				
8MB	(2) 1M x 36	None				
16MB	(2) 2M x 36	None				
16MB	(2) 1M x 36	(2) 1M x 36				
24MB	(2) 2M x 36	(2) 1M x 36				
32MB	(2) 4M x 36	None				
32MB	(2) 2M x 36	(2) 2M x 36				
40MB	(2) 4M x 36	(2) 1M x 36				
48MB	(2) 4M x 36	(2) 2M x 36				
64MB	(2) 8M x 36	None				
64MB	(2) 4M x 36	(2) 4M x 36				
72MB	(2) 8M x 36	(2) 1M x 36				
80MB	(2) 8M x 36	(2) 2M x 36				
96MB	(2) 8M x 36	(2) 4M x 36				
128MB	(2) 8M x 36	(2) 8M x 36				
128MB	(2) 16M x 36	None				
lote: Board accepts EDO memory.	Board also accepts x 32 SIMMs. Banks a	re interchangeable.				

## CACHE CONFIGURATION

Note: Board accepts 256KB/512KB cache. The size of the chips and location are unidentified.

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	CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	JP6	JP10	JP11	JP14	JP15	JP16
120MHz	50MHz	2x	Open	Closed	Closed	2 & 3	1 & 2	Open
133MHz	55MHz	2x	Open	Open	Open	2 & 3	1 & 2	Open
150MHz	60MHz	2x	1 & 2	Closed	Open	2 & 3	1 & 2	Closed
166MHz 66MHz 2x 2 & 3 Open Closed 2 & 3 1 & 2 Closed								
Note: Pins des	Note: Pins designated should be in the closed position.							

	CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP6	JP10	JP11	JP14	JP15	JP16
75MHz	50MHz	1.5x	Open	Closed	Closed	2 & 3	2 & 3	Open
90MHz	55MHz	1.5x	Open	Open	Open	2 & 3	2 & 3	Open
90MHz	60MHz	1.5x	1 & 2	Closed	Open	2 & 3	2 & 3	Closed
100MHz 66MHz 1.5x 2 & 3 Open Closed 2 & 3 2 & 3 Closed								
Note: Pins des	Note: Pins designated should be in the closed position.							

	CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JP6	JP10	JP11	JP14	JP15	JP16
75MHz	50MHz	1.5x	Open	Closed	Closed	2 & 3	2 & 3	Open
90MHz	60MHz	1.5x	1 & 2	Closed	Open	2 & 3	2 & 3	Closed
100MHz	66MHz	1.5x	2 & 3	Open	Closed	2 & 3	2 & 3	Closed
120MHz	60MHz	2x	1 & 2	Closed	Open	2 & 3	1 & 2	Closed
133MHz	66MHz	2x	2 & 3	Open	Closed	2 & 3	1 & 2	Closed
150MHz	60MHz	2.5x	1 & 2	Closed	Open	1 & 2	1 & 2	Closed
166MHz	66MHz	2.5x	2 & 3	Open	Closed	1 & 2	1 & 2	Closed
180MHz	60MHz	3x	1 & 2	Closed	Open	1 & 2	2 & 3	Closed
200MHz	66MHz	3x	2 & 3	Open	Closed	1 & 2	2 & 3	Closed
Note: Pins des	signated should b	e in the closed p	osition.					

	CPU VOLTAGE SELECTION (SINGLE)						
Voltage J10 J11 JP7 JP26							
3.3v	Closed	Pins 2 & 3 closed	Closed	Open			
í 3.5v	Closed	Pins 2 & 3 closed	Closed	Closed			

CPU VOLTAGE SELECTION (DUAL)							
Voltage	V core	J10	J11	JP7	JP21	JP26	
3.3v	2.5v	Open	1 & 2	Open	Open	Open	
3.3v	2.7v	Open	1 & 2	Open	1 & 2	Open	
3.3v	2.9v	Open	1 & 2	Open	2 & 3	Open	
í 3.5v	2.5v	Open	1 & 2	Open	Open	Closed	
í 3.5v	2.7v	Open	1 & 2	Open	1 & 2	Closed	
í 3.5v	2.9v	Open	1 & 2	Open	2 & 3	Closed	
Note: Pins desig	nated should be	e in the closed po	sition.				