CX M1/AM K5/Pentium **Processor**

Processor Speed 75/90/100/120/133/150/166/180/200MHz

Chip Set **Video Chip Set** None

Maximum Onboard Memory 128MB (EDO supported)

Maximum Video Memory None Cache 256/512KB **BIOS** Award

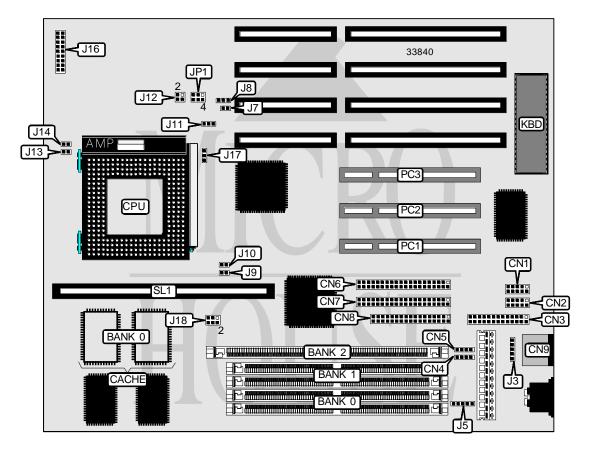
Dimensions 260mm x 220mm

I/O Options 32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces

(2), parallel port, PS/2 mouse port, PS/2 mouse interface, serial ports (2), cache

slot, IR connector, USB connectors (2)

NPU Options None



Continued on next page. . .

. . . continued from previous page

CONNECTIONS					
Purpose	Location	Purpose	Location		
Serial port 2	CN1	IR connector	J5		
Serial port 1	CN2	Power LED & keylock	J16 pins 1 - 5		
Parallel port	CN3	IDE interface LED	J16 pins 6 & 13		
USB connector	CN4	Green PC connector	J16 pins 7 & 12		
USB connector	CN5	Reset switch	J16 pins 8 & 11		
IDE interface 2	CN6	Turbo LED	J16 pins 9 & 10		
IDE interface 1	CN7	Speaker	J16 pins 15 - 18		
Floppy drive interface	CN8	32-bit PCI slots	PC1 - PC3		
PS/2 mouse port	CN9	Cache slot	SL1		
PS/2 mouse interface	J3				

USER CONFIGURABLE SETTINGS					
Function Label Position					
í CMOS memory normal operation	J7	Open			
CMOS memory clear	J7	Closed			
Flash BIOS voltage select 12v	18	Pins 1 & 2 closed			
Flash BIOS voltage select 5v	18	Pins 2 & 3 closed			
í Factory configured - do not alter	J17	Unidentified			

DRAM/DIMM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2			
8MB	(2) 1M x 36	None	None			
16MB	(2) 2M x 36	None	None			
16MB	(2) 1M x 36	(2) 1M x 36	None			
16MB	None	(2) 1M x 36	(1) 1M x 64			
24MB	None	(2) 1M x 36	(1) 2M x 64			
24MB	None	(2) 2M x 36	(1) 1M x 64			
24MB	(2) 1M x 36	(2) 2M x 36	None			
32MB	None	(2) 2M x 36	(1) 2M x 64			
32MB	(2) 4M x 36	None	None			
32MB	(2) 2M x 36	(2) 2M x 36	None			
40MB	None	(2) 1M x 36	(1) 4M x 64			
40MB	None	(2) 4M x 36	(1) 1M x 64			
40MB	(2) 1M x 36	2) 1M x 36 (2) 4M x 36				
48MB	None	(2) 4M x 36	(1) 2M x 64			
48MB	None	(2) 2M x 36	(1) 4M x 64			
48MB	(2) 4M x 36	(2) 2M x 36	None			
64MB	None	(2) 4M x 36	(1) 4M x 64			
64MB	(2) 8M x 36	None	None			
64MB	(2) 4M x 36	(2) 4M x 36	None			
72MB	None	(2) 1M x 36	(1) 8M x 64			
72MB	None	(2) 8M x 36	(1) 1M x 64			

Continued on next page. . .

 $\dots continued \ from \ previous \ page$

DRAM/DIMM CONFIGURATION (CON'T)					
Size	Bank 0	Bank 1	Bank 2		
72MB	(2) 1M x 36	(2) 8M x 36	None		
80MB	None	(2) 8M x 36	(1) 2M x 64		
80MB	None	(2) 2M x 36	(1) 8M x 64		
80MB	(2) 8M x 36	(2) 2M x 36	None		
96MB	None	(2) 8M x 36	(1) 4M x 64		
96MB	None	(2) 4M x 36	(1) 8M x 64		
96MB	(2) 4M x 36	(2) 8M x 36	None		
128MB	None	(2) 8M x 36	(1) 8M x 64		
128MB	(2) 8M x 36	(2) 8M x 36	None		
Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. Banks 0 & 1 are interchangeable.					

DIMM VOLTAGE CONFIGURATION				
Size J18				
3.3v	Pins 1 & 3, 2 & 4 closed			
5v	Pins 3 & 5, 4 & 6 closed			

CACHE CONFIGURATION					
Size	Size Bank 0 SL1				
256KB	(2) 32K x 32	Not installed	(1) 8K x 8		
256KB	None	256KB module installed	None		
512KB	(2) 32K x 32	256KB module installed	(1) 16K x 8		
512KB	(2) 64K x 32	Not installed	(1) 16K x 8		
512KB None 512KB module installed None					
Note: The location of the TAG is unidentified.					

CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	19	J10	J11	J13	J14
100MHz	66MHz	2x	Open	Closed	2 & 3	Open	Open
110MHz	55MHz	2x	Open	Open	2 & 3	Closed	Open
120MHz	60MHz	2x	Closed	Open	1 & 2	Closed	Open
133MHz	66MHz	2x	Open	Closed	2 & 3	Closed	Open
Note: Pins designated should be in the closed position.							

		CPl	J SPEED SELEC	CTION (AMD)			
CPU speed	Clock speed	Multiplier	19	J10	J11	J13	J14
75MHz	50MHz	1.5x	Closed	Closed	2 & 3	Open	Open
90MHz	60MHz	1.5x	Closed	Open	1 & 2	Open	Open
Note: Pins designated should be in the closed position.							

Continued on next page. . .

. . . continued from previous page

		CPL	J SPEED SELEC	TION (INTEL)			
CPU speed	Clock speed	Multiplier	19	J10	J11	J13	J14
75MHz	50MHz	1.5x	Closed	Closed	2 & 3	Open	Open
90MHz	60MHz	1.5x	Closed	Open	1 & 2	Open	Open
100MHz	66MHz	1.5x	Open	Closed	2 & 3	Open	Open
120MHz	60MHz	2x	Closed	Open	1 & 2	Closed	Open
133MHz	66MHz	2x	Open	Closed	2 & 3	Closed	Open
150MHz	50MHz	3	Closed	Closed	2 & 3	Open	Closed
166MHz	66MHz	2.5x	Open	Closed	2 & 3	Closed	Closed
180MHz	60MHz	3x	Closed	Open	1 & 2	Open	Closed
200MHz	66MHz	3x	Open	Closed	2 & 3	Open	Closed
Note: Pins des	Note: Pins designated should be in the closed position.						

	CPU VOLTAGE SELECTION	
Voltage	JP1	J12
2.5v	Pins 3 & 6 closed	Pins 2 & 4 closed
2.8v	Pins 2 & 5 closed	Pins 2 & 4 closed
3.3v	Pins 1 & 4 closed	Pins 1 & 3 closed