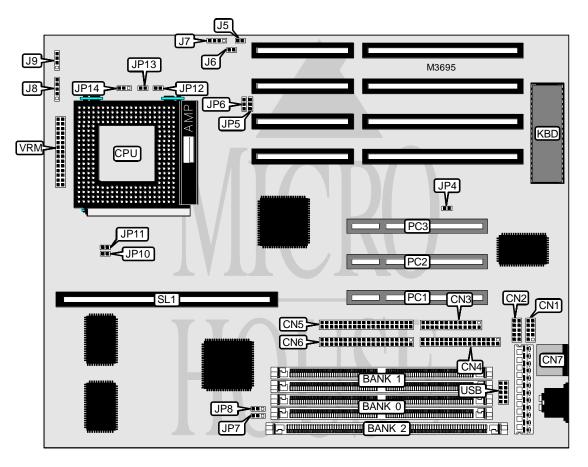
Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	1MB
Cache	256/512KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, PS/2 mouse port,
	IDE interfaces (2), parallel port, serial ports (2), cache slot, VRM connector, USB
	connector
NPU Options	None



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CONNECTIONS					
Purpose	Location	Purpose	Location		
Serial port 1	CN1	External battery	J7		
Serial port 2	CN2	Power LED & keylock	18		
Parallel port	CN3	Speaker	19		
Floppy drive interface	CN4	Green PC connector	JP6		
IDE interface 2	CN5	32-bit PCI slots	PC1 - PC3		
IDE interface 1	CN6	Cache slot	SL1		
PS/2 mouse port	CN7	USB connector	USB		
IDE interface LED	J5	VRM connector	VRM		
Reset switch	J6				

USER CONFIGURABLE SETTINGS					
Function	Label	Position			
Battery type select internal	J7	Pins 2 & 3 closed			
Battery type select external	J7	Closed			
CMOS memory clear	J7	Pins 3 & 4 closed			
í Flash BIOS voltage select 5v or EPROM	JP4	Open			
Flash BIOS voltage select 12v	JP4	Closed			
Green PC disabled	JP6	Open			
Green PC enabled	JP6	Pins 1 & 2 closed			
í Factory configured - do not alter	JP8	Unidentified			

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2			
8MB	(2) 1M x 36	None	None			
8MB	None	(2) 1M x 36	None			
8MB	None	None	(1) 8M x 1			
16MB	(2) 2M x 36	None	None			
16MB	None	(2) 2M x 36	None			
16MB	None	None	(1) 16M x 1			
16MB	(2) 1M x 36	(2) 1M x 36	None			
16MB	None	(2) 1M x 36	(1) 8M x 1			
24MB	(2) 1M x 36 (2) 2M x 36		None			
24MB	None	None (2) 1M x 36				
24MB	(2) 2M x 36	/1 x 36 (2) 1M x 36				
24MB	None	ne (2) 2M x 36 (1) 8				
32MB	(2) 4M x 36	None	None			
32MB	None	(2) 4M x 36	None			
32MB	None	None	(1) 32M x 1			
32MB	(2) 2M x 36	(2) 2M x 36	None			
32MB	None	(2) 2M x 36	(1) 16M x 1			
40MB	(2) 1M x 36	(2) 4M x 36	None			

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DRAM CONFIGURATION (CON'T)					
Size	Bank 0	Bank 1	Bank 2		
40MB	None	(2) 1M x 36	(1) 32M x 1		
40MB	(2) 4M x 36	(2) 1M x 36	None		
40MB	None	(2) 4M x 36	(1) 8M x 1		
48MB	(2) 2M x 36	(2) 4M x 36	None		
48MB	None	(2) 2M x 36	(1) 32M x 1		
48MB	(2) 4M x 36	(2) 2M x 36	None		
48MB	None	(2) 4M x 36	(1) 16M x 1		
64MB	(2) 8M x 36	None	None		
64MB	None	(2) 8M x 36	None		
64MB	None	None	(1) 64M x 1		
64MB	(2) 4M x 36	(2) 4M x 36	None		
64MB	None	(2) 4M x 36	(1) 32M x 1		
72MB	(2) 1M x 36	(2) 8M x 36	None		
72MB	None	(2) 1M x 36	(1) 64M x 1		
72MB	(2) 8M x 36	(2) 1M x 36	None		
72MB	None	(2) 8M x 36	(1) 8M x 1		
80MB	(2) 2M x 36	(2) 8M x 36	None		
80MB	None	(2) 2M x 36	(1) 64M x 1		
80MB	(2) 8M x 36	(2) 2M x 36	None		
80MB	None	(2) 8M x 36	(1) 16M x 1		
96MB	(2) 4M x 36	(2) 8M x 36	None		
96MB	None	(2) 4M x 36	(1) 64M x 1		
96MB	(2) 8M x 36	(2) 4M x 36	None		
96MB	None	(2) 8M x 36	(1) 32M x 1		
128MB	(2) 8M x 36	(2) 8M x 36	None		
128MB	None	(2) 8M x 36	(1) 64M x 1		
	None	. ,			

DIMM VOLTAGE CONFIGURATION				
Voltage JP7				
3.3v	Pins 2 & 3 closed			
í 5v	Pins 1 & 2 closed			

CACHE CONFIGURATION						
Size	SL1	TAG				
256KB	256KB module installed	(1) 8K/16K/32K x 8				
512KB 512KB module installed (1) 16K/32K x 8						
Note: The location of the TAG is unidentified.						

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CPU SPEED SELECTION (CYRIX)							
CPU speed	Clock speed	Multiplier	JP5	JP10	JP11	JP12	JP13
120MHz	60MHz	2x	1&2	Open	Closed	Closed	Open
133MHz	66MHz	2x	1&2	Closed	Open	Closed	Open
150MHz	60MHz	2x	1&2	Open	Closed	Closed	Open
166MHz	66MHz	2x	1&2	Closed	Open	Closed	Open
Note: Pins desig	Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (AMD)							
CPU speed	Clock speed	Multiplier	JP5	JP10	JP11	JP12	JP13
75MHz	50MHz	1.5x	2&3	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	1&2	Open	Closed	Open	Open
100MHz	66MHz	1.5x	1&2	Closed	Open	Open	Open
120MHz	60MHz	1.5x	1&2	Open	Closed	Closed	Open
133MHz	66MHz	1.5x	1&2	Closed	Open	Closed	Open
Note: Pins desig	Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION (INTEL)							
CPU speed	Clock speed	Multiplier	JP5	JP10	JP11	JP12	JP13
75MHz	50MHz	1.5x	2&3	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	1&2	Open	Closed	Open	Open
100MHz	66MHz	1.5x	1&2	Closed	Open	Open	Open
120MHz	60MHz	2x	1&2	Open	Closed	Closed	Open
133MHz	66MHz	2x	1&2	Closed	Open	Closed	Open
150MHz	60MHz	2.5x	1&2	Open	Closed	Closed	Closed
166MHz	66MHz	2.5x	1&2	Closed	Open	Closed	Closed
180MHz	60MHz	3x	1&2	Open	Closed	Open	Closed
200MHz	66MHz	Зx	1&2	Closed	Open	Open	Closed
Note: Pins desig	Note: Pins designated should be in the closed position.						

CPU VOLTAGE SELECTION				
Voltage JP14				
3.3v	Pins 2 & 3 closed			
3.5v	Pins 1 & 2 closed			