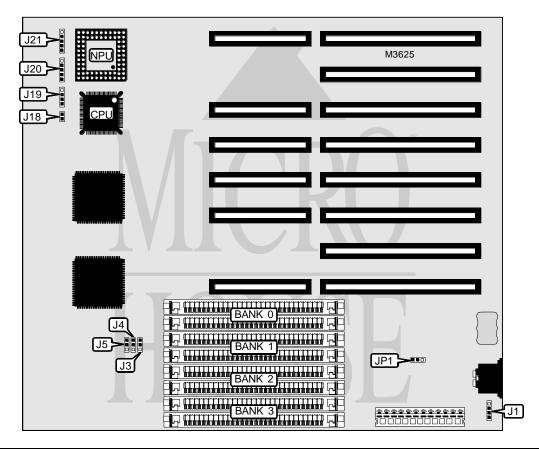
UNIDENTIFIED 404 386SX

Processor 80386SX **Processor Speed** 16/20/25MHz

Chip Set ALI **Video Chip Set** None **Maximum Onboard Memory** 8MB **Maximum Video Memory** None Cache None **BIOS** AMI

Dimensions 254mm x 218mm

I/O Options None **NPU Options** 80387SX



CONNECTIONS						
Purpose	Location	Purpose	Location			
External battery	J1	Power LED & keylock	J20			
Reset switch	J18	Turbo LED	J21 pins 1 & 2			
Speaker	J19	Turbo switch	J21 pins 4 & 5			

Continued on next page. . .

UNIDENTIFIED 404 386SX

. . . continued from previous page

USER CONFIGURABLE SETTINGS				
Function	Label	Position		
Fast A20 gate signal from chip set	J4	Pins 1 & 2 closed		
Fast A20 gate signal from keyboard controller	J4	Pins 2 & 3 closed		
Monitor type select color	JP1	Pins 2 & 3 closed		
Monitor type select monochrome	JP1	Pins 1 & 2 closed		

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	
512KB	(2) 256K x 9	None	None	None	
1MB	(2) 256K x 9	(2) 256K x 9	None	None	
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	None	
2MB	(2) 256K x 9				
2MB	(2) 1M x 9	None	None	None	
4MB	(2) 1M x 9	(2) 1M x 9	None	None	
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	None	
8MB	(2) 1M x 9				

CPU SPEED SELECTION				
Speed	J3	J5		
16MHz	Pins 1 & 2 closed	Pins 1 & 2 closed		
20MHz	Pins 1 & 2 closed	Pins 2 & 3 closed		
25MHz	Pins 2 & 3 closed	Pins 2 & 3 closed		