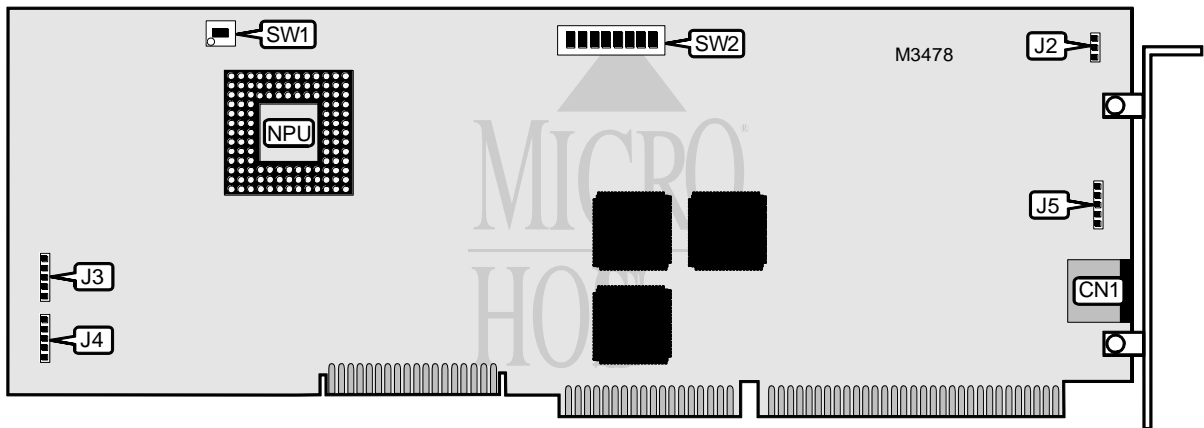


NCR CORPORATION MODEL 3386-0XXX PC 920

| | |
|-------------------------------|---------------------------------------|
| Processor | 80386 |
| Processor Speed | 20MHz |
| Chip Set | Unidentified |
| Video Chip Set | None |
| Maximum Onboard Memory | 2MB (on external memory card) |
| Maximum Video Memory | None |
| Cache | None |
| BIOS | Unidentified |
| Dimensions | Unidentified |
| I/O Options | External memory card, PS/2 mouse port |
| NPU Options | 80387/3167 |



| CONNECTIONS | | | |
|------------------|----------|------------------------------|----------|
| Purpose | Location | Purpose | Location |
| PS/2 mouse port | CN1 | Speaker | J4 |
| External battery | J2 | Auxiliary keyboard connector | J5 |
| Keylock | J3 | | |

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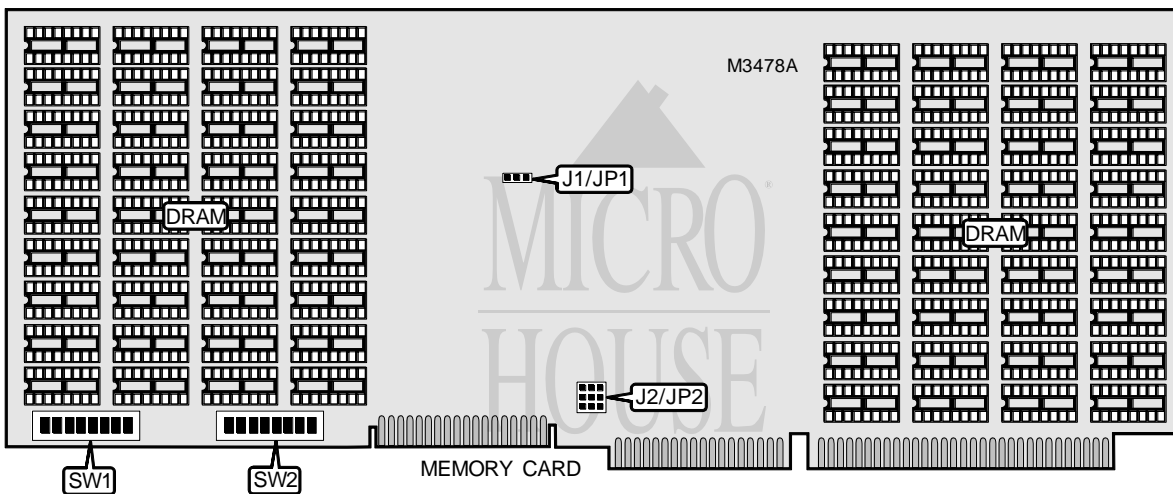
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| USER CONFIGURABLE SETTINGS | | |
|---|-------|----------|
| Function | Label | Position |
| í Monitor type select color | SW1 | On |
| Monitor type select monochrome | SW1 | Off |
| í NPU not installed (if Weitek NPU is installed, set switch to off) | SW2/1 | Off |
| NPU installed | SW2/1 | On |
| í PS/2 mouse disabled | SW2/2 | Off |
| PS/2 mouse enabled | SW2/2 | On |
| í Video ROM copied to RAM | SW2/3 | Off |
| Video ROM not copied to RAM | SW2/3 | On |
| í Non maskable interrupt generated from fail safe timer disabled | SW2/4 | Off |
| Non maskable interrupt generated from fail safe timer enabled | SW2/4 | On |
| í IRQ10 disabled | SW2/5 | Off |
| IRQ10 enabled | SW2/5 | On |
| í IRQ11 disabled | SW2/6 | Off |
| IRQ11 enabled | SW2/6 | On |
| í IRQ12 disabled | SW2/7 | Off |
| IRQ12 enabled | SW2/7 | On |

| CPU SPEED SELECTION | |
|---------------------------|-------|
| Speed | SW2/8 |
| 8MHz (auto speed select) | Off |
| 20MHz (high speed select) | On |



| USER CONFIGURABLE SETTINGS | | |
|---------------------------------|--------|-------------------|
| Function | Label | Position |
| Wait state select 0 wait states | J1/JP1 | Pins 1 & 2 closed |
| Wait state select 1 wait states | J1/JP1 | Pins 2 & 3 closed |

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| DRAM CONFIGURATION | | |
|---|-------------|-------------|
| Size | Bank 0 | Bank 1 |
| 1MB | (36) 411000 | None |
| 2MB | (36) 411000 | (36) 411000 |
| Note: The location of banks 0 & 1 are unidentified. | | |

| DRAM JUMPER CONFIGURATION | |
|---------------------------|---------------------------------|
| Size | J2/JP2 |
| 1MB | Pins 2 & 3, 5 & 6, 8 & 9 closed |
| 2MB | Pins 1 & 2, 4 & 5, 7 & 8 closed |

| DRAM SWITCH CONFIGURATION (ONE 2MB BOARD INSTALLED) | | | | | | | | |
|---|-------|-------|-------|-------|-------|-------|-------|-------|
| Setting | SW1/1 | SW1/2 | SW1/3 | SW1/4 | SW1/5 | SW1/6 | SW1/7 | SW1/8 |
| Set for first 2MB boundary | On | On | On | On | On | On | On | On |
| Setting | SW2/1 | SW2/2 | SW2/3 | SW2/4 | SW2/5 | SW2/6 | SW2/7 | SW2/8 |
| Locate 256KB at end of second MB of space. | On | Off | On | On | On | On | On | On |

| DRAM SWITCH CONFIGURATION (TWO 2MB BOARDS INSTALLED) | | | | | | | | |
|--|-------|-------|-------|-------|-------|-------|-------|-------|
| Setting | SW1/1 | SW1/2 | SW1/3 | SW1/4 | SW1/5 | SW1/6 | SW1/7 | SW1/8 |
| Set for first 2MB boundary (board 1) | On | On | On | On | On | On | On | On |
| Set for first 2MB boundary (board 2) | Off | On | On | On | On | On | On | Off |
| Setting | SW2/1 | SW2/2 | SW2/3 | SW2/4 | SW2/5 | SW2/6 | SW2/7 | SW2/8 |
| Locate 256KB at end of second MB of space. (board 1) | On | On | Off | On | On | On | On | On |
| Not the first MB in the system (board 2) | Off | Off | Off | Off | Off | Off | Off | Off |

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| DRAM SWITCH CONFIGURATION (THREE 2MB BOARDS INSTALLED) | | | | | | | | |
|--|-------|-------|-------|-------|-------|-------|-------|-------|
| Setting | SW1/1 | SW1/2 | SW1/3 | SW1/4 | SW1/5 | SW1/6 | SW1/7 | SW1/8 |
| Set for first 2MB boundary (board 1) | On | On | On | On | On | On | On | On |
| Set for first 2MB boundary (board 2) | Off | On | On | On | On | On | On | Off |
| Set for third 2MB boundary (board 3) | On | Off | On | On | On | On | On | Off |
| Setting | SW2/1 | SW2/2 | SW2/3 | SW2/4 | SW2/5 | SW2/6 | SW2/7 | SW2/8 |
| Locate 256KB at end of sixth MB of space. (board 1) | On | Off | Off | On | On | On | On | On |
| All switches off (board 2) | Off | Off | Off | Off | Off | Off | Off | Off |
| All switches off (board 3) | Off | Off | Off | Off | Off | Off | Off | Off |

| DRAM SWITCH CONFIGURATION (FOUR 2MB BOARDS INSTALLED) | | | | | | | | |
|---|-------|-------|-------|-------|-------|-------|-------|-------|
| Setting | SW1/1 | SW1/2 | SW1/3 | SW1/4 | SW1/5 | SW1/6 | SW1/7 | SW1/8 |
| Set for first 2MB boundary (board 1) | On | On | On | On | On | On | On | On |
| Set for first 2MB boundary (board 2) | Off | On | On | On | On | On | On | Off |
| Set for third 2MB boundary (board 3) | On | Off | On | On | On | On | On | Off |
| Set for third 2MB boundary (board 4) | Off | Off | On | On | On | On | On | Off |
| All switches off (board 4) | N/A | N/A | N/A | N/A | N/A | N/A | N/A | N/A |
| Setting | SW2/1 | SW2/2 | SW2/3 | SW2/4 | SW2/5 | SW2/6 | SW2/7 | SW2/8 |
| Locate 256KB at end of eighth MB of space. (board 1) | On | On | On | Off | On | On | On | On |
| All switches off (board 2) | Off | Off | Off | Off | Off | Off | Off | Off |
| All switches off (board 3) | Off | Off | Off | Off | Off | Off | Off | Off |
| All switches off (board 4) | Off | Off | Off | Off | Off | Off | Off | Off |